AM81214-030

## RF \& MICROWAVE TRANSISTORS L-BAND RADAR APPLICATIONS

- REFRACTORY/GOLD METALLIZATION
- EMITTER SITE BALLASTED
- RUGGEDIZED VSWR $\infty: 1$
- LOW THERMAL RESISTANCE
- INPUT/OUTPUT MATCHING
- OVERLAY GEOMETRY
- METAL/CERAMIC HERMETIC PACKAGE
- Pout $=26$ W MIN. WITH 7.2 dB GAIN


AM81214-030
81214-30

$.310 \times .310$ 2LFL (S064) hermetically sealed

BRANDING

## DESCRIPTION

The AM81214-030 device is a high power transistor specifically designed for L-Band Radar pulsed driver applications.
The device is capable of operation over a wide range of pulse widths, duty cycles and temperatures and is capable of withstanding $\infty: 1$ output VSWR at rated RF conditions. Low RF thermal resistance and computerized automatic wire bonding techniques ensure high reliability and product consistency.
The AM81214-030 is supplied in the IMPAC ${ }^{\text {TM }}$ Hermetic Metal/Ceramic package with internal Input/Output matching structures.


ABSOLUTE MAXIMUM RATINGS (Tcase $=25^{\circ} \mathrm{C}$ )

| Symbol | Parameter | Value | Unit |
| :---: | :---: | :---: | :---: |
| Pdiss | Power Dissipation* ( $\left.\mathrm{T}_{\mathrm{C}} \leq 100^{\circ} \mathrm{C}\right)$ | 63 | W |
| Ic | Device Current* | 2.75 | A |
| VCC | Collector-Supply Voltage* | 32 | V |
| TJ | Junction Temperature (Pulsed RF Operation) | 250 | ${ }^{\circ} \mathrm{C}$ |
| TSTG | Storage Temperature | -65 to +200 | ${ }^{\circ} \mathrm{C}$ |

## THERMAL DATA

| $R_{\text {TH }}^{(j-\mathrm{c})}$ | Junction-Case Thermal Resistance ${ }^{*}$ | 2.4 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| :---: | :--- | :---: | :---: |

[^0]ELECTRICAL SPECIFICATIONS $\left(T_{\text {case }}=25^{\circ} \mathrm{C}\right)$
STATIC

| Symbol | Test Conditions |  | Value |  |  | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | Min. | Typ. | Max. |  |
| BV'bo | $\mathrm{IC}=10 \mathrm{~mA}$ | $\mathrm{IE}=0 \mathrm{~mA}$ | 55 | - | - | V |
| BVEbo | $\mathrm{I}_{\mathrm{E}}=1 \mathrm{~mA}$ | $\mathrm{I}_{\mathrm{C}}=0 \mathrm{~mA}$ | 3.5 | - | - | V |
| BVCER | $\mathrm{IC}=20 \mathrm{~mA}$ | $\mathrm{R}_{\text {be }}=10 \Omega$ | 55 | - | - | V |
| Ices | $\mathrm{V}_{\text {be }}=0 \mathrm{~V}$ | $\mathrm{V}_{\mathrm{CE}}=28 \mathrm{~V}$ | - | - | 5 | mA |
| hFE | $\mathrm{V}_{\text {CE }}=5 \mathrm{~V}$ | $\mathrm{lc}=1 \mathrm{~A}$ | 15 | - | 150 | - |

DYNAMIC

| Symbol | Test Conditions |  |  | Value |  |  | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  | Min. | Typ. | Max. |  |
| Pin | $\mathrm{f}=1215-1400 \mathrm{MHz}$ | PIN $=5 \mathrm{~W}$ Peak | $\mathrm{Vcc}=28 \mathrm{~V}$ | 26 | 36 | - | W |
| $\eta_{c}$ | $\mathrm{f}=1215-1400 \mathrm{MHz}$ | PIN $=5 \mathrm{~W}$ Peak | $\mathrm{V}_{\mathrm{CC}}=28 \mathrm{~V}$ | 45 | 49 | - | \% |
| Gp | $\mathrm{f}=1215-1400 \mathrm{MHz}$ | PIN $=5 \mathrm{~W}$ Peak | $V_{C C}=28 \mathrm{~V}$ | 7.2 | 8.5 | - | dB |

Note: Pulse Width $=1000 \mu \mathrm{~S}$ Duty Cycle $=10 \%$

TYPICAL PERFORMANCE


IMPEDANCE DATA


## TEST CIRCUIT



## PACKAGE MECHANICAL DATA

Ref.: Dwg. No: JI33100D


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[^0]:    *Applies only to rated RF amplifier operation

