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Renesas Technology Corp.
Customer Support Dept.
April 1, 2003

Cautions

Keep safety first in your circuit designs!

1. Renesas Technology Corporation puts the maximum effort into making semiconductor products better and more reliable, but there is always the possibility that trouble may occur with them. Trouble with semiconductors may lead to personal injury, fire or property damage.

Remember to give due consideration to safety when making your circuit designs, with appropriate measures such as (i) placement of substitutive, auxiliary circuits, (ii) use of nonflammable material or (iii) prevention against any malfunction or mishap.

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HSM276ASR

Silicon Schottky Barrier Diode for Balanced Mixer



ADE-208-840(Z)

Rev. 0
Feb. 2000

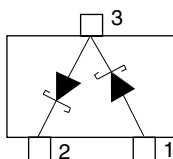
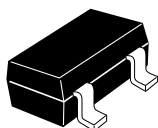
Features

- High forward current, Low capacitance.
- HSM276ASR which is interconnected in series configuration is designed for balanced mixer use.
- MPAK package is suitable for high density surface mounting and high speed assembly.

Ordering Information

| Type No. | Laser Mark | Package Code |
|-----------|------------|--------------|
| HSM276ASR | S20 | MPAK |

Pin Arrangement



(Top View)

- 1 Anode 1
- 2 Cathode 2
- 3 Cathode 1
Anode 2

Absolute Maximum Ratings

(Ta = 25°C)

| Item | Symbol | Value | Unit |
|---------------------------------|-----------|-------------|------|
| Repetitive peak reverse voltage | V_{RRM} | 5 | V |
| Reverse voltage | V_R | 3 | V |
| Average rectified current | I_O^* | 30 | mA |
| Junction temperature | Tj | 125 | °C |
| Storage temperature | Tstg | -55 to +125 | °C |

Note: Per one device

Electrical Characteristics ^{*2}

(Ta = 25°C)

| Item | Symbol | Min | Typ | Max | Unit | Test Condition |
|------------------------------|------------|-----|-----|------|---------------|--|
| Reverse voltage | V_R | 3.0 | — | — | V | $I_R = 1 \text{ mA}$ |
| Reverse current | I_R | — | — | 50 | μA | $V_R = 0.5\text{V}$ |
| Forward current | I_F | 35 | — | — | mA | $V_F = 0.5\text{V}$ |
| Capacitance | C | — | — | 0.90 | pF | $V_R = 0.5\text{V}, f = 1 \text{ MHz}$ |
| Capacitance deviation | ΔC | — | — | 0.10 | pF | $V_R = 0.5\text{V}, f = 1 \text{ MHz}$ |
| ESD-Capability ^{*1} | — | 30 | — | — | V | C=200pF, R= 0 Ω Both forward and reverse direction 1pulse. |

Notes: 1. Failure criterion ; $I_R \geq 100\mu\text{A}$ at $V_R = 0.5 \text{ V}$

2. Per one device

Main Characteristic

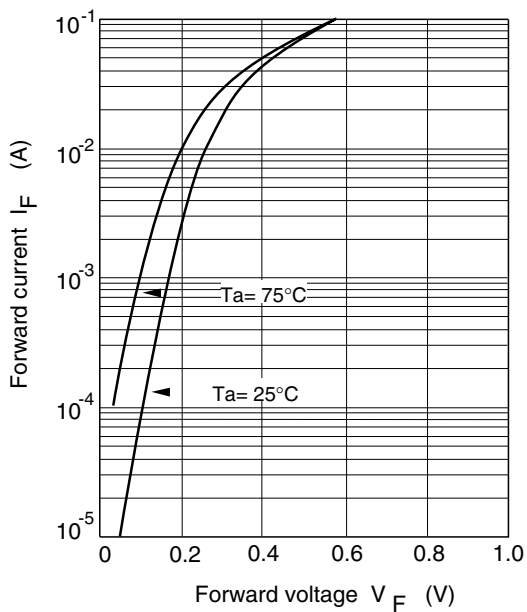


Fig.1 Forward current Vs. Forward voltage

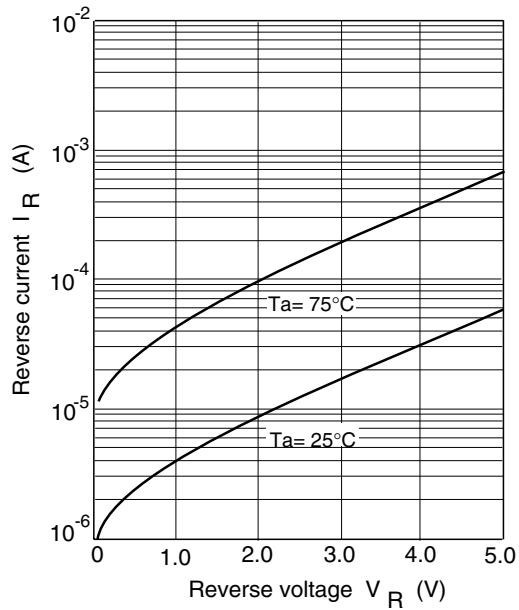


Fig.2 Reverse current Vs. Reverse voltage

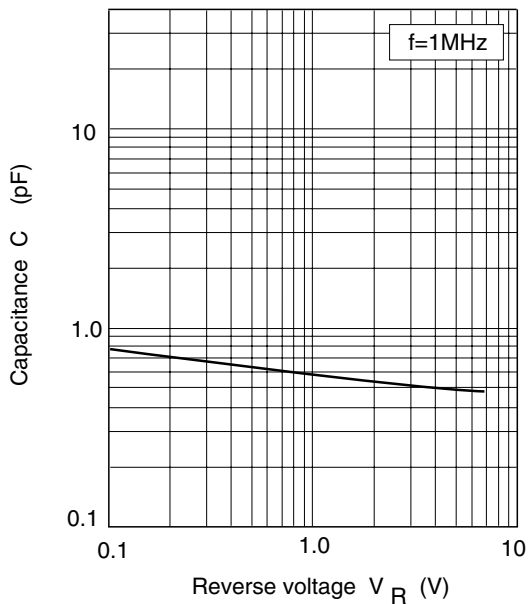
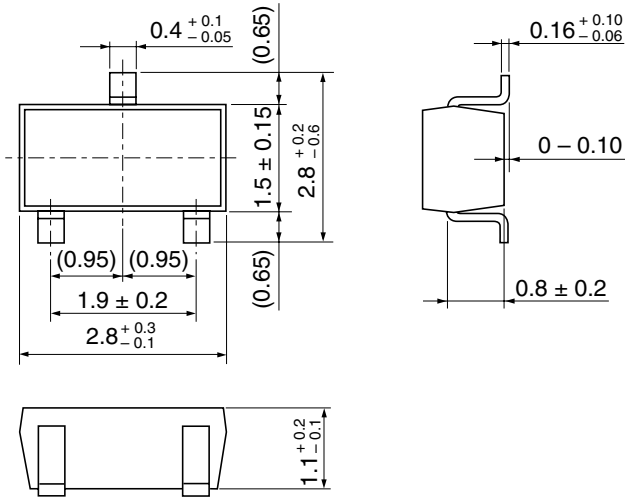


Fig.3 Capacitance Vs. Reverse voltage

Package Dimensions

Unit: mm



| | |
|--------------|----------|
| Hitachi Code | MPAK |
| JEDEC | — |
| EIAJ | Conforms |
| Mass | 0.011 g |

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