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Renesas Technology Corp. Customer Support Dept. April 1, 2003



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## HVD365

### Variable Capacitance Diode for VCO

# RENESAS

ADE-208-951A (Z)

Rev.1 Mar. 2002

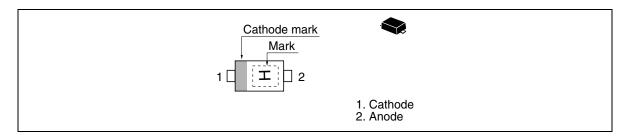
#### Features

- High capacitance ratio and good C-V linearity.
- Super small Flat Package (SFP) is suitable for surface mount design.

#### **Ordering Information**

Туре No.	Laser Mark	Package Code
HVD365	Н	SFP

#### **Pin Arrangement**



#### HVD365

#### **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Value	Unit
Reverse voltage	V <sub>R</sub>	15	V
Junction temperature	Tj	125	٥C
Storage temperature	Tstg	–55 to +125	°C

#### **Electrical Characteristics**

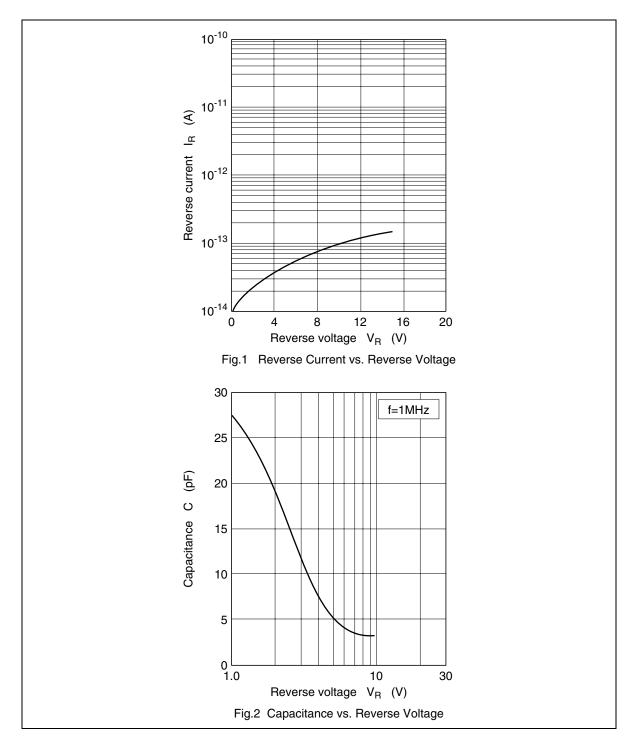
 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse current	I <sub>R1</sub>		_	10	nA	V <sub>R</sub> = 10 V
	I <sub>R2</sub>	_	—	100	_	V <sub>R</sub> = 10 V, Ta = 60°C
Capacitance	<b>C</b> <sub>1</sub>	27.05	_	28.55	pF	$V_{_{\mathrm{H}}} = 1 \text{ V}, \text{ f} = 1 \text{ MHz}$
	<b>C</b> <sub>4</sub>	6.05	_	7.55		$V_{_{\mathrm{R}}} = 4 \text{ V}, \text{ f} = 1 \text{ MHz}$
Capacitance ratio	n	3.0	_	_	_	C <sub>1</sub> / C <sub>4</sub>
Series resistance	r <sub>s</sub>		_	1.5	Ω	V <sub>B</sub> = 4 V, f = 100 MHz
ESD-Capability *1		200			V	$C$ = 200 pF , $R$ = 0 $\Omega,$ Both forward and reverse direction 1 pulse.

Notes 1. Failure criterion ;  $I_{_{\rm R}} \ge 20$  nA at  $V_{_{\rm R}}$  = 10 V

2. Please do not use the soldering iron due to avoid high stress to the SFP package.

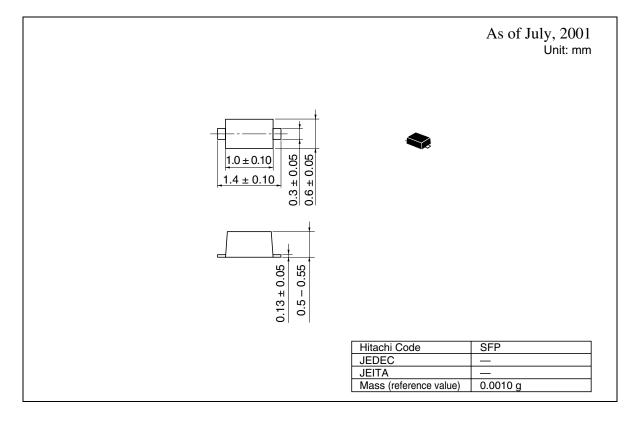
#### **Main Characteristic**



RENESAS

### HVD365

### **Package Dimensions**





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