

## HSB285S

Silicon Schottky Barrier Diode for High frequency detection

REJ03G0010-0100Z

Rev.1.00

Apr,16.2003

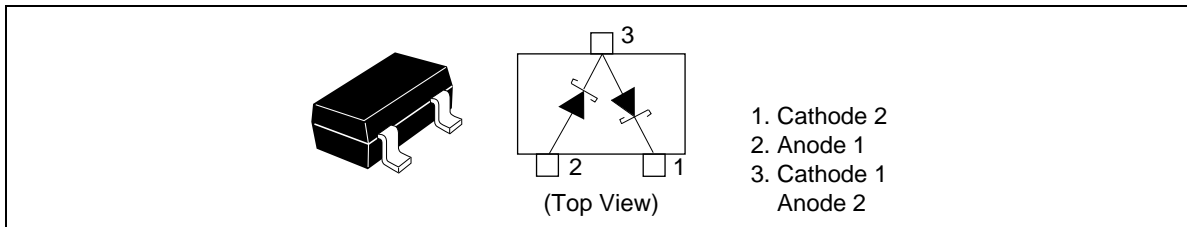
### Features

- Low forward voltage, Low capacitance and High detection sensitivity.
- HSB285S which is interconnected in series configuration. is designed for voltage doubler use.
- CMPAK package is suitable for high density surface mounting and high speed assembly.

### Ordering Information

Type No.	Laser Mark	Package Code
HSB285S	S3	CMPAK

### Pin Arrangement



## HSB285S

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### Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Reverse voltage	$V_R$	2	V
Average rectified current	$I_O^{*1}$	5	mA
Junction temperature	Tj	125	°C
Storage temperature	Tstg	-55 to +125	°C

Note: 1. Per one device

### Electrical Characteristics \*<sup>1</sup>

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Forward voltage	$V_{F1}$	—	—	0.15	V	$I_F = 0.1 \text{ mA}$
	$V_{F2}$	—	—	0.27	V	$I_F = 1 \text{ mA}$
Capacitance	C	—	0.3	—	pF	$V_R = 1 \text{ V}, f = 1 \text{ MHz}$
ESD-Capability * <sup>2</sup>	—	10	—	—	V	C = 200 pF, $R_L = 0 \Omega$ , Both forward and reverse direction 1 pulse.

Notes: 1. Per one device

2. Failure criterion ;  $I_R \geq 100 \mu\text{A}$  at  $V_R = 0.5 \text{ V}$

Main Characteristic

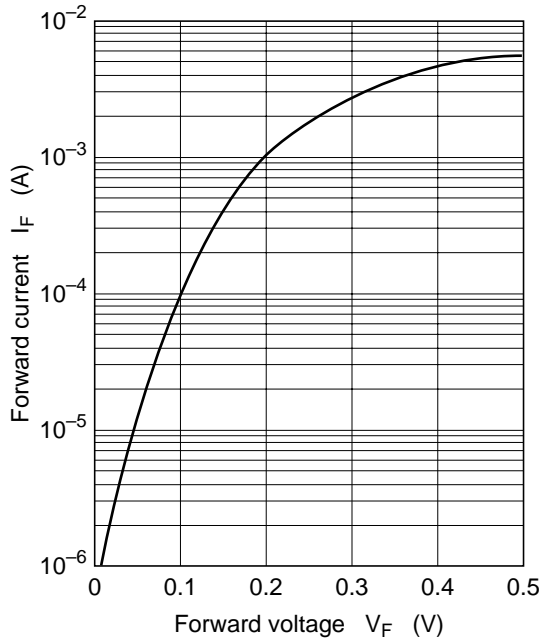


Fig.1 Forward current vs. Forward voltage

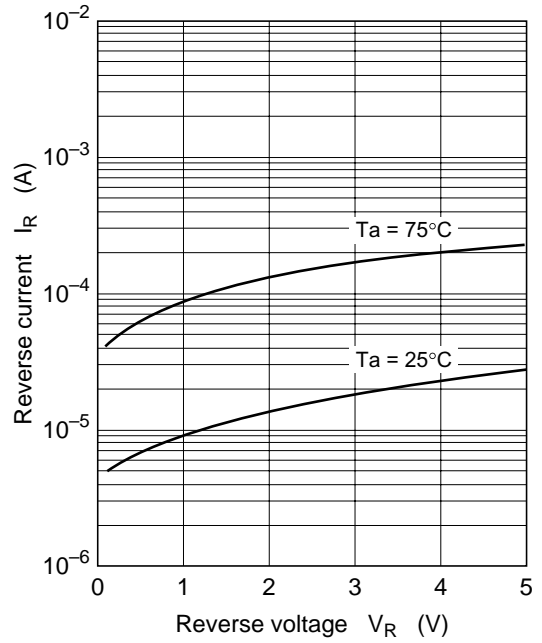


Fig.2 Reverse current vs. Reverse voltage

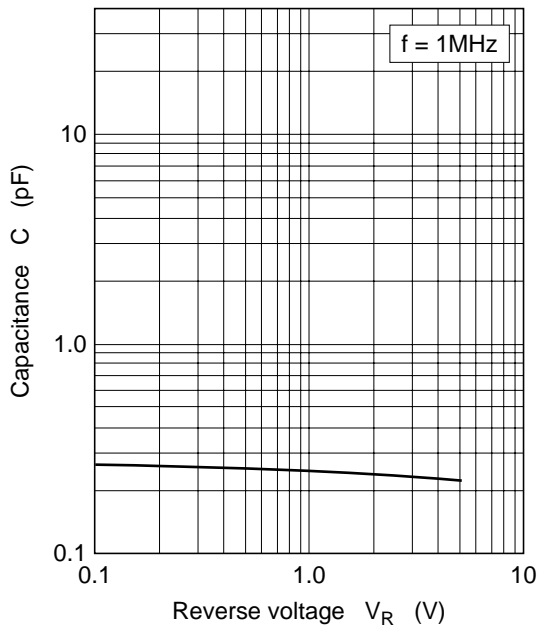
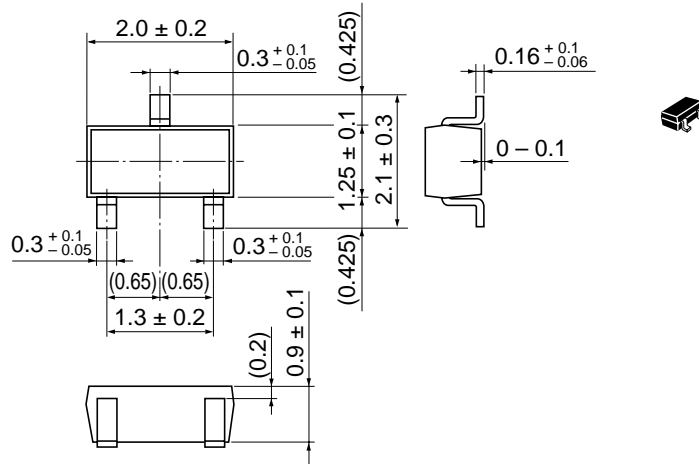


Fig.3 Capacitance vs. Reverse voltage

Package Dimensions

As of January, 2003  
Unit: mm



Package Code	CMPAK
JEDEC	—
JEITA	Conforms
Mass (reference value)	0.006 g

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