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Silicon N-Channel Power MOS FET Array

RENESAS

ADE-208-1208 (Z) 1st. Edition Mar. 2001

Application

High speed power switching

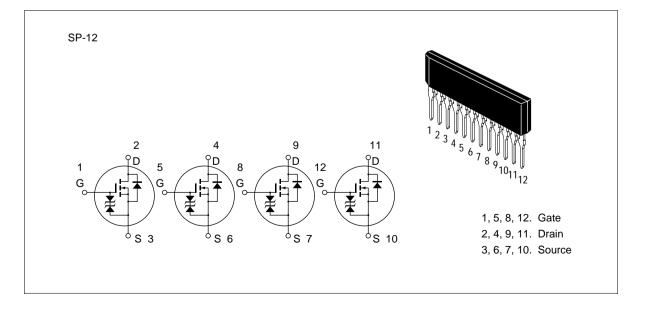
Features

Low on-resistance

$$\begin{split} R_{DS(on)} &\leq 0.06 \ , \, V_{GS} \ = 10 \ V, \, I_D = 5 \ A \\ R_{DS(on)} &\leq 0.075 \ , \, V_{GS} \ = 4 \ V, \, I_D = 5 \ A \end{split}$$

- Capable of 4 V gate drive
- Low drive current
- High speed switching
- High density mounting
- Suitable for motor driver and solenoid driver and lamp driver

Outline



Absolute Maximum Ratings ($Ta = 25^{\circ}C$)

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	60	V
Gate to source voltage	V _{GSS}	±20	V
Drain current	I _D	10	A
Drain peak current	I *1 D(pulse)	32	A
Body to drain diode reverse drain current	I _{DR}	10	A
Channel dissipation	Pch (Tc = 25°C)*2	28	W
Channel dissipation	Pch*2	4	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	–55 to +150	°C

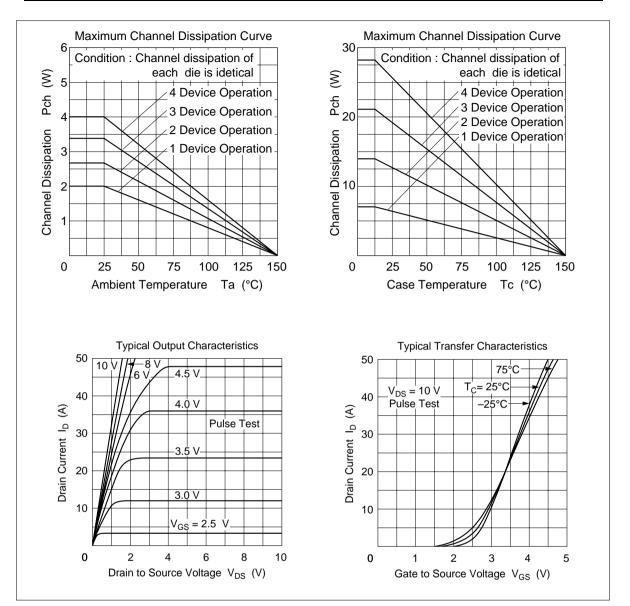
Notes: 1. $PW \le 10 \ \mu s$, duty cycle $\le 1\%$

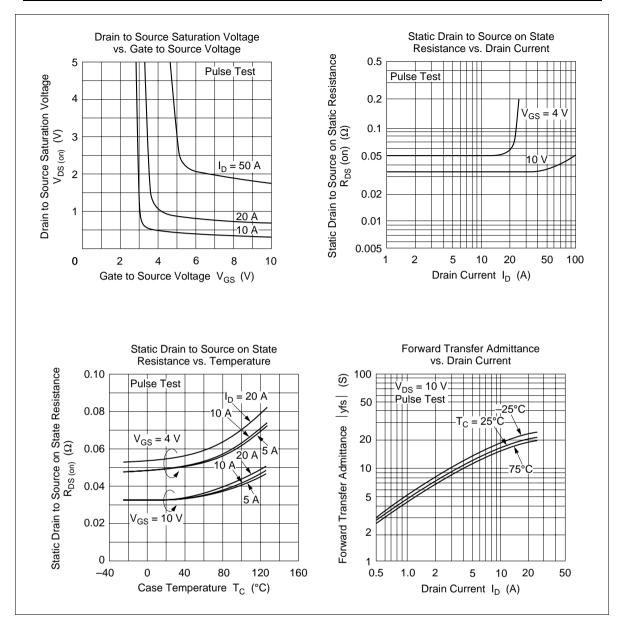
2. 4 Devices operation

Electrical Characteristics (Ta = 25° C)

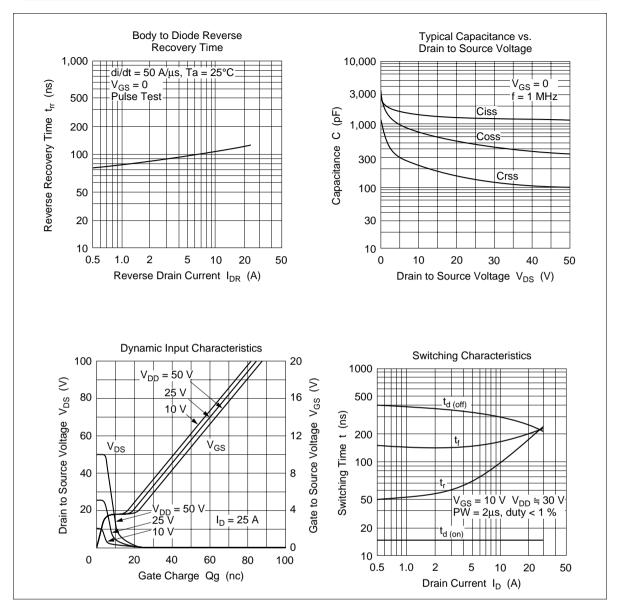
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(\text{BR})\text{DSS}}$	60	—	_	V	$I_{\rm D}$ = 10 mA, $V_{\rm GS}$ = 0
Gate to source breakdown voltage	$V_{(\text{BR})\text{GSS}}$	±20	—	_	V	$I_{g} = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I _{GSS}	—	_	±10	μA	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I _{DSS}	_		250	μA	$V_{\rm DS} = 50 \text{ V}, \text{ V}_{\rm GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.0	_	2.0	V	$I_{\rm D} = 1 \text{ mA}, V_{\rm DS} = 10 \text{ V}$
Static drain to source on state resistance	$R_{DS(on)}$	—	0.045	0.06	Ω	$I_{\rm D} = 5 \text{ A}$ $V_{\rm GS} = 10 \text{ V}^{*1}$
		_	0.056	0.075	Ω	$I_{\rm D} = 5 \text{ A}$ $V_{\rm GS} = 4 \text{ V}^{*1}$
Forward transfer admittance	y _{fs}	10	12	_	S	$I_{\rm D} = 5 \text{ A}$ $V_{\rm DS} = 10 \text{ V}^{*1}$
Input capacitance	Ciss	_	1400	_	pF	V _{DS} = 10 V
Output capacitance	Coss	_	720	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	—	220		pF	f = 1 MHz
Turn-on delay time	t _{d(on)}	_	15		ns	I _D = 10 A
Rise time	t,	_	95	_	ns	V _{GS} = 10 V
Turn-off delay time	$t_{d(off)}$	_	300	_	ns	$R_{L} = 3 \Omega$
Fall time	t _f	_	170	_	ns	
Body to drain diode forward voltage	V_{DF}	_	1.05	—	V	$I_{\rm F} = 10$ A, $V_{\rm GS} = 0$
Body to drain diode reverse recovery time	t _{rr}	_	110	—	μs	$I_{F} = 10 \text{ A}, V_{GS} = 0,$ dIF/dt = 50 A/µs
Note: 1 Pulse Test						

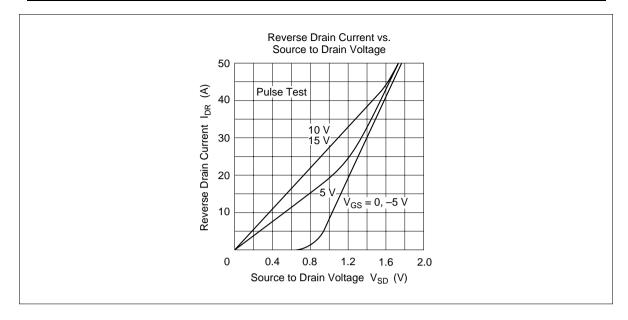
Note: 1. Pulse Test



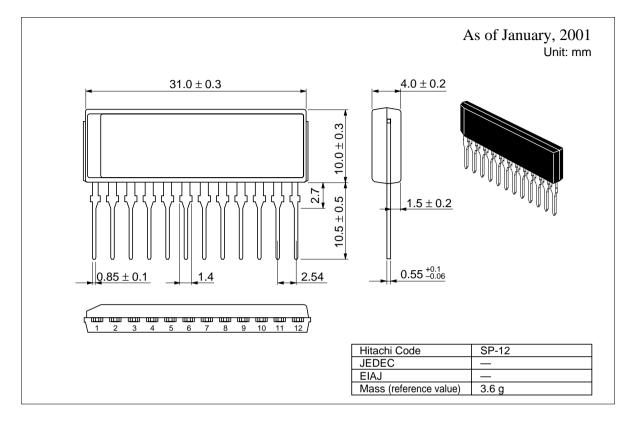


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Package Dimensions



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Semiconductor & Integrated Circuits. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan Tel: Tokyo (03) 3270-2111 Fax: (03) 3270-5109

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For further information write to:

Hitachi Semiconductor (America) Inc. 179 East Tasman Drive, San Jose,CA 95134 Tel: <1> (408) 433-1990 Fax: <1>(408) 433-0223	Hitachi Asia Ltd. Hitachi Tower 16 Collyer Quay #20-00, Singapore 049318 Tel : <65>-538-6533/538-8577 Fax : <65>-538-6933/538-3877 URL : http://www.hitachi.com.sg Hitachi Asia Ltd. (Taipei Branch Office) 4/F, No. 167, Tun Hwa North Road, Hung-Kuo Building, Taipei (105), Taiwan Tel : <886>-(2)-2718-3866 Fax : <886>-(2)-2718-3180 Telex : <23222 HAS-TP URL : http://www.hitachi.com.tw	Hitachi Asia (Hong Kong) Ltd. Group III (Electronic Components) 7/F., North Tower, World Finance Centre, Harbour City, Canton Road Tsim Sha Tsui, Kowloon, Hong Kong Tel : <852>-(2)-735-9218 Fax : <852>-(2)-730-0281 URL : http://www.hitachi.com.hk

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