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Silicon NPN Epitaxial

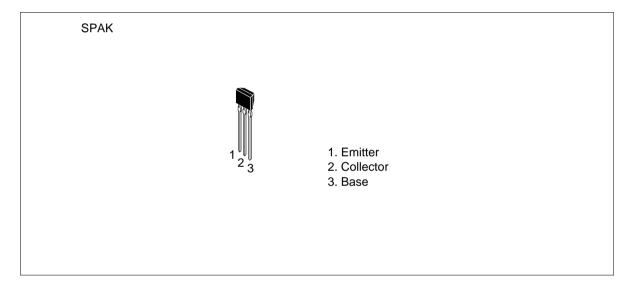


ADE-208-1092 (Z) 1st. Edition Mar. 2001

#### Application

Low frequency amplifier, switching

#### Outline



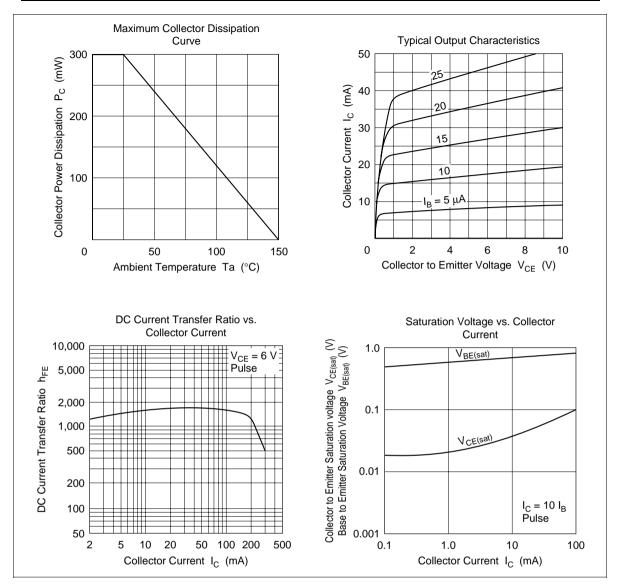
#### **Absolute Maximum Ratings** (Ta = 25°C)

Item	Symbol	Ratings	Unit
Collector to base voltage	V <sub>CBO</sub>	60	V
Collector to emitter voltage	V <sub>CEO</sub>	50	V
Emitter to base voltage	V <sub>EBO</sub>	15	V
Collector current	Ι <sub>c</sub>	300	mA
Collector power dissipation	Pc	300	mW
Junction temperature	Tj	150	°C
Storage temperature	Tstg	-55 to +150	°C

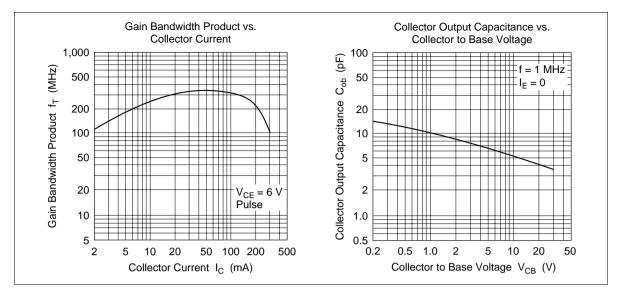
#### **Electrical Characteristics** (Ta = 25°C)

Item	Symbol	Min	Тур	Max	Unit	Test conditions
Collector to base breakdown voltage	$V_{(\text{BR})\text{CBO}}$	60	_	_	V	$I_{c} = 10 \ \mu A, \ I_{E} = 0$
Collector to emitter breakdown voltage	$V_{(\text{BR})\text{CEO}}$	50	_	_	V	$I_c = 1 \text{ mA}, R_{BE} =$
Emitter to base breakdown voltage	$V_{\rm (BE)EBO}$	15	—	_	V	$I_{\rm E} = 10 \ \mu A, \ I_{\rm C} = 0$
Collector cutoff current	I <sub>CBO</sub>	—	—	1	μA	$V_{CB} = 50 \text{ V}, \text{ I}_{E} = 0$
Base to emitter voltage	$V_{BE}$	—	_	0.75	V	$V_{ce} = 6 V, I_c = 1 mA$
DC current transfer ratio	$h_{\text{FE1}}$	800	_	2000		$V_{ce} = 6 \text{ V}, \text{ I}_{c} = 100 \text{ mA}$ (pulse test)
	$h_{\text{FE2}}$	500	_	—		$V_{ce} = 6 V, I_c = 1 mA$
Collector to emitter saturation voltage	$V_{\text{CE(sat)}}$		—	0.3	V	$I_c = 300 \text{ mA}, I_B = 30 \text{ mA}$ (pulse test)

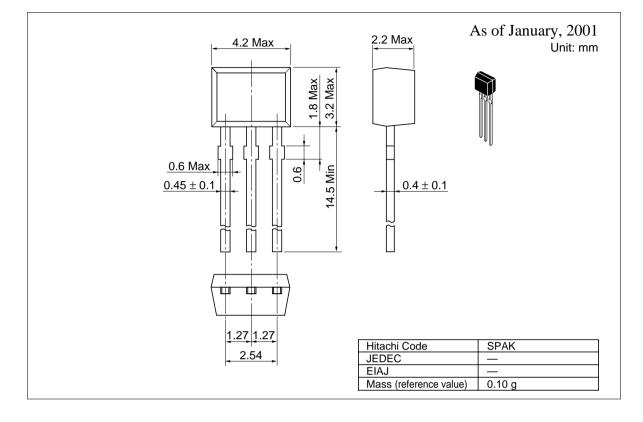








#### **Package Dimensions**



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