

# AM83135-015

# RF & MICROWAVE TRANSISTORS S-BAND RADAR APPLICATIONS

#### REFRACTORY/GOLD METALLIZATION

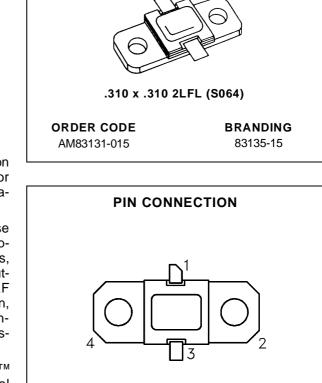
- EMITTER SITE BALLASTED
- LOW THERMAL RESISTANCE
- INPUT/OUTPUT MATCHING
- OVERLAY GEOMETRY
- METAL/CERAMIC HERMETIC PACKAGE
- Pout = 15 W MIN. WITH 5.2 dB GAIN

#### DESCRIPTION

The AM83135-015 device is a high power silicon bipolar NPN transistor specifically designed for S-Band radar pulsed output and driver applications.

This device is characterized at  $100\mu$ sec pulse width and 10% duty cycle, but is capable of operation over a range of pulse widths, duty cycles, and temperatures, and can withstand a 3:1 output VSWR with a + 1 dB input overdrive. Low RF thermal resistance, refractory/gold metallization, and computerized automatic wire bonding techniques ensure high reliability and product consistency (including phase characteristics).

The AM83135-015 is supplied in the IMPAC<sup>™</sup> Hermetic Metal/Ceramic package with internal Input/Output impedance matching circuitry, and is intended for military and other high reliability applications.



1. Collector3. Emitter2. Base4. Base

## **ABSOLUTE MAXIMUM RATINGS** (T<sub>case</sub> = 25°C)

Symbol	Parameter Value		Unit
PDISS	Power Dissipation <sup>*</sup> $(T_C \le 50^{\circ}C)$	sipation* $(T_C \le 50^{\circ}C)$ 71	
Ic	Device Current* 3.0		А
Vcc	Collector-Supply Voltage*	46	V
TJ	Junction Temperature (Pulsed RF Operation) 250		°C
T <sub>STG</sub>	Storage Temperature	– 65 to +200	°C

#### THERMAL DATA

	R <sub>TH(j-c)</sub>	Junction-Case Thermal Resistance*	2.8	°C/W				
*Applies only to rated PE amplifier operation								

\*Applies only to rated RF amplifier operation

PRELIMINARY DATA

# **ELECTRICAL SPECIFICATIONS** ( $T_{case} = 25^{\circ}C$ )

### STATIC

Symbol	Test Conditions	Value			Unit		
	Test conditions		Min.	Тур.	Max.	Unit	
BV <sub>CBO</sub>	$I_C = 10 \text{ mA}$	$I_E = 0 mA$		55	—	—	V
BV <sub>EBO</sub>	$I_E = 2 \text{ mA}$	$I_{C} = 0 \text{ mA}$		3.5			V
BV <sub>CER</sub>	$I_C = 10 \text{ mA}$	$R_{BE} = 10 \ \Omega$		55	_		V
ICES	$V_{BE} = 0 V$	$V_{CE} = 40 V$			_	8	mA
hFE	$V_{CE} = 5 V$	$I_C = 1 A$		30		300	—

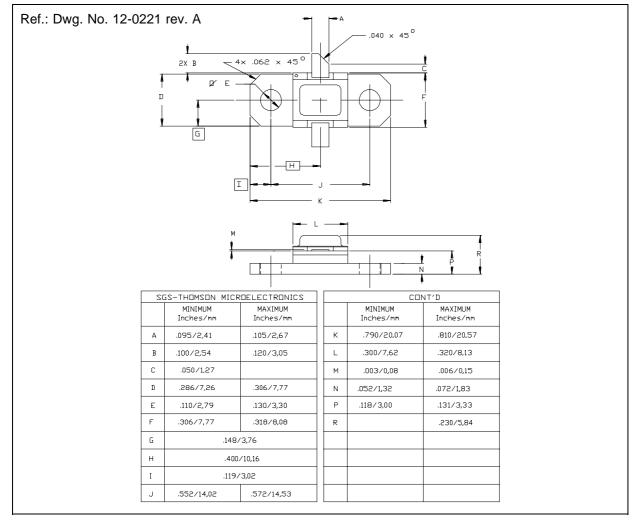
#### DYNAMIC

Symbol	Test Conditions			Value			
Symbol	indor rest conditions			Min.	Тур.	Max.	Unit
Pout	f = 3.1 - 3.5  GHz	$P_{IN}=4.5\ W$	$V_{CC} = 40 \ V$	15			W
η <sub>c</sub>	f = 3.1 - 3.5  GHz	$P_{OUT} = 15 W$	$V_{CC} = 40 V$	30	_		%
Pg	f = 3.1 - 3.5  GHz	$P_{OUT} = 15 W$	$V_{CC} = 40 V$	5.2			dB

Note: Pulse Width =  $100\mu$ S Duty Cycle = 10%



#### PACKAGE MECHANICAL DATA



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