CS-1009

2.5 Volt Reference

Description

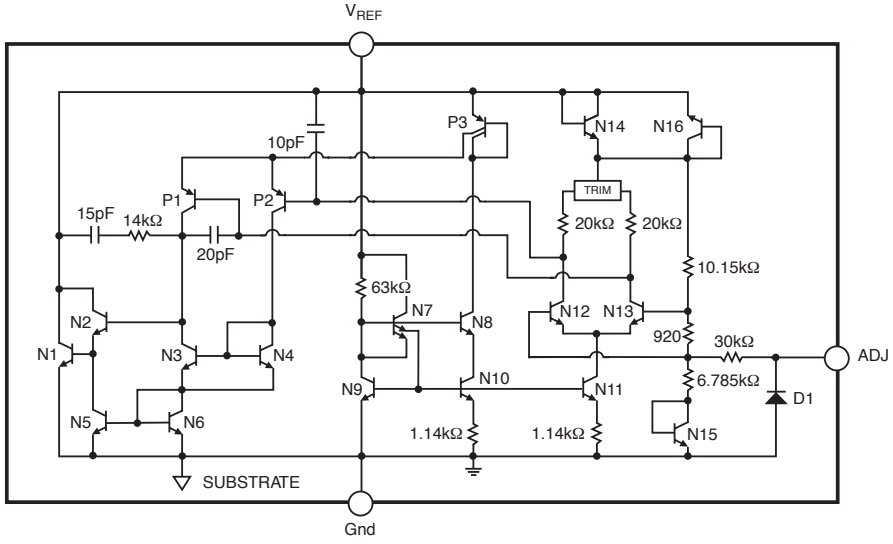
The CS-1009 is a precision trimmed 2.500V ±5mV shunt regulator diode. The low dynamic impedance and wide operating current range enhances its versatility. The tight reference tolerance is achieved by on-chip trimming which minimizes voltage tolerance and temperature drift.

A third terminal allows the reference voltage to be adjusted ±5% to calibrate out system errors. In many applications, the CS-1009CZ can be used as a pin-to-pin replacement of the LT1009CZ and the LM136Z-2.5 with the external trim network eliminated.

Absolute Maximum Ratings

Reverse Current 20mA
Forward 10mA
Operating Temperature Range
Commercial .....0˚C to 70˚C
Extended .....-40˚C to 105˚C
Storage Temperature Range ..... -65˚C to 150˚C
Lead Temperature Soldering
Wave Solder(through hole styles only).....10 sec. max, 260°C peak
Reflow (SMD styles only).....60 sec. max above 183°C, 230°C peak

Block Diagram

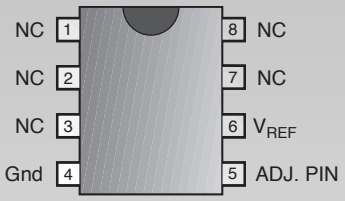


Features

- **0.2% initial tolerance max.**
- **Guaranteed temperature stability**
- **Maximum 0.6Ω dynamic impedance**
- **Wide operating current range**
- **Directly interchangeable with LT1009 and LM136 for improved performance**
- **No adjustments needed for minimum temperature coefficient**
- **Meets Mil Std 883C ESD requirements**

Package Options

8 Lead SO



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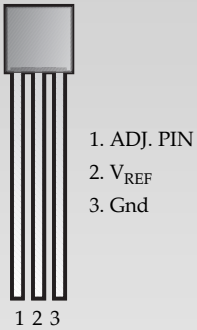

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Electrical Characteristics: T_A = 25°C unless otherwise specified

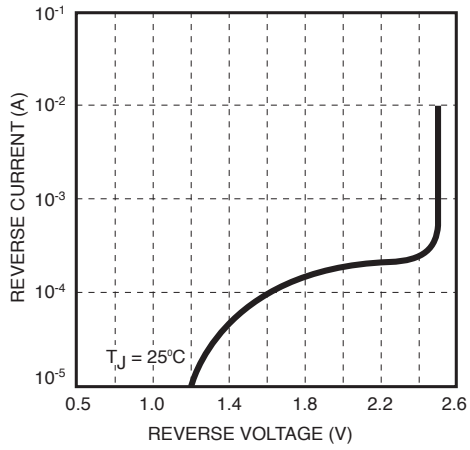
PARAMETER	TEST CONDITIONS		CS-1009 C MIN	CS-1009 C TYP	CS-1009 C MAX	CS-1009 X MIN	CS-1009 X TYP	CS-1009 X MAX	UNITS
Reverse Breakdown Voltage	I_R = 1mA		2.495	2.500	2.505	2.492	2.500	2.508	V
Reverse Breakdown Voltage	0°C ≤ T_A ≤ 105°C					2.492	2.500	2.508	V
Reverse Breakdown Voltage	-40°C ≤ T_A ≤ 0°C					2.480	2.500	2.508	V
Reverse Breakdown	400μA ≤ I_R ≤ 10mA			2.6	10		2.6	10	mV
Change with Current		•		3	12		3	12	mV
Reverse Dynamic Impedance	I_R = 1mA			0.2	1.0		0.2	1.0	Ω
		•		0.4	1.4		0.4	1.4	Ω
Temperature Stability	0°C ≤T_A ≤ 70°C (Note 1)			1.8	4.0				mV
Avg. Temp. Coefficient	0°C ≤ T_A ≤ 70°C (Note 1)			15	25				ppm/°C
Long Term Stability	T_A = 25°C ± 0.1C, I_R = 1mA			20			20		ppm/kHr

The • denotes the specifications which apply over full operating temperature range.

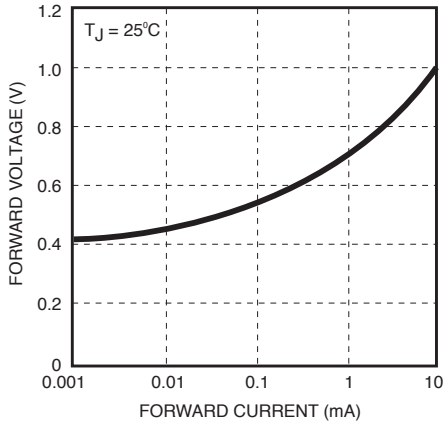
Note 1: Average temperature coefficient is defined as the total voltage change divided by the specified temperature range.

Typical Performance Characteristics

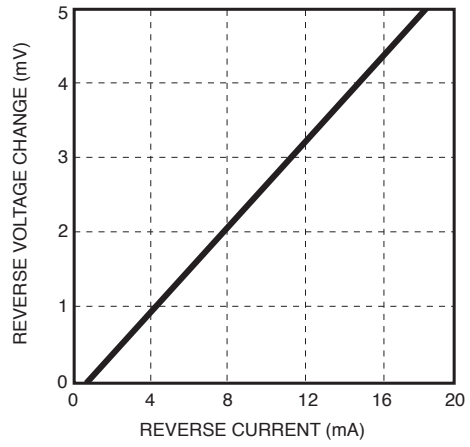
Reverse Current vs. Reverse Voltage



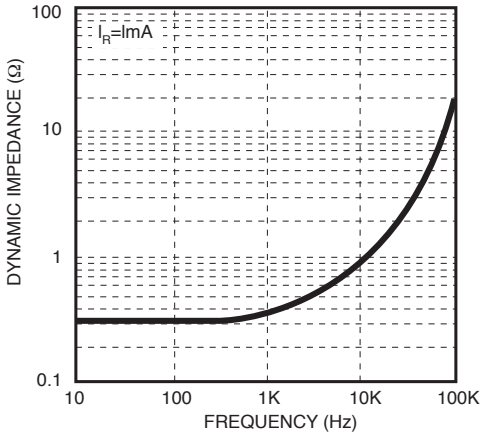
Forward Voltage vs. Forward Current



Change in Reverse Voltage vs. Reverse Current

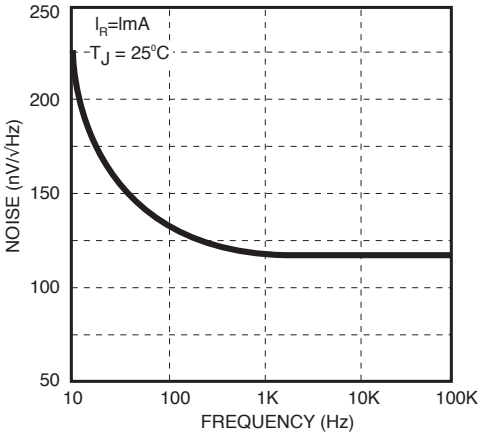


Dynamic Impedance vs. Frequency

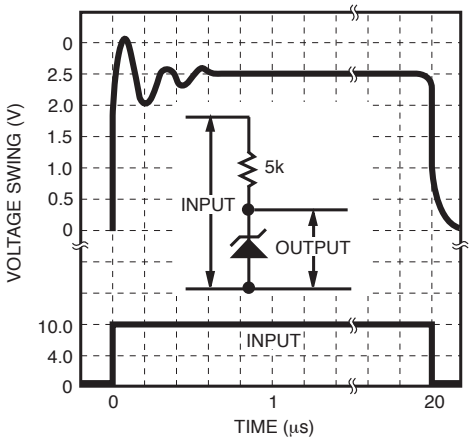


Typical Performance Characteristics: continued

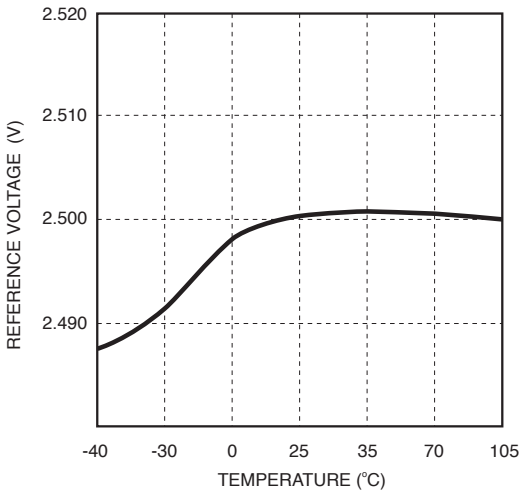
Zener Noise Voltage vs. Frequency



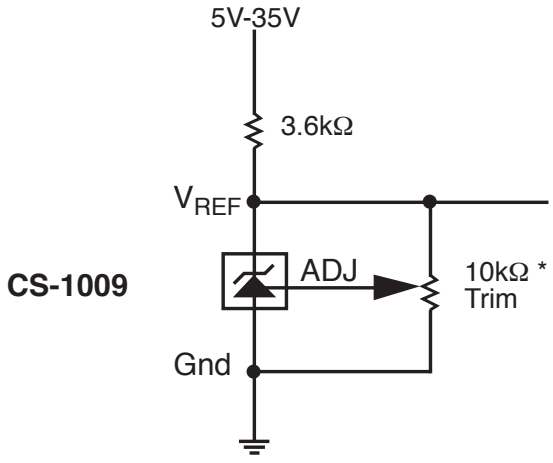
Response Time



Reference Voltage vs. Temperature



Application Diagram



* ±5% Trim Range

Package Specification

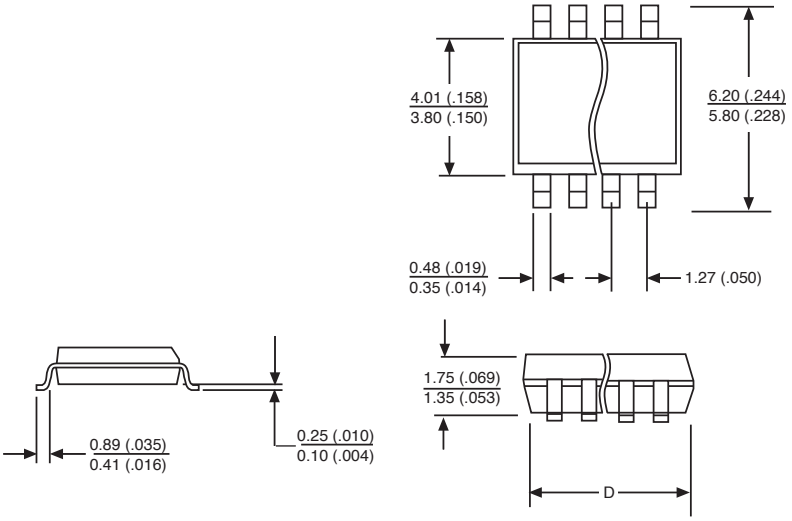
PACKAGE DIMENSIONS IN mm (INCHES)

Lead Count	D			
	Metric		English	
	Max	Min	Max	Min
8L SO Narrow	5.00	4.80	.197	.188

PACKAGE THERMAL DATA

Thermal Data		8L SO	TO-92	
$R_{\Theta JC}$	typ	45	–	°C/W
$R_{\Theta JA}$	typ	165	170	°C/W

SO-Narrow



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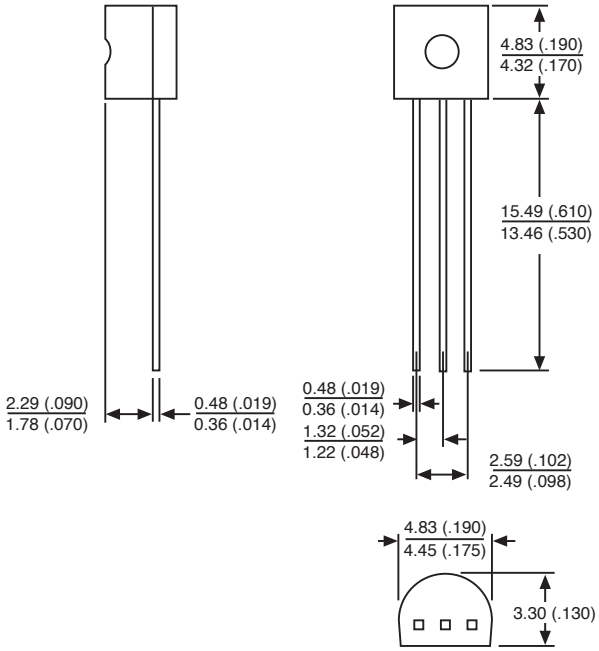

Ordering Information

Part Number	0°C to 70°C	-40°C to 105°C	Description
CS-1009CD8	•		8L SO
CS-1009CZ	•		TO-92
CS-1009XD8		•	8L SO
CS-1009XZ		•	TO-92

Cherry Semiconductor Corporation reserves the right to make changes to the specifications without notice. Please contact Cherry Semiconductor Corporation for the latest available information.