MOS FET Power Amplifier Module for E-GSM Handy Phone

HITACHI

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Application

- For E-GSM class4 880 to 915 MHz
- For 3.5 V nominal battery use

Features

• High gain 3stage amplifier: 0 dBm input

• Lead less thin & Small package: 2 mm Max, 0.2cc

High efficiency: 48% Typ at 35.5 dBmWide gain control range: 70 dB Typ

Pin Arrangement



1 : Pin 2 : Vapc 3 : Vdd

4 : Pout G : GND

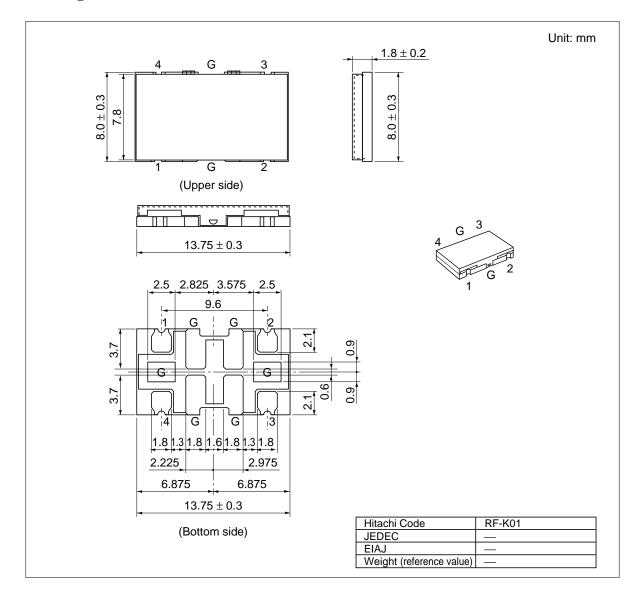
Absolute Maximum Ratings (Tc = 25°C)

Item	Symbol	Rating	Unit	
Supply voltage	V_{DD}	8	V	
Supply current	I _{DD}	3	Α	
V _{APC} voltage	V _{APC}	4	V	
Input power	Pin	10	mW	
Operating case temperature	Tc (op)	−30 to +100	°C	
Storage temperature	Tstg	-30 to +100	°C	
Output power	Pout	5	W	

Electrical Characteristics (Tc = 25°C)

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Frequency range	f	880	_	915	MHz	
Control voltage range	V_{APC}	0.5	_	2.2	V	
Drain cutoff current	I _{DS}	_	_	100	μΑ	$V_{DD} = 8 \text{ V}, V_{APC} = 0 \text{ V}$
Total efficiency	$\eta_{\scriptscriptstyle T}$	43	48		%	$Pin = 0 dBm, V_{DD} = 3.5 V,$
2nd harmonic distortion	2nd H.D.	_	- 55	-35	dBc	Pout = 35.5 dBm, Vapc = control
3rd harmonic distortion	3rd H.D.	_	- 55	-35	dBc	$R_L = Rg = 50 \Omega$, $Tc = 25^{\circ}C$
Input VSWR	VSWR (in)	_	1.5	3	_	
Output power (1)	Pout (1)	35.5	36.0	_	dBm	$Pin = 0 dBm, V_{DD} = 3.5 V,$ $V_{APC} = 2.2 V, R_{L} = Rg = 50 Ω,$ Tc = 25°C
Output power (2)	Pout (2)	33.5	34.2	_	dBm	$\begin{aligned} &\text{Pin} = 0 \text{ dBm}, \text{ V}_{\text{DD}} = 3.0 \text{ V}, \\ &\text{V}_{\text{APC}} = 2.2 \text{ V}, \text{ R}_{\text{L}} = \text{Rg} = 50 \Omega, \\ &\text{Tc} = 85^{\circ}\text{C} \end{aligned}$
Isolation	_		-40	-36	dBm	$\begin{aligned} &\text{Pin} = 0 \text{ dBm}, \text{ V}_{\text{DD}} = 3.5 \text{ V}, \\ &\text{V}_{\text{APC}} = 0.5 \text{ V}, \text{ R}_{\text{L}} = \text{Rg} = 50 \Omega, \\ &\text{Tc} = 25^{\circ}\text{C} \end{aligned}$
Switching time	tr, tf		1	2	μs	Pin = 0 dBm, V_{DD} = 3.5 V, Pout = 0 to 35.5 dBm R_L = Rg = 50 Ω, Tc = 25°C
Stability	_	No parasitic oscillation			_	Pin = 0 dBm, V_{DD} = 3 to 5.1 V, Pout \leq 35.5 dBm, Vapc \leq 2.2 V GSM pulse. Rg = 50 Ω , Tc = 25°C, Output VSWR = 6 : 1 All phases
Load VSWR tolerance	_	No degradation			_	Pin = 0 dBm, V_{DD} = 3 to 5.1 V, Pout \leq 35.5 dBm, Vapc \leq 2.2 V GSM pulse. Rg = 50 Ω , t = 20 sec., Tc = 25°C, Output VSWR = 10 : 1 All phases

Package Dimensions



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