

L7900AC SERIES

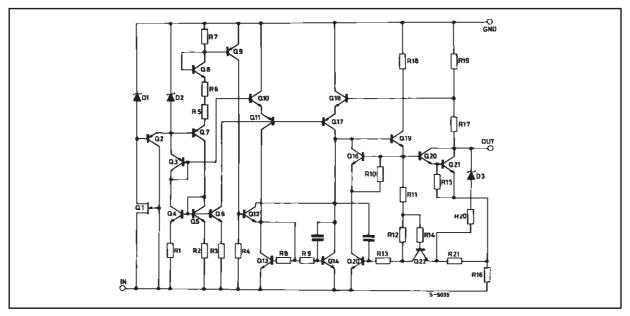
2% NEGATIVE VOLTAGE REGULATORS

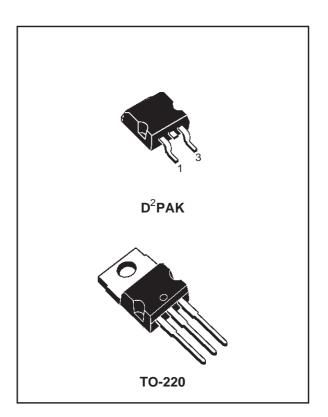
- OUTPUT CURRENT UP TO 1.5 A
- OUTPUT VOLTAGES OF -5; -5.2; -6; -8; -12; -15; -18; -20; -22; -24V
- THERMAL OVERLOAD PROTECTION
- SHORT CIRCUIT PROTECTION
- OUTPUT TRANSITION SOA PROTECTION

DESCRIPTION

The L7900AC series of three-terminal negative regulators is available in TO-220 and D²PAK packages and several fixed output voltages. These regulators can provide local on-card regulation, eliminating the distribution problems associated with single point regulation; furthermore, having the same voltage option as the L7800A positive standard series, they are particularly suited for split power supplies. In addition, the -5.2V is also available for ECL system. If adequate heat sinking is provided, they can deliver over 1.5A output current. Although designed primarily as fixed voltage regulators, these devices can be used with external components to obtain adjustable voltages and currents.

SCHEMATIC DIAGRAM





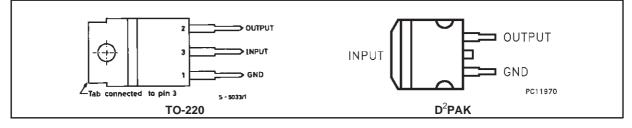
ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
Vi	DC Input Voltage (for $V_0 = -5$ to $-18V$) (for $V_0 = -20, -24V$)	-35 -40	V V
l _o	Output Current	Internally limited	
P _{tot}	Power Dissipation	Internally limited	
T _{op}	Operating Junction Temperature Range	0 to 125	°C
T _{stg}	Storage Temperature Range	- 65 to 150	°C

THERMAL DATA

Symbol	Parameter	D ² PAK	TO-220	Unit
R _{thj-case}	Thermal Resistance Junction-case Max	3	3	°C/W
R _{thj-amb}	Thermal Resistance Junction-ambient Max	62.5	50	°C/W

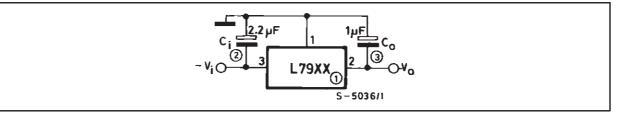
CONNECTION DIAGRAM AND ORDERING NUMBERS (top view)



Туре	TO-220	D ² PAK (*)	Output Voltage
L7905AC	L7905ACV	L7905ACD2T	-5V
L7952AC	L7952ACV	L7952ACD2T	-5.2V
L7906AC	L7906ACV	L7906ACD2T	-6V
L7908AC	L7908ACV	L7908ACD2T	-8V
L7912AC	L7912ACV	L7912ACD2T	-12V
L7915AC	L7915ACV	L7915ACD2T	-15V
L7918AC	L7918ACV	L7918ACD2T	-18V
L7920AC	L7920ACV	L7920ACD2T	-20V
L7922AC	L7922ACV	L7922ACD2T	-22V
L7924AC	L7924ACV	L7924ACD2T	-24V

(*) AVAILABLE IN TAPE AND REEL WITH "-TR" SUFFIX

APPLICATION CIRCUIT



Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Vo	Output Voltage	$T_j = 25 \ ^{\circ}C$	-4.9	-5	-5.1	V
Vo	Output Voltage	$ I_o = -5 \text{ mA to } -1 \text{ A} P_o \leq 15 \text{ W} $ $ V_i = 8 \text{ to } 20 \text{ V} $	-4.8	-5	-5.2	V
ΔV_0^*	Line Regulation				100 50	mV mV
ΔV_0^*	Load Regulation				100 50	mV mV
ld	Quiescent Current	$T_j = 25 °C$			3	mA
ΔI_d	Quiescent Current Change	l _o = 5 to 1000 mA			0.5	mA
ΔI_d	Quiescent Current Change	$V_i = -8 \text{ to } -25 \text{ V}$			1.3	mA
$\frac{\Delta V_o}{\Delta T}$	Output Voltage Drift	l _o = 5 mA		-0.4		mV/°C
e _N	Output Noise Voltage	B = 10Hz to 100KHz $T_j = 25 ^{\circ}C$		100		μV
SVR	Supply Voltage Rejection	$\Delta V_i = 10 \text{ V} \qquad f = 120 \text{ Hz}$	54	60		dB
V _d	Dropout Voltage	$I_o = 1 A$ $T_j = 25 °C$ $\Delta V_O = 100 mV$		1.4		V
lsc	Short Circuit Current			2.1		А
Iscp	Short Circuit Peak Current	$T_j = 25 \ ^{\circ}C$		2.5		А

ELECTRICAL CHARACTERISTICS FOR L7905A (refer to the test circuits, $T_j = 0$ to 125 °C, $V_i = -10V$, $I_0 = 500$ mA, $C_i = 2.2 \ \mu$ F, $C_0 = 1 \ \mu$ F unless otherwise specified)

ELECTRICAL CHARACTERISTICS FOR L7952A (refer to the test circuits, $T_j = 0$ to 125 °C,

 $V_i = -10V$, $I_0 = 500$ mA, $C_i = 2.2 \mu F$, $C_0 = 1 \mu F$ unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Vo	Output Voltage	$T_j = 25 °C$	-5.1	-5.2	-5.3	V
Vo	Output Voltage	$ I_{o} = -5 \text{ mA to } -1 \text{ A } P_{o} \le 15 \text{ W} $ $ V_{i} = -9 \text{ to } -21 \text{ V} $	-5	-5.2	-5.4	V
ΔV_0^*	Line Regulation				105 52	mV mV
ΔV_{o}^{*}	Load Regulation				105 52	mV mV
١ _d	Quiescent Current	$T_j = 25 ^{\circ}C$			3	mA
ΔI_d	Quiescent Current Change	I _o = 5 to 1000 mA			0.5	mA
ΔI_d	Quiescent Current Change	V _i = -9 to -25 V			1.3	mA
$\frac{\Delta V_o}{\Delta T}$	Output Voltage Drift	$I_0 = 5 \text{ mA}$		-0.5		mV/ºC
e _N	Output Noise Voltage	B = 10Hz to 100KHz $T_j = 25 ^{\circ}C$		125		μV
SVR	Supply Voltage Rejection	$\Delta V_i = 10 \text{ V}$ f = 120 Hz	54	60		dB
V _d	Dropout Voltage	$I_o = 1 A$ $T_j = 25 °C$ $\Delta V_O = 100 mV$		1.4		V
lsc	Short Circuit Current			2.1		A
Iscp	Short Circuit Peak Current	$T_j = 25 ^{\circ}C$		2.5		A

* Load and line regulation are specified at constant junction temperature. Changes in Vo due to heating effects must be taken into account separately. Pulce testing with low duty cycle is used.

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Vo	Output Voltage	$T_j = 25 °C$	-5.88	-6	-6.12	V
Vo	Output Voltage	$\begin{array}{l} I_{o} = -5 \text{ mA to -1 A} P_{o} \leq 15 \text{ W} \\ V_{i} = -9.5 \text{ to -21.5 V} \end{array}$	-5.76	-6	-6.24	V
ΔVo*	Line Regulation	$ V_i = -8.5 \text{ to } -25 \text{ V} T_j = 25 \ ^{\circ}\text{C} \\ V_i = -9 \text{ to } -15 \text{ V} T_j = 25 \ ^{\circ}\text{C} $			120 60	mV mV
ΔVo*	Load Regulation	$ I_o = 5 \text{ to } 1500 \text{ mA} T_j = 25 \ ^o\text{C} \\ I_o = 250 \text{ to } 750 \text{ mA} T_j = 25 \ ^o\text{C} $			120 60	mV mV
ld	Quiescent Current	$T_j = 25 °C$			3	mA
ΔI_d	Quiescent Current Change	$I_0 = 5 \text{ to } 1000 \text{ mA}$			0.5	mA
ΔI_d	Quiescent Current Change	$V_i = -9.5 \text{ to } -25 \text{ V}$			1.3	mA
$\frac{\Delta V_o}{\Delta T}$	Output Voltage Drift	$I_0 = 5 \text{ mA}$		-0.6		mV/°C
e _N	Output Noise Voltage	$B = 10Hz$ to 100KHz $T_j = 25 ^{\circ}C$		144		μV
SVR	Supply Voltage Rejection	$\Delta V_i = 10 \text{ V}$ f = 120 Hz	54	60		dB
V _d	Dropout Voltage	$I_o = 1 A$ $T_j = 25 °C$ $\Delta V_O = 100 mV$		1.4		V
lsc	Short Circuit Current			2		А
Iscp	Short Circuit Peak Current	$T_j = 25 °C$		2.5		А

ELECTRICAL CHARACTERISTICS FOR L7906A (refer to the test circuits, $T_j = 0$ to 125 °C,

 $V_i = -11V$, $I_o = 500$ mA, $C_i = 2.2 \ \mu$ F, $C_o = 1 \ \mu$ F unless otherwise specified)

ELECTRICAL CHARACTERISTICS FOR L7908A (refer to the test circuits, $T_j = 0$ to 125 °C,

 $V_i = -14V$, $I_0 = 500$ mA, $C_i = 2.2 \mu$ F, $C_0 = 1 \mu$ F unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Vo	Output Voltage	$T_j = 25 °C$	-7.84	-8	-8.16	V
Vo	Output Voltage	$ I_o = -5 \text{ mA to } -1 \text{ A} P_o \le 15 \text{ W} \\ V_i = -11.5 \text{ to } -23 \text{ V} $	-7.68	-8	-8.32	V
ΔV_0^*	Line Regulation	$V_i = -10.5 \text{ to } -25 \text{ V} T_j = 25 ^{\circ}\text{C}$ $V_i = -11 \text{ to } -17 \text{ V} T_j = 25 ^{\circ}\text{C}$			160 80	mV mV
ΔV_{o}^{*}	Load Regulation				160 80	mV mV
١ _d	Quiescent Current	$T_j = 25 \ ^{\circ}C$			3	mA
ΔI_d	Quiescent Current Change	I _o = 5 to 1000 mA			0.5	mA
ΔI_d	Quiescent Current Change	V _i = -11.5 to -25 V			1	mA
$\frac{\Delta V_o}{\Delta T}$	Output Voltage Drift	$I_o = 5 \text{ mA}$		-0.6		mV/ºC
e _N	Output Noise Voltage	$B = 10$ Hz to 100KHz $T_j = 25 $ °C		175		μV
SVR	Supply Voltage Rejection	$\Delta V_i = 10 \text{ V}$ f = 120 Hz	54	60		dB
V _d	Dropout Voltage	$I_o = 1 A$ $T_j = 25 °C$ $\Delta V_O = 100 mV$		1.1		V
lsc	Short Circuit Current			1.5		A
Iscp	Short Circuit Peak Current	$T_j = 25 ^{\circ}C$		2.5		A

 * Load and line regulation are specified at constant junction temperature. Changes in V_o due to heating effects must be taken into account separately. Pulce testing with low duty cycle is used.

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Vo	Output Voltage	$T_j = 25 °C$	-11.75	-12	-12.25	V
Vo	Output Voltage	$ I_o = -5 \text{ mA to -1 A} P_o \le 15 \text{ W} $ $ V_i = -15.5 \text{ to -27 V} $	-11.5	-12	-12.5	V
ΔV_{o}^{\star}	Line Regulation				240 120	mV mV
ΔV_0^*	Load Regulation	$ I_{o} = 5 \text{ to } 1500 \text{ mA} T_{j} = 25 \ ^{o}\text{C} \\ I_{o} = 250 \text{ to } 750 \text{ mA} T_{j} = 25 \ ^{o}\text{C} $			240 120	m∨ mV
١ _d	Quiescent Current	$T_j = 25 °C$			3	mA
ΔI_d	Quiescent Current Change	$I_0 = 5 \text{ to } 1000 \text{ mA}$			0.5	mA
ΔI_d	Quiescent Current Change	V _i = -15 to -25 V			1	mA
$\frac{\Delta V_o}{\Delta T}$	Output Voltage Drift	I _o = 5 mA		-0.8		mV/°C
e _N	Output Noise Voltage	$B = 10Hz$ to 100KHz $T_j = 25 °C$		200		μV
SVR	Supply Voltage Rejection	$\Delta V_i = 10 \text{ V}$ f = 120 Hz	54	60		dB
V _d	Dropout Voltage	$I_o = 1 A$ $T_j = 25 °C$ $\Delta V_O = 100 mV$		1.1		V
lsc	Short Circuit Current			1.5		А
Iscp	Short Circuit Peak Current	$T_j = 25 °C$		2.5		А

ELECTRICAL CHARACTERISTICS FOR L7912A (refer to the test circuits, $T_j = 0$ to 125 °C, $V_i = -19V$, $I_0 = 500$ mA, $C_i = 2.2 \mu$ F, $C_0 = 1 \mu$ F unless otherwise specified)

ELECTRICAL CHARACTERISTICS FOR L7915A (refer to the test circuits, $T_j = 0$ to 125 °C,

 $V_i = -23V$, $I_0 = 500$ mA, $C_i = 2.2 \mu$ F, $C_0 = 1 \mu$ F unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Vo	Output Voltage	$T_j = 25 °C$	-14.7	-15	-15.3	V
Vo	Output Voltage	$ I_o = -5 \text{ mA to -1 A} P_o \le 15 \text{ W} \\ V_i = -18.5 \text{ to -30 V} $	-14.4	-15	-15.6	V
ΔV_0^*	Line Regulation				300 150	mV mV
ΔV_{o}^{*}	Load Regulation	$ \begin{array}{ll} I_{o} = 5 \text{ to } 1500 \text{ mA} & T_{j} = 25 \ ^{o}\text{C} \\ I_{o} = 250 \text{ to } 750 \text{ mA} & T_{j} = 25 \ ^{o}\text{C} \end{array} $			300 150	mV mV
١ _d	Quiescent Current	$T_j = 25 °C$			3	mA
ΔI_d	Quiescent Current Change	I _o = 5 to 1000 mA			0.5	mA
ΔI_d	Quiescent Current Change	V _i = -18.5 to -30 V			1	mA
$\frac{\Delta V_o}{\Delta T}$	Output Voltage Drift	$I_o = 5 \text{ mA}$		-0.9		mV/ºC
e _N	Output Noise Voltage	$B = 10Hz$ to 100KHz $T_j = 25 °C$		250		μV
SVR	Supply Voltage Rejection	$\Delta V_i = 10 \text{ V} f = 120 \text{ Hz}$	54	60		dB
V _d	Dropout Voltage	$I_o = 1 A$ $T_j = 25 °C$ $\Delta V_O = 100 mV$		1.1		V
lsc	Short Circuit Current			1.3		A
Iscp	Short Circuit Peak Current	T _j = 25 °C		2.3		A

* Load and line regulation are specified at constant junction temperature. Changes in Vo due to heating effects must be taken into account separately. Pulce testing with low duty cycle is used.

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Vo	Output Voltage	$T_j = 25 °C$	-17.64	-18	-18.36	V
Vo	Output Voltage	$ I_o = -5 \text{ mA to } -1 \text{ A } P_o \le 15 \text{ W} $ $ V_i = -22 \text{ to } -33 \text{ V} $	-17.3	-18	-18.7	V
ΔV_0^*	Line Regulation				360 180	mV mV
ΔV_0^*	Load Regulation				360 180	m∨ mV
ld	Quiescent Current	$T_j = 25 °C$			3	mA
ΔI_d	Quiescent Current Change	I _o = 5 to 1000 mA			0.5	mA
ΔI_d	Quiescent Current Change	V _i = -22 to -33 V			1	mA
$\frac{\Delta V_o}{\Delta T}$	Output Voltage Drift	$I_0 = 5 \text{ mA}$		-1		mV/°C
e _N	Output Noise Voltage	$B = 10Hz$ to 100KHz $T_j = 25 ^{\circ}C$		300		μV
SVR	Supply Voltage Rejection	$\Delta V_i = 10 \text{ V} \text{ f} = 120 \text{ Hz}$	54	60		dB
V _d	Dropout Voltage	$I_o = 1 A$ $T_j = 25 °C$ $\Delta V_O = 100 mV$		1.1		V
l _{sc}	Short Circuit Current			1.1		А
Iscp	Short Circuit Peak Current	$T_j = 25 °C$		2.2		А

ELECTRICAL CHARACTERISTICS FOR L7918A (refer to the test circuits, $T_j = 0$ to 125 °C,

 $V_i = -27V$, $I_o = 500$ mA, $C_i = 2.2 \,\mu$ F, $C_o = 1 \,\mu$ F unless otherwise specified)

ELECTRICAL CHARACTERISTICS FOR L7920A (refer to the test circuits, $T_j = 0$ to 125 °C,

 $V_i = -29V$, $I_0 = 500$ mA, $C_i = 2.2 \mu F$, $C_0 = 1 \mu F$ unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Vo	Output Voltage	$T_j = 25 ^{\circ}C$	-19.6	-20	-20.4	V
Vo	Output Voltage	$ I_{o} = -5 \text{ mA to -1 A} P_{o} \le 15 \text{ W} \\ V_{i} = -24 \text{ to -35 V} $	-19.2	-20	-20.8	V
ΔV_0^*	Line Regulation				400 200	mV mV
ΔV_{o}^{*}	Load Regulation				400 200	mV mV
ld	Quiescent Current	$T_j = 25 °C$			3	mA
ΔI_d	Quiescent Current Change	I _o = 5 to 1000 mA			0.5	mA
ΔI_d	Quiescent Current Change	V _i = -24 to -35 V			1	mA
$\frac{\Delta V_o}{\Delta T}$	Output Voltage Drift	I _o = 5 mA		-1.1		mV/ºC
e _N	Output Noise Voltage	$B = 10$ Hz to 100KHz $T_j = 25 ^{\circ}C$		350		μV
SVR	Supply Voltage Rejection	$\Delta V_i = 10 V$ f = 120 Hz	54	60		dB
V _d	Dropout Voltage	$I_o = 1 A$ $T_j = 25 \ ^oC$ $\Delta V_O = 100 \ mV$		1.1		V
lsc	Short Circuit Current			0.9		А
Iscp	Short Circuit Peak Current	$T_j = 25 °C$		2.2		A

 * Load and line regulation are specified at constant junction temperature. Changes in V_o due to heating effects must be taken into account separately. Pulce testing with low duty cycle is used.

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Vo	Output Voltage	$T_j = 25 °C$	-21.5	-22	-22.4	V
Vo	Output Voltage		-21.1	-22	-22.8	V
ΔV_0^*	Line Regulation				440 220	mV mV
ΔV_0^*	Load Regulation				440 220	mV mV
ld	Quiescent Current	$T_j = 25 °C$			3	mA
ΔI_d	Quiescent Current Change	I _o = 5 to 1000 mA			0.5	mA
ΔI_d	Quiescent Current Change	V _i = -26 to -37 V			1	mA
$\frac{\Delta V_o}{\Delta T}$	Output Voltage Drift	I _o = 5 mA		-1.1		mV/°C
e _N	Output Noise Voltage	$B = 10$ Hz to 100KHz $T_j = 25 $ °C		375		μV
SVR	Supply Voltage Rejection	$\Delta V_i = 10 \text{ V}$ f = 120 Hz	54	60		dB
V _d	Dropout Voltage	$I_o = 1 A$ $T_j = 25 °C$ $\Delta V_O = 100 mV$		1.1		V
lsc	Short Circuit Current			1.1		А
Iscp	Short Circuit Peak Current	$T_j = 25 °C$		2.2		А

ELECTRICAL CHARACTERISTICS FOR L7922A (refer to the test circuits, $T_j = 0$ to 125 °C, $V_i = -31V$, $I_0 = 500$ mA, $C_i = 2.2 \mu$ F, $C_0 = 1 \mu$ F unless otherwise specified)

ELECTRICAL CHARACTERISTICS FOR L7924A (refer to the test circuits, $T_j = 0$ to 125 °C,

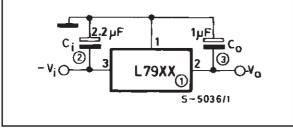
 $V_i = -33V$, $I_0 = 500$ mA, $C_i = 2.2 \mu$ F, $C_0 = 1 \mu$ F unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Vo	Output Voltage	$T_j = 25 °C$	-23.5	-24	-24.5	V
Vo	Output Voltage	$ I_o = -5 \text{ mA to -1 A} P_o \leq 15 \text{ W} \\ V_i = -27 \text{ to -38 V} $	-23	-24	-25	V
ΔV_0^*	Line Regulation				480 240	mV mV
ΔV_{o}^{*}	Load Regulation				480 240	mV mV
١ _d	Quiescent Current	$T_j = 25 \ ^{\circ}C$			3	mA
ΔI_d	Quiescent Current Change	I _o = 5 to 1000 mA			0.5	mA
ΔI_d	Quiescent Current Change	V _i = -27 to -38 V			1	mA
$\frac{\Delta V_o}{\Delta T}$	Output Voltage Drift	$I_o = 5 \text{ mA}$		-1		mV/ºC
e _N	Output Noise Voltage	$B = 10Hz$ to 100KHz $T_j = 25 °C$		400		μV
SVR	Supply Voltage Rejection	$\Delta V_i = 10 V$ f = 120 Hz	54	60		dB
Vd	Dropout Voltage	$I_o = 1 A$ $T_j = 25 °C$ $\Delta V_O = 100 mV$		1.1		V
lsc	Short Circuit Current			1.1		А
Iscp	Short Circuit Peak Current	$T_j = 25 ^{\circ}C$		2.2		А

 * Load and line regulation are specified at constant junction temperature. Changes in V_o due to heating effects must be taken into account separately. Pulce testing with low duty cycle is used.

APPLICATION INFORMATION

Figure 1 : Fixed Output Regulator.

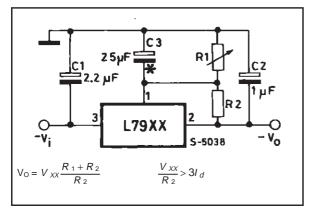


Notes :

1. To specify an output voltage, substitute voltage value for "XX". 2. Required for stability. For value given, capacitor must be solid tantalum. If aluminium electrolitics are used, at least ten times value should be selected. C₁ is required if regulator is located an appreciable distance from power supply filter.

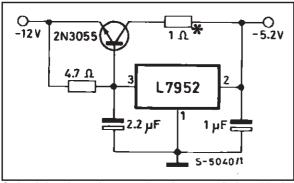
3. To improve transient response. If large capacitors are used, a high current diode from input to output (1N4001 or similar) should be introduced to protect the device from momentary input short circuit.

Figure 3 : Circuit for Increasing Output Voltage.



C3 Optional for improved transient response and ripple rejection.

Figure 5 : Typical ECL System Power Supply (- 5.2V/4A).



Optional dropping resistor to reduce the power dissipated in the boost transistor.

- 20 VO - 3 L7915 2 - 0-15V

0.33µF

2.2 µF

Against potential latch-up problems.

Figure 2 : Split Power Supply (± 15V/1A).

+20VO

L7815

3

0.1 µF

1µF

O +15 V

1N4001

1N4001

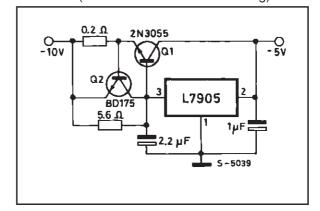
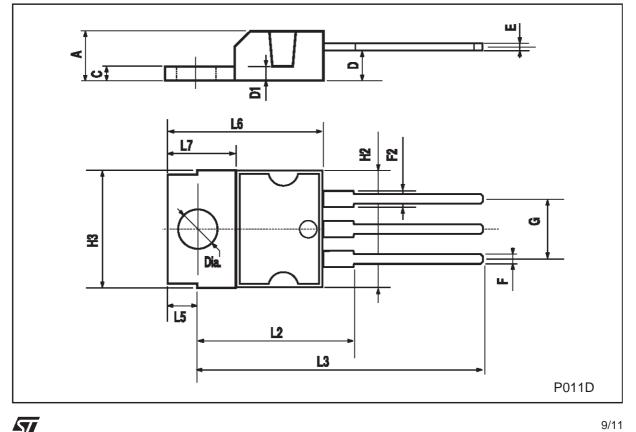


Figure 4 : High Current Negative Regulator (– 5V/4A with 5A current limiting).

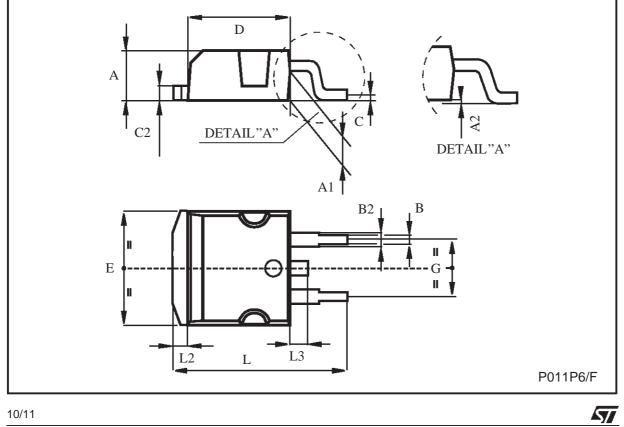
DIM.	mm			inch		
DINI.	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
А			4.8			0.189
С			1.37			0.054
D	2.4		2.8	0.094		0.110
D1	1.2		1.35	0.047		0.053
E	0.35		0.55	0.014		0.022
F	0.61		0.94	0.024		0.037
F2	1.15		1.4	0.045		0.055
G	4.95	5.08	5.21	0.195	0.200	0.205
H2			10.4			0.409
H3	10.05		10.4	0.396		0.409
L2		16.2			0.638	
L3	26.3	26.7	27.1	1.035	1.051	1.067
L5	2.6		3	0.102		0.118
L6	15.1		15.8	0.594		0.622
L7	6		6.6	0.236		0.260
Dia.	3.65		3.85	0.144		0.152





DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
А	4.4		4.6	0.173		0.181
A1	2.49		2.69	0.098		0.106
В	0.7		0.93	0.027		0.036
B2	1.14		1.7	0.044		0.067
С	0.45		0.6	0.017		0.023
C2	1.23		1.36	0.048		0.053
D	8.95		9.35	0.352		0.368
E	10		10.4	0.393		0.409
G	4.88		5.28	0.192		0.208
L	15		15.85	0.590		0.624
L2	1.27		1.4	0.050		0.055





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