

3-Pin Reset Monitors For 5V Systems

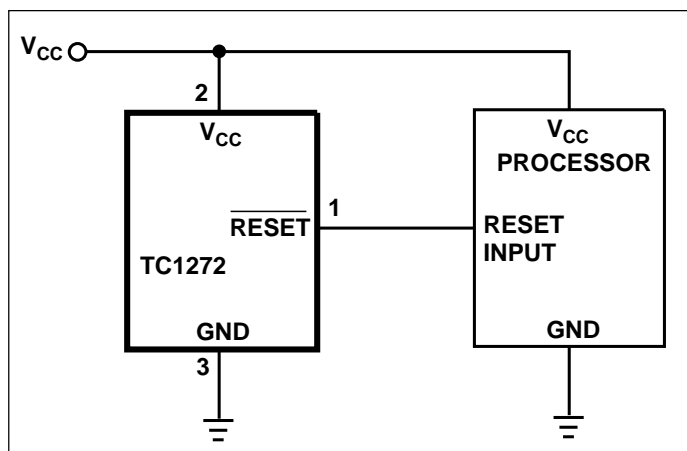
FEATURES

- Precision V_{CC} Monitor for 5.0V Systems
- 100 msec Guaranteed Minimum $\overline{\text{RESET}}$, RESET Output Duration
- Output Guaranteed to $V_{CC} = 1.2V$
- V_{CC} Transient Immunity
- 3-Pin SOT-23B Package
- No External Components

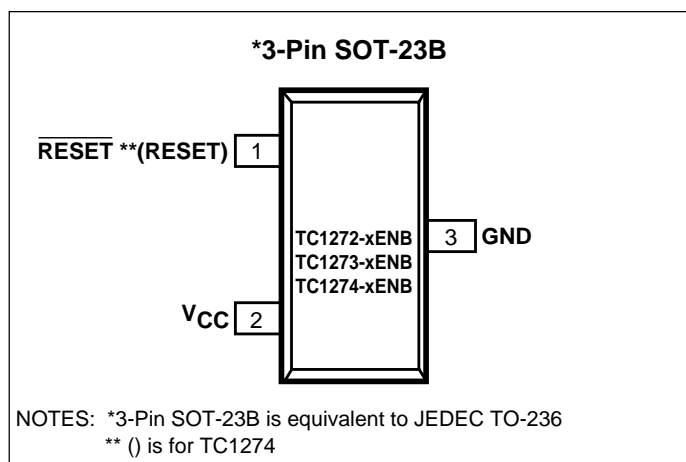
TYPICAL APPLICATIONS

- Computers
- Embedded Systems
- Battery Powered Equipment
- Critical μP Power Supply Monitoring

TYPICAL OPERATING CIRCUIT



PIN CONFIGURATION



GENERAL DESCRIPTION

The TC1272, TC1273 and TC1274 are cost-effective system supervisor circuits designed to monitor V_{CC} in digital systems and provide a reset signal to the host processor when necessary. No external components are required.

The reset output is driven active within 20 μ sec of V_{CC} falling through the reset voltage threshold. Reset is maintained active for a minimum of 100msec after V_{CC} rises above the reset threshold. The TC1274 has an active-high RESET output while the TC1272 and TC1273 have an active-low $\overline{\text{RESET}}$ output. The TC1272 and TC1274 each have a complimentary output while the TC1273 has an open drain output. The output of the TC1272 and TC1273 is guaranteed valid down to $V_{CC} = 1.2V$. The TC1274 is guaranteed valid down to $V_{CC} = 1.8V$. All three devices are available in a 3-Pin SOT-23B package.

The TC1272/3/4 are optimized to reject fast transient glitches on the V_{CC} line.

ORDERING INFORMATION

Part No.	Order	Package	Temp. Range
TC1272-xENB	Complimentary	3-Pin SOT-23B	-40°C to +85°C
TC1273-xENB	Open Drain	3-Pin SOT-23B	-40°C to +85°C
TC1274-xENB	Complimentary	3-Pin SOT-23B	-40°C to +85°C

NOTE: The "x" denotes a suffix for V_{CC} threshold - see table below.

Suffix	Reset V_{CC} Threshold (V)
5	4.62
10	4.37
15	4.12

TC1272 TC1273 TC1274

ABSOLUTE MAXIMUM RATINGS*

Supply Voltage (V_{CC} to GND	+6.0V
RESET, RESET	- 0.3V to ($V_{CC} + 0.3V$)
Input Current, V_{CC}	20mA
Output Current, RESET, RESET	20mA
Operating Temperature Range	- 40°C to +85°C

Power Dissipation ($T_A \leq 70^\circ\text{C}$)

3-Pin SOT-23B (derate 4mW/°C above +70°C) 230mW
Storage Temperature Range - 65°C to +150°C
Lead Temperature (Soldering, 10 sec) +260°C

*This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of the specifications is not implied. Exposure to Absolute Maximum Rating Conditions for extended periods may affect device reliability.

RECOMMENDED DC OPERATING CONDITIONS: $T_A = -40^\circ\text{C}$ to $+85^\circ\text{C}$ unless otherwise specified. Typical values apply at $T_A = +25^\circ\text{C}$.

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
V_{CC}	Supply Voltage(TC1272, TC1273) (TC1274)	(note 1)	1.2	—	5.5	V
			1.8	—	5.5	V

DC ELECTRICAL CHARACTERISTICS: $T_A = -40^\circ\text{C}$ to $+85^\circ\text{C}$ unless otherwise specified. Typical values apply at $T_A = +25^\circ\text{C}$.

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
V_{OH}	Output Voltage @ 0-500 μA (TC1272, TC1274)	(Note 1)	$V_{CC} - 0.5V$	$V_{CC} - 0.1V$	—	V
I_{OH}	Output Current @2.4 Volts $V_{CC} = 5V$ (TC1272) $V_{CC} = 4V$ (TC1274)	(Note 2)	—	10	—	mA
			—	8	—	mA
I_{OL}	Output Current @0.4 Volts	(Notes 2,5)	+10	30	—	mA
I_{CC}	Operating Current $V_{CC} < 5.5V$ (TC1272, TC1274) $V_{CCTP} < V_{CC} < 5.5V$ (TC1273) $V_{CC} < V_{CCTP}$ (TC1273)	(Note 3)	—	17	40	μA
		(Note 3)	—	17	40	μA
		(Note 3)	—	700	1200	μA
		(Note 3)	—	700	1200	μA
V_{CCTP-5}	V_{CC} Trip Point (TC1272/3/4-5)	(Note 1)	4.50	4.62	4.75	V
$V_{CCTP-10}$	V_{CC} Trip Point (TC1272/3/4-10)	(Note 1)	4.25	4.37	4.49	V
$V_{CCTP-15}$	V_{CC} Trip Point (TC1272/3/4-15)	(Note 1)	4.00	4.12	4.24	V
C_{OUT}	Output Capacitance		—	9	—	pF
R_P	Internal Pull-Up Resistor (TC1273)		3	6	9	k Ω

AC ELECTRICAL CHARACTERISTICS: $T_A = -40^\circ\text{C}$ to $+85^\circ\text{C}$ unless otherwise specified. Typical values apply at $T_A = +25^\circ\text{C}$.

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
t_{RST}	RESET Active Time		100	200	300	msec
t_{RPD1}	V_{CC} Detect to RESET (TC1272, TC1273)	$V_{CC(LOW)} = 1V$ (Figure 2)	—	20	50	μsec
t_{RPD2}	V_{CC} Detect to RESET (TC1274)	$V_{CC(LOW)} = 1V$ (Figure 4)	—	20	50	μsec
t_F	V_{CC} Slew Rate (V_{CCTP} (MAX) to V_{CCTP} (MIN))	(Figures 2, 4)	300	—	—	μsec
t_R	V_{CC} Slew Rate (V_{CCTP} (MIN) to V_{CCTP} (MAX))	(Figures 1, 3)	0	—	—	nsec
t_{RPU1}	V_{CC} Detect to RESET (TC1272, TC1273)	(Note 4, Figure 1)	100	200	300	msec
t_{RPU2}	V_{CC} Detect to RESET (TC1274)	(Note 4, Figure 3)	100	200	300	msec

- NOTES:**
1. All voltages are referenced to ground.
 2. Measured with $V_{CC} \geq 2.7$ volts.
 3. Measured with RESET output open for TC1272/3; measured with RESET output open for TC1274.
 4. $t_R = 5 \mu\text{sec}$.
 5. A 1k Ω external resistor may be required in some applications for proper operation of the microprocessor reset control circuit when using the TC1273.

3-Pin Monitors For 5V Systems

TC1272
TC1273
TC1274

PIN DESCRIPTION

Pin No. (3-Pin SOT-23B)	Symbol	Description
1	RESET (TC1272, TC1273)	RESET output remains low while V_{CC} is below the reset voltage threshold, and for 200msec (100msec min.) after V_{CC} rises above reset threshold. The output stage of the TC1272 is complimentary. The output stage of the TC1273 is open drain.
1	RESET (TC1274)	RESET output remains high while V_{CC} is below the reset voltage threshold, and for 200msec (100msec min.) after V_{CC} rises above reset threshold. The output stage of the TC1274 is complimentary.
2	V_{CC}	Supply voltage (1.2V to 5.5V TC1272 and TC1273, 1.8V to 5.5V TC1274)
3	GND	Ground

APPLICATION INFORMATION

Operation - Power Monitor

The TC1272, TC1273, TC1274 provide the function of detecting out-of-tolerance power supply conditions and warning a processor-based system of impending power failure. When V_{CC} is detected as out-of-tolerance, the RESET signal is asserted. On power-up, RESET is kept active for approximately 200 ms after the power supply has reached the selected tolerance. This allows the power supply and microprocessor to stabilize before RESET is released.

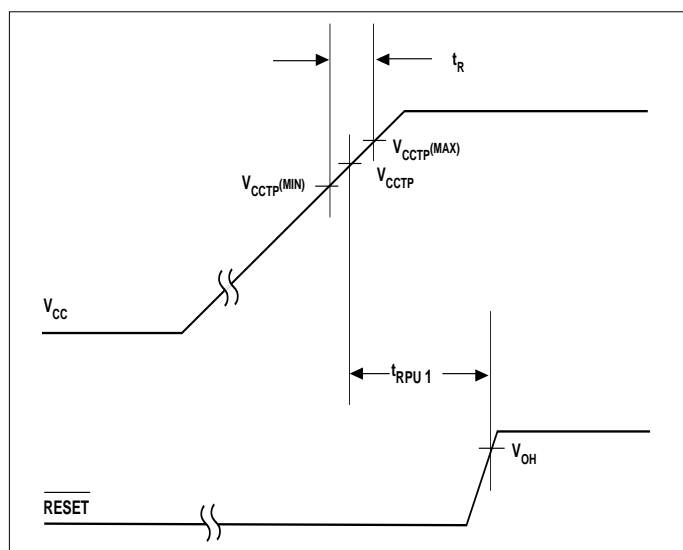


Figure 1. Timing Diagram: Power Up (TC1272, TC1273)

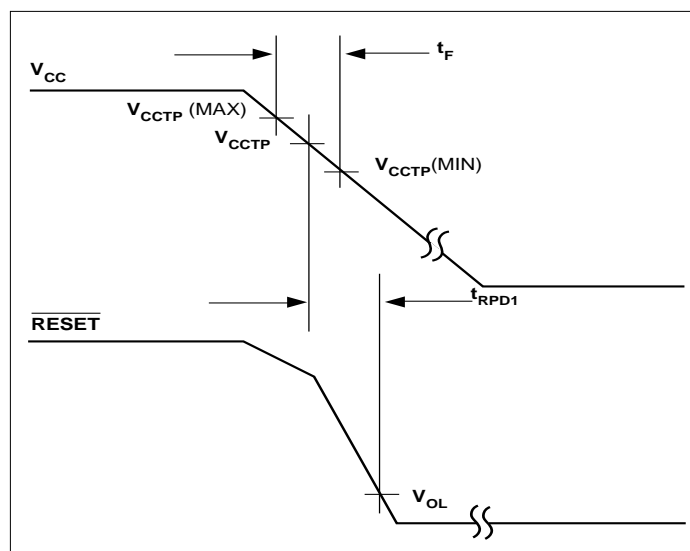


Figure 2. Timing Diagram: Power Down (TC1272, TC1273)

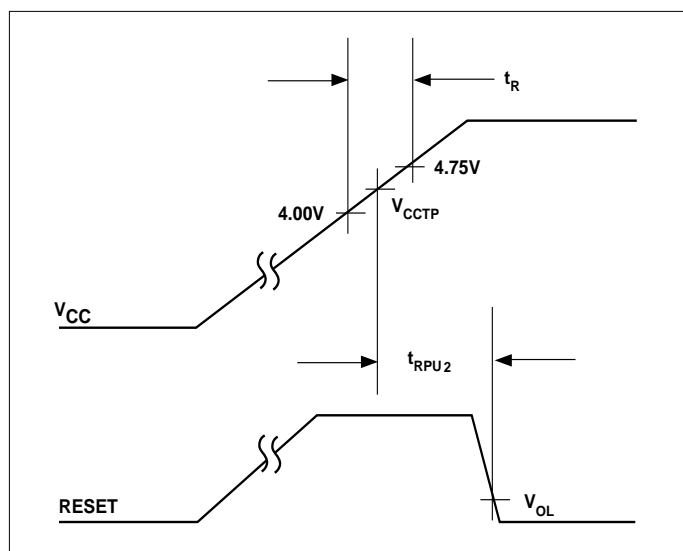


Figure 3. Timing Diagram: Power Up (TC1274)

TC1272
TC1273
TC1274

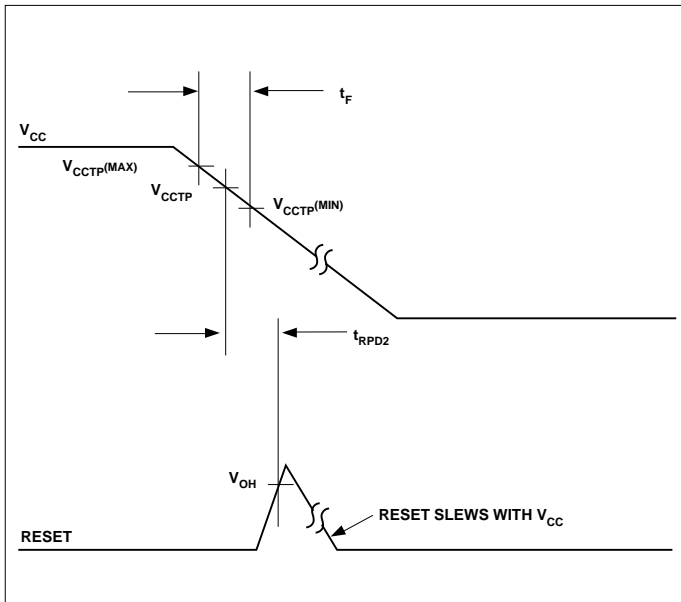


Figure 4. Timing Diagram: Power Down (TC1274)

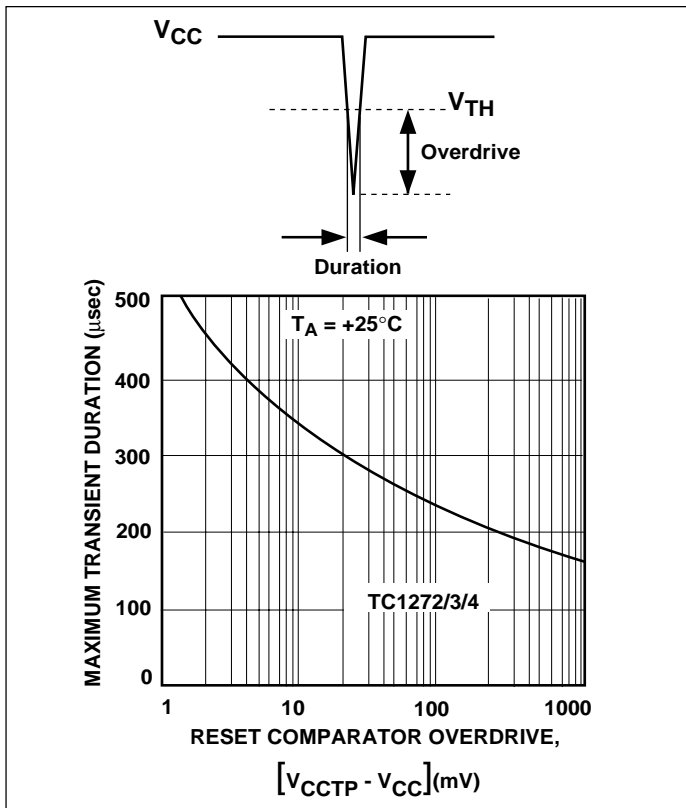


Figure 5. Maximum Transient Duration vs. Overdrive for Glitch Rejection at 25°C

VCC Transient Rejection

The TC1272/3/4 provides accurate V_{CC} monitoring and reset timing during power-up, power-down, and brownout/sag conditions, and rejects negative-going transients (glitches) on the power supply line. Figure 5 shows the maximum transient duration vs. maximum negative excursion (overdrive) for glitch rejection. Any combination of duration and overdrive which lies **under** the curve will **not** generate a reset signal. Combinations above the curve are detected as a brownout or power-down. Transient immunity can be improved by adding a capacitor in close proximity to the V_{CC} pin of the TC1272/3/4.

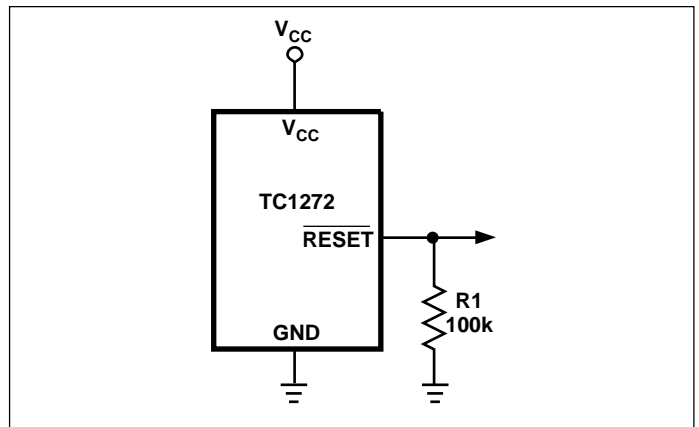
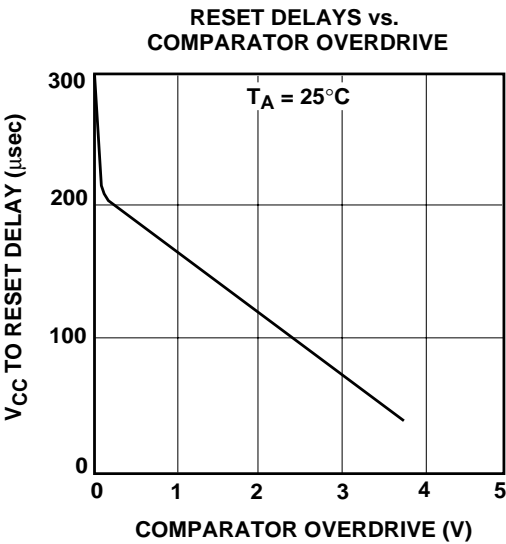
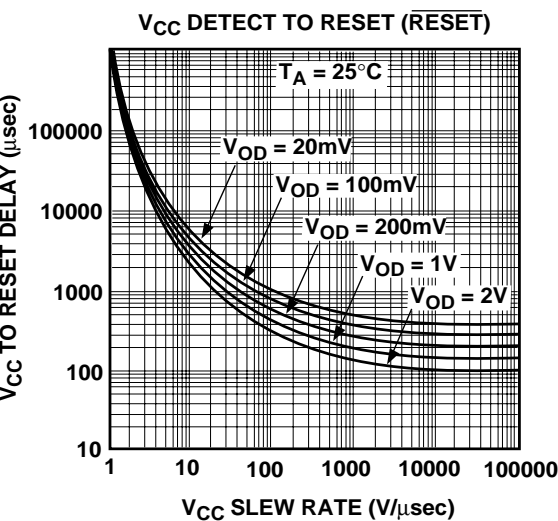


Figure 6. Ensuring $\overline{\text{RESET}}$ Valid to $V_{CC} = 0V$

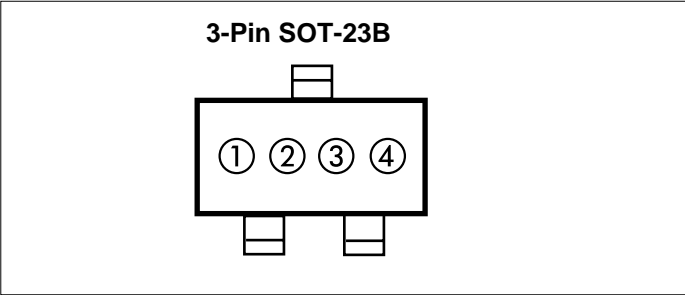
$\overline{\text{RESET}}$ Signal Integrity During Power-Down

The TC1272 $\overline{\text{RESET}}$ output is valid to $V_{CC} = 1.2V$. Below this voltage the output becomes an "open circuit" and does not sink current. This means CMOS logic inputs to the μP will be floating at an undetermined voltage. Most digital systems are completely shutdown well above this voltage. However, in situations where $\overline{\text{RESET}}$ must be maintained valid to $V_{CC} = 0V$, a pull-down resistor must be connected from $\overline{\text{RESET}}$ to ground to discharge stray capacitances and hold the output low (Figure 6). This resistor value, though not critical, should be chosen such that it does not appreciably load $\overline{\text{RESET}}$ under normal operation (100kΩ will be suitable for most applications). Similarly, a pull-up resistor to V_{CC} is required for the TC1274 to ensure a valid high $\overline{\text{RESET}}$ for V_{CC} below 1.8V.

TYPICAL CHARACTERISTICS



MARKINGS



PART NUMBERS AND PART MARKINGS

① & ② = part number code + temperature range and voltage

TC1272 (V)	Code
4.62	X1
4.37	X2
4.12	X3

TC1273 (V)	Code
4.62	Y1
4.37	Y2
4.12	Y3

TC1274 (V)	Code
4.62	Z1
4.37	Z2
4.12	Z3

ex: 1272-5 = (X)①○○○

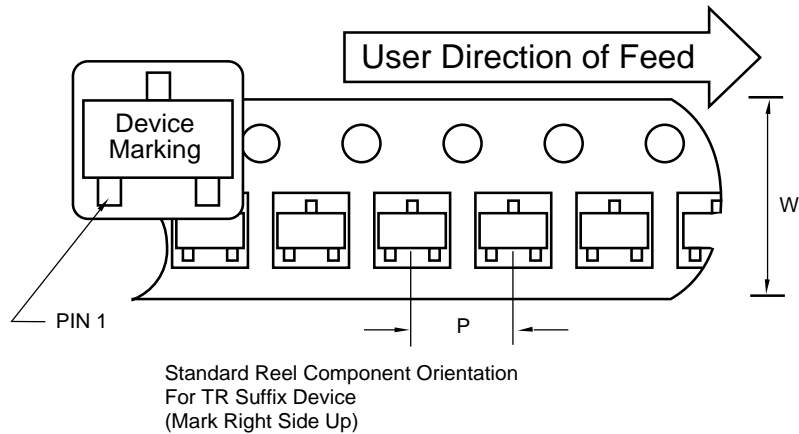
③ represents year and quarter code

④ represents lot ID number

TC1272
TC1273
TC1274

TAPING FORM

Component Taping Orientation for 3-Pin SOT-23B (JEDEC TO-236) Devices

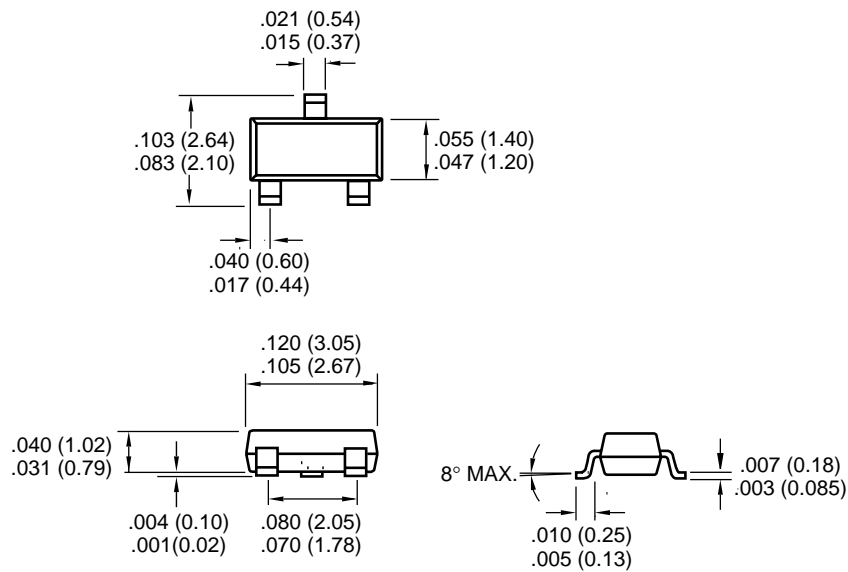


Carrier Tape, Number of Components Per Reel and Reel Size

Package	Carrier Width (W)	Pitch (P)	Part Per Full Reel	Reel Size
3-Pin SOT-23B	8 mm	4 mm	3000	7 in

PACKAGE DIMENSIONS

3-Pin SOT-23B (JEDEC TO-236)





WORLDWIDE SALES AND SERVICE

AMERICAS

Corporate Office

2355 West Chandler Blvd.
Chandler, AZ 85224-6199
Tel: 480-792-7200 Fax: 480-792-7277
Technical Support: 480-792-7627
Web Address: <http://www.microchip.com>

Rocky Mountain

2355 West Chandler Blvd.
Chandler, AZ 85224-6199
Tel: 480-792-7966 Fax: 480-792-7456

Atlanta

500 Sugar Mill Road, Suite 200B
Atlanta, GA 30350
Tel: 770-640-0034 Fax: 770-640-0307

Austin

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Suite A-201
Austin, TX 78759
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Boston

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Westford, MA 01886
Tel: 978-692-3848 Fax: 978-692-3821

Boston

Analog Product Sales
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Concord, MA 01742
Tel: 978-371-6400 Fax: 978-371-0050

Chicago

333 Pierce Road, Suite 180
Itasca, IL 60143
Tel: 630-285-0071 Fax: 630-285-0075

Dallas

4570 Westgrove Drive, Suite 160
Addison, TX 75001
Tel: 972-818-7423 Fax: 972-818-2924

Dayton

Two Prestige Place, Suite 130
Miamisburg, OH 45342
Tel: 937-291-1654 Fax: 937-291-9175

Detroit

Tri-Atria Office Building
32255 Northwestern Highway, Suite 190
Farmington Hills, MI 48334
Tel: 248-538-2250 Fax: 248-538-2260

Los Angeles

18201 Von Karman, Suite 1090
Irvine, CA 92612
Tel: 949-263-1888 Fax: 949-263-1338

Mountain View

Analog Product Sales
1300 Terra Bella Avenue
Mountain View, CA 94043-1836
Tel: 650-968-9241 Fax: 650-967-1590

New York

150 Motor Parkway, Suite 202
Hauppauge, NY 11788
Tel: 631-273-5305 Fax: 631-273-5335

San Jose

Microchip Technology Inc.
2107 North First Street, Suite 590
San Jose, CA 95131
Tel: 408-436-7950 Fax: 408-436-7955

Toronto

6285 Northam Drive, Suite 108
Mississauga, Ontario L4V 1X5, Canada
Tel: 905-673-0699 Fax: 905-673-6509

ASIA/PACIFIC

China - Beijing

Microchip Technology Beijing Office
Unit 915
New China Hong Kong Manhattan Bldg.
No. 6 Chaoyangmen Beidajie
Beijing, 100027, No. China
Tel: 86-10-85282100 Fax: 86-10-85282104

China - Shanghai

Microchip Technology Shanghai Office
Room 701, Bldg. B
Far East International Plaza
No. 317 Xian Xia Road
Shanghai, 200051
Tel: 86-21-6275-5700 Fax: 86-21-6275-5060

Hong Kong

Microchip Asia Pacific
RM 2101, Tower 2, Metroplaza
223 Hing Fong Road
Kwai Fong, N.T., Hong Kong
Tel: 852-2401-1200 Fax: 852-2401-3431

India

Microchip Technology Inc.
India Liaison Office
Divyasree Chambers
1 Floor, Wing A (A3/A4)
No. 11, O'Shaughnessy Road
Bangalore, 560 025, India
Tel: 91-80-2290061 Fax: 91-80-2290062

Japan

Microchip Technology Intl. Inc.
Benex S-1 6F
3-18-20, Shinyokohama
Kohoku-Ku, Yokohama-shi
Kanagawa, 222-0033, Japan
Tel: 81-45-471-6166 Fax: 81-45-471-6122

Korea

Microchip Technology Korea
168-1, Youngbo Bldg. 3 Floor
Samsung-Dong, Kangnam-Ku
Seoul, Korea
Tel: 82-2-554-7200 Fax: 82-2-558-5934

ASIA/PACIFIC (continued)

Singapore

Microchip Technology Singapore Pte Ltd.
200 Middle Road
#07-02 Prime Centre
Singapore, 188980
Tel: 65-334-8870 Fax: 65-334-8850

Taiwan

Microchip Technology Taiwan
11F-3, No. 207
Tung Hua North Road
Taipei, 105, Taiwan
Tel: 886-2-2717-7175 Fax: 886-2-2545-0139

EUROPE

Australia

Microchip Technology Australia Pty Ltd
Suite 22, 41 Rawson Street
Epping 2121, NSW
Australia
Tel: 61-2-9868-6733 Fax: 61-2-9868-6755

Denmark

Microchip Technology Denmark ApS
Regus Business Centre
Lautrup høj 1-3
Ballerup DK-2750 Denmark
Tel: 45 4420 9895 Fax: 45 4420 9910

France

Arizona Microchip Technology SARL
Parc d'Activite du Moulin de Massy
43 Rue du Saule Trappu
Batiment A - 1er Etage
91300 Massy, France
Tel: 33-1-69-53-63-20 Fax: 33-1-69-30-90-79

Germany

Arizona Microchip Technology GmbH
Gustav-Heinemann Ring 125
D-81739 Munich, Germany
Tel: 49-89-627-144 0 Fax: 49-89-627-144-44

Germany

Analog Product Sales
Lochamer Strasse 13
D-82152 Martinsried, Germany
Tel: 49-89-895650-0 Fax: 49-89-895650-22

Italy

Arizona Microchip Technology SRL
Centro Direzionale Colleoni
Palazzo Taurus 1 V. Le Colleoni 1
20041 Agrate Brianza
Milan, Italy
Tel: 39-039-65791-1 Fax: 39-039-6899883

United Kingdom

Arizona Microchip Technology Ltd.
505 Eskdale Road
Winnersh Triangle
Wokingham
Berkshire, England RG41 5TU
Tel: 44 118 921 5869 Fax: 44-118 921-5820

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