TOSHIBA BIPOLAR DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

# **TD62308APA**

#### 4CH LOW INPUT ACTIVE HIGH-CURRENT DARLINGTON SINK DRIVER

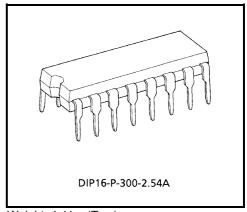
The TD62308APA is non-inverting transistor array which is comprised of four NPN darlington output stages and PNP input stages.

This device is low level input active driver and is suitable for operation with TTL,  $5\ V\ CMOS$  and  $5\ V\ Microprocessor$  which have sink current output drivers.

Applications include relay, hammer, lamp and stepping moter drivers.

#### **FEATURES**

- Output current (single output) 1.5 A (Max.)
- High sustaining voltage output 50 V (Min.)
- Output clamp diodes
- Input compatible with TTL and 5 V CMOS
- Low level active inputs
- Standard supply voltage
- Two VCC terminals VCC1, VCC2 (Separated)
- GND and SUB terminal = Heat Sink
- Package type-APA: DIP-16 pin

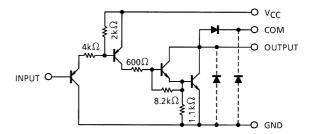


Weight: 1.11 g (Typ.)

### **PIN CONNECTION (TOP VIEW)**

#### HEAT SINK & GND COM $\cap 4$ IΔ 13 03 COM 14 13 11 15 10 9 3 6 8 01 11 12 02 $V_{CC2}$ $V_{CC1}$ HEAT SINK & GND

### **SCHEMATICS (EACH DRIVER)**



Note: The input and output parasitic diodes cannot be used as clamp diodes.



### MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT	
Supply Voltage	V <sub>CC</sub>	-0.5~10	V	
Output Sustaining Voltage	V <sub>CE</sub> (SUS)	-0.5~50	V	
Output Current	I <sub>OUT</sub>	1.5	A / ch	
Input Current	I <sub>IN</sub>	-10	mA	
Input Voltage	V <sub>IN</sub>	-0.5~30	٧	
Clamp Diode Reverse Voltage	$V_{R}$	50	٧	
Clamp Diode Forward Current	lF	1.5	Α	
Power Dissipation	P <sub>D</sub> (Note)	1.47 / 2.7	W	
Operating Temperature	T <sub>opr</sub>	-40~85	°C	
Storage Temperature	T <sub>stg</sub>	-55~150	°C	

Note: On Glass Epoxy PCB (50 × 50 × 1.6 mm Cu 50%)

### RECOMMENDED OPERATING CONDITIONS (Ta = $-40 \sim 85$ °C)

CHARACTERISTIC		SYMBOL	CONDITION		MIN	TYP.	MAX	UNIT	
Supply Voltage		V <sub>CC</sub>			4.5	_	5.0	V	
Output Sustaining Voltage		V <sub>CE</sub> (SUS)			0	_	50	V	
Output Current			DC 1 Circuit Ta = 25°C		0	_	1250		
		I <sub>OUT</sub>	$T_{pw} \le 25 \text{ ms}$ 4 Circuits $Ta = 85^{\circ}C$ $T_j = 120^{\circ}C$	Duty = 10%	0	_	1250	mA / ch	
				Duty = 50%	0	_	700		
		V <sub>IN</sub>	•		0	_	25		
Input Voltage	Output On	V <sub>IN</sub> (ON)			0	_	V <sub>CC</sub> -3.6	V	
	Output Off	V <sub>IN</sub> (OFF)			V <sub>CC</sub> -1.0	_	V <sub>CC</sub>		
Clamp Diode Reverse Voltage		V <sub>R</sub>			_	_	50	V	
Clamp Diode Forward Current		I <sub>F</sub>			_	_	1.25	Α	
Power Dissipation		PD	Ta = 85°C (Note)		1	_	1.4	W	

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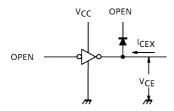
Note: On Glass Epoxy (50 × 50 × 1.6 mm Cu 50%)

### **ELECTRICAL CHARACTERISTICS (Ta = 25°C)**

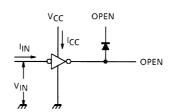
CHARACTERISTIC		SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN	TYP.	MAX	UNIT	
Output Leakage Current		I <sub>CEX</sub>	1	V <sub>CE</sub> = 50 V, Ta = 25°C	_	_	50	μΑ	
				V <sub>CE</sub> = 50 V, Ta = 85°C	_	_	100		
Output Saturation Voltage		V <sub>CE (sat)</sub>	3	I <sub>OUT</sub> = 1.25 A	_	_	1.8	V	
				I <sub>OUT</sub> = 0.7 A	_	_	1.3		
Input Voltage	"H" Level	V <sub>IH</sub>			V <sub>CC</sub> −1.6	_	25	- V	
	"L" Level	V <sub>IL</sub>			_	_	V <sub>CC</sub> -3.6		
Input Current	"H" Level	I <sub>IH</sub>	- 2		_	_	10	μΑ	
	"L" Level	I <sub>IL</sub>			_	-0.05	-0.36	mA	
Clamp Diode Reverse Current		I <sub>R</sub>	4	V <sub>R</sub> = 50 V, Ta = 25°C	_	_	50	μΑ	
				V <sub>R</sub> = 50 V, Ta = 85°C	_	_	100		
Clamp Diode Forward Voltage		V <sub>F</sub>	5	I <sub>F</sub> = 1.25 A	_	1.5	2.0	V	
Supply Current	Output On	I <sub>CC</sub> (ON)	- 2	V <sub>CC</sub> = 5.5 V, V <sub>IN</sub> = 0 V	_	8.5	12.5	mA / ch	
	Output Off	I <sub>CC</sub> (OFF)		$V_{CC}$ = 5.5 V, $V_{IN}$ = $V_{CC}$	_	_	10	μA	
Turn-On Delay		t <sub>ON</sub>	6	$C_L$ = 15 pF, $V_{OUT}$ = 50 V $R_L$ = 40 $\Omega$	_	0.2	_	- µs	
Turn-Off Delay		t <sub>OFF</sub>			_	5.0	_		

### **TEST CIRCUIT**

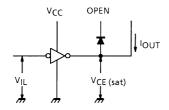
### 1. ICEX



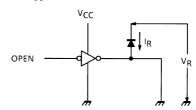
## 2. Icc



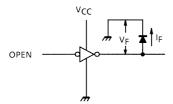
## 3. V<sub>CE (sat)</sub>



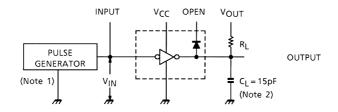
### 4. I<sub>R</sub>

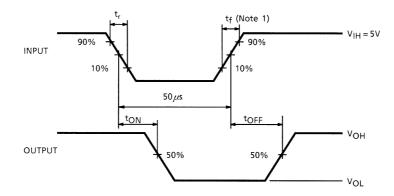


### 5. V<sub>F</sub>



#### 6. ton, toff





Note 1: Pulse width 50 µs, duty cycle 10%

Output impedance 50  $\Omega$  tr  $\leq$  5 ns, tf  $\leq$  10 ns

Note 2: C<sub>L</sub> includes probe and jig capacitance.

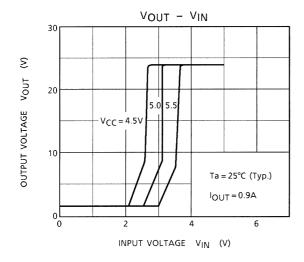
### **PRECAUTIONS for USING**

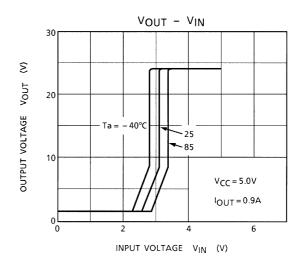
This IC does not include built-in protection circuits for excess current or overvoltage.

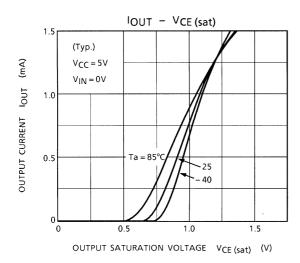
If this IC is subjected to excess current or overvoltage, it may be destroyed.

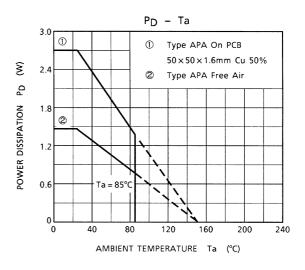
Hence, the utmost care must be taken when systems which incorporate this IC are designed.

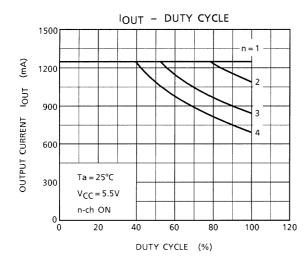
Utmost care is necessary in the design of the output line, VCC, COMMON and GND line since IC may be destroyed due to short–circuit between outputs, air contamination fault, or fault by improper grounding.

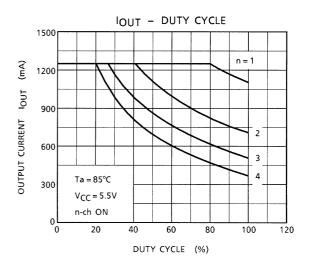






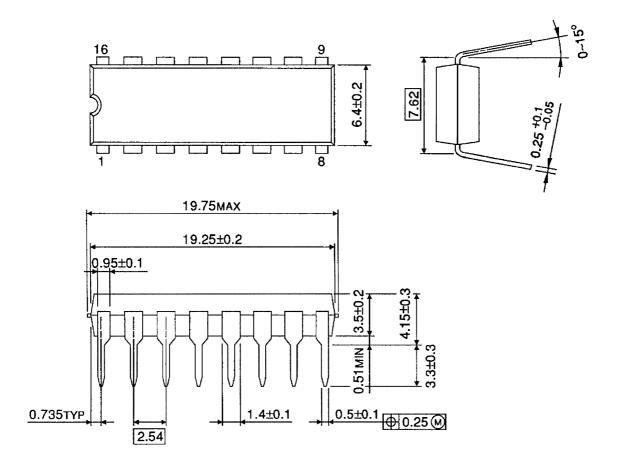






### **PACKAGE DIMENSIONS**

DIP16-P-300-2.54A Unit: mm



Weight: 1.11 g (Typ.)

### RESTRICTIONS ON PRODUCT USE

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