TOSHIBA BI-DIRECTIONAL TRIODE THYRISTOR ILICON PLANAR TYPE

# SM25GZ51,SM25JZ51

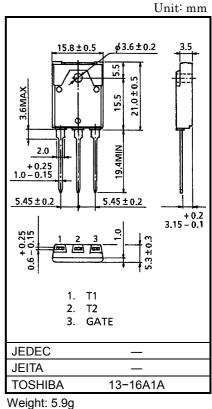
## AC POWER CONTROL APPLICATIONS

- Repetitive Peak Off-State Voltage : V<sub>DRM</sub> = 400, 600V
- R.M.S On-State Current •
- : IT (RMS) = 25A
- High Commutating (dv / dt)
- $(dv / dt) c = 10V / \mu s$
- $: V_{Isol} = 1500 V AC$

#### **MAXIMUM RATINGS**

• Isolation Voltage

CHARACTER	ISTIC	SYMBOL	RATING	UNIT	
Repetitive Peak	SM25GZ51	V <sub>DRM</sub>	400	V	
Off-State Voltage	SM25JZ51	VDRM	600	v	
R.M.S On-State Currer (Full Sine Waveform To		I <sub>T (RMS)</sub>	25	А	
Peak One Cycle Surge On-State Current (Non-Repetitive)		l=o.	230 (50Hz)	A	
		ITSM	253 (60Hz)		
I <sup>2</sup> t Limit Value		l <sup>2</sup> t	260	A <sup>2</sup> s	
Critical Rate of Rise of On-State Current	(Note 1)	di / dt	50	Α / μs	
Peak Gate Power Dissipation		P <sub>GM</sub>	5	W	
Average Gate Power D	ssipation	P <sub>G (AV)</sub>	0.5	W	
Peak Gate Voltage		V <sub>GM</sub>	10	V	
Peak Gate Current		I <sub>GM</sub>	2	A	
Junction Temperature		Tj	-40~125	°C	
Storage Temperature R	ange	T <sub>stg</sub>	-40~125	°C	
Isolation Voltage (AC, t	= 1 min.)	V <sub>Isol</sub>	1500	V	



Note 1: di / dt Test Condition V<sub>DRM</sub> = 0.5 × Rated I<sub>TM</sub> ≤ 40A t<sub>gw</sub> ≥ 10µs t<sub>qr</sub> ≤ 250ns  $i_{gp} = I_{GT} \times 2.0$ 

# ELECTRICAL CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC SYMBO		SYMBOL	TEST CONDITION		MIN	TYP.	MAX	UNIT
Repetitive Peak Off-State Current		I <sub>DRM</sub>	V <sub>DRM</sub> = Rated		_	—	20	μA
Gate Trigger Voltage	Ι	V <sub>GT</sub>	V <sub>D</sub> = 12V R <sub>L</sub> = 20Ω	T2 (+) , Gate (+)		_	1.5	V
	Ш			T2 (+) , Gate (−)	-	_	1.5	
	III			T2 (-) , Gate (-)	_	_	1.5	
	Ι		V <sub>D</sub> = 12V R <sub>L</sub> = 20Ω	T2 (+) , Gate (+)	_	_	30	mA
Gate Trigger Current	Ш	I <sub>GT</sub>		T2 (+) , Gate (−)	_	_	30	
	III			T2 (-) , Gate (-)		_	30	
Peak On-State Voltage		V <sub>TM</sub>	I <sub>TM</sub> = 40A		_	_	1.5	V
Gate Non-Trigger Voltage		V <sub>GD</sub>	V <sub>D</sub> = Rated, Tc = 125°C		0.2	_	_	V
Holding Current		Ι <sub>Η</sub>	V <sub>D</sub> = 12V, I <sub>TM</sub> = 1A			_	60	mA
Thermal Resistance		R <sub>th (j−c)</sub>	Junction to Case, AC		_	_	1.3	°C/W
Critical Rate of Rise of Off-State Voltage		dv / dt	V <sub>DRM</sub> = Rated, T <sub>j</sub> = 125°C Exponential Rise		_	300	_	V / µs
Critical Rate of Rise of Off-State Voltage at Commutation		(dv / dt) c	V <sub>DRM</sub> = 400V, T <sub>j</sub> = 125°C (di / dt) c =  – 15A / ms		10	_	_	V / µs

# MARKING

	NUMBER	SYMBOL		MARK	
	*1	TYPE	SM25GZ51	M25GZ51	
<b>7</b>		1117 -	SM25JZ51	M25JZ51	
	*2	Lot Number Month (Starting from Alphabet A) Year (Last Decimal Digit of the Current Year)		Example 8A : January 1998 8B : February 1998 8L : December 1998	

30 50

 $V_D = 12V$ 

 $R_L = 20\Omega$ 

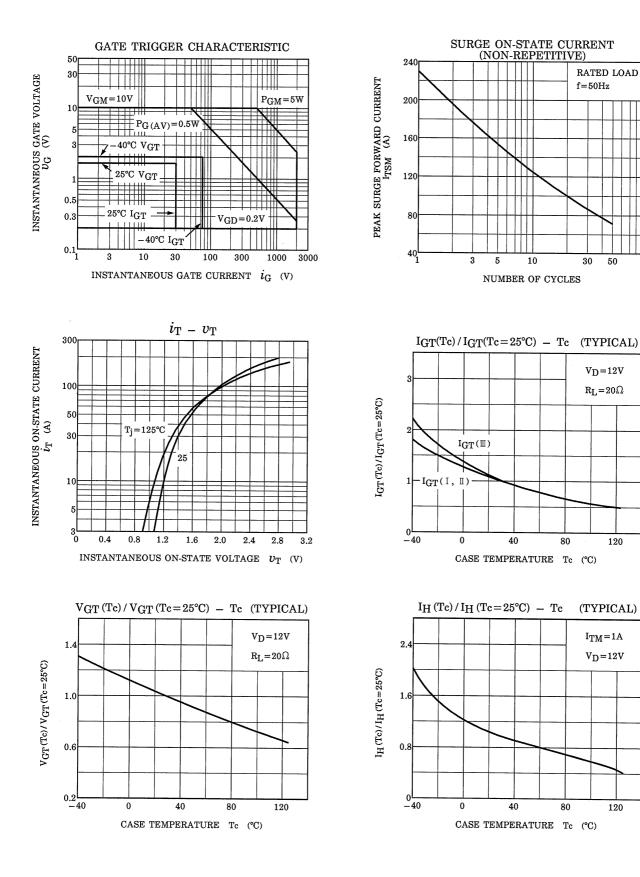
120

 $I_{TM} = 1A$ 

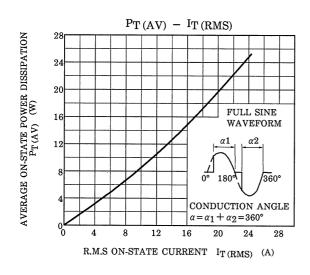
 $V_D = 12V$ 

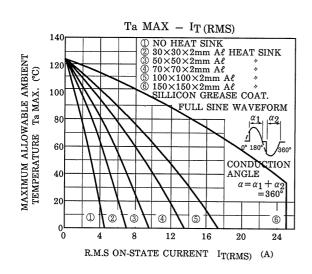
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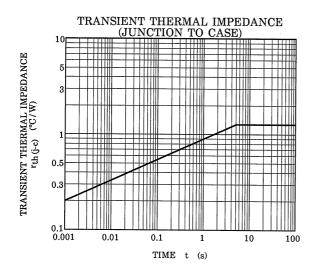
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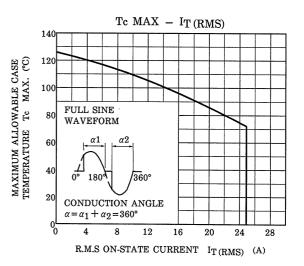


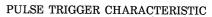
# **TOSHIBA**

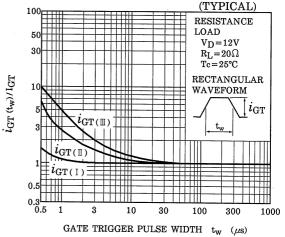












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