## TOSHIBA BI-DIRECTIONAL TRIODE THYRISTOR SILICON PLANAR TYPE

# SM2GZ47,SM2GZ47A,SM2JZ47,SM2JZ47A

## AC POWER CONTROL APPLICATIONS

• IT (RMS) = 1A (Ta = 65°C without radiator)

• Gate Trigger Current : I<sub>GT</sub> = 5mA Max. (TYPE "A")

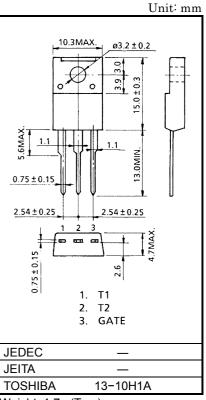
• Repetitive Peak Off-State Voltage: VDRM = 400V, 600V

• R.M.S On–State Current : IT (RMS) = 2A (Tc = 110°C)

• Isolation Voltage :  $V_{ISOL} = 1500V \text{ (AC, } t = 60s)$ 

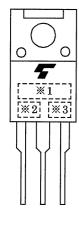
### **MAXIMUM RATINGS**

CHARACTER	ISTIC	SYMBOL	RATING	UNIT	
Repetitive Peak Off-State Voltage and	SM2GZ47 SM2GZ47A	$V_{DRM}$	400	V	
Repetitive Peak Reverse Voltage	SM2JZ47 SM2JZ47A	V DRIM	600		
R.M.S On-State Current (Full Sine Waveform)	Tc = 110°C	IT (DMO)	2	Α	
	Ta = 65°C	IT (RMS)	1		
Peak One Cycle Surge On-State Current (Non-Repetitive)		I <sub>TSM</sub>	8 (50Hz)	Α	
			8.8 (60Hz)	A	
I <sup>2</sup> t Limit Value		1 <sup>2</sup> t	0.32	A <sup>2</sup> s	
Peak Gate Power Dissipation		$P_{GM}$	3	W	
Average Gate Power Dissipation		P <sub>G (AV)</sub>	0.3	W	
Peak Gate Voltage		$V_{FGM}$	10	V	
Peak Gate Current		I <sub>GM</sub>	SM 1.6		
Junction Temperature		Tj	-40~125	°C	
Storage Temperature Range		T <sub>stg</sub>	-40~125	°C	
Isolation Voltage (AC, t = 1min.)		V <sub>ISOL</sub>	1500	V	



Weight: 1.7g (Typ.)

### **MARKING**

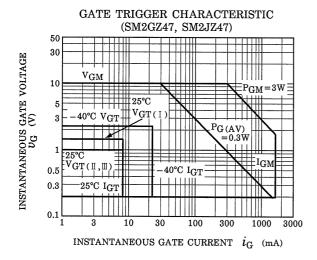


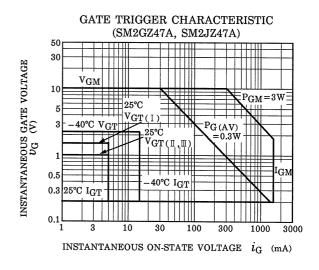
NUMBER		SYMBOL	MARK		
*1	TYPE	SM2GZ47, SM2GZ47A SM2JZ47, SM2JZ47A	M2GZ47 M2JZ47		
*2		SM2GZ47A, SM2JZ47A	A		
*3	Lot Number  Month (Starting from Alphabet A)  Year (Last Decimal Digit of the Current Year)		Example 8A : January 1998 8B : February 1998 8L : December 1998		

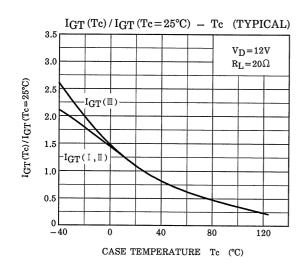


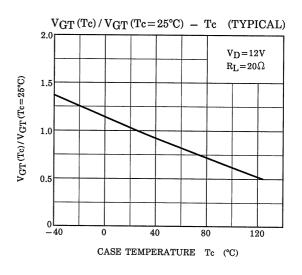
# **ELECTRICAL CHARACTERISTICS (Ta = 25°C)**

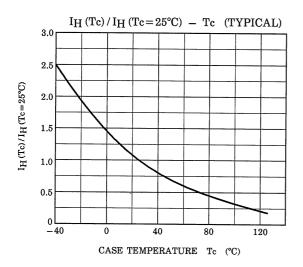
CHARACTERISTIC		SYMBOL	TEST CONDITION		MIN	TYP.	MAX	UNIT	
Repetitive Peak Off-State Current		I <sub>DRM</sub>	V <sub>DRM</sub> = Rated		_	_	20	μA	
Gate Trigger Voltage  II  III  IV		V	V <sub>D</sub> = 12V	T2 (+) , Gate (+)	-	_	1.5	V	
				T2 (+) , Gate (-)	_	_	1		
		Ш	V <sub>GT</sub>	R <sub>L</sub> = 20Ω	T2 (-) , Gate (-)	_	_	1	
		IV			T2 (-) , Gate (+)	_	_	_	
Gate Trigger Current		I	l <sub>GT</sub>	$V_D = 12V$ $R_L = 20\Omega$	T2 (+) , Gate (+)	_	_	8	mA
	SM2GZ47	Ш			T2 (+) , Gate (-)	_	_	8	
	SM2JZ47	III			T2 (-) , Gate (-)	_	_	8	
		IV			T2 (-) , Gate (+)	-	_	_	
		- 1			T2 (+) , Gate (+)	_	_	5	
	SM2GZ47A SM2JZ47A	II			T2 (+) , Gate (-)	_	_	5	
		III			T2 (-) , Gate (-)	_	_	5	
		IV			T2 (-) , Gate (+)	-	_	_	
Peak On-State Voltage		V <sub>TM</sub>	I <sub>TM</sub> = 3A		_	_	1.7	V	
Gate Non-Trigger Voltage		$V_{GD}$	V <sub>D</sub> = Rated, Tc = 125°C		0.2	_	_	V	
Holding Current		I <sub>H</sub>	$R_L = 100\Omega$		_	_	10	mA	
Thermal Resistance		R <sub>th (j−a)</sub>	Junction to Ambient, AC		_	_	55	°C / W	

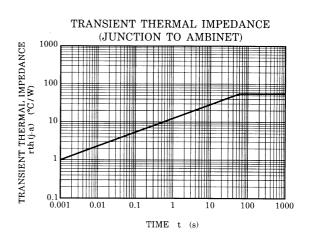


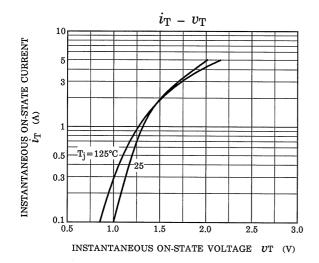


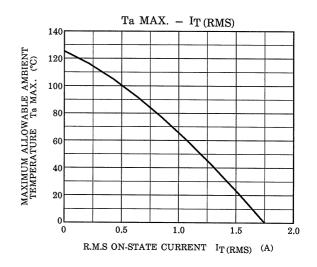












#### <CONDITION>

♦ NO HEAT SINK

◆ LEAD FORMING: LB182

◆ PRINT BOARD

 $\begin{pmatrix} t = 1.6mm \\ SOLDER LAND : 2mm \phi \end{pmatrix}$ 

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