TOSHIBA TLP665G

TOSHIBA PHOTOCOUPLER GaAs IRED & PHOTO-TRIAC

TLP665G

OFFICE MACHINE

HOUSEHOLD USE EQUIPMENT

TRIAC DRIVER

SOLID STATE RELAY

The TOSHIBA TLP665G consists of a photo-triac optically coupled to a gallium arsenide infrared emitting diode in a six lead plastic DIP.

• Peak Off-State Voltage : 400V (Min.)

• Trigger LED Current : 10mA (Max.)

• On-State Current : 100mA (Max.)

• UL Recognized : UL1577, File No. E67349

• Isolation Voltage : 5000V_{rms} (Min.)

• Option (D4) type

VDE Approved : DIN VDE0884 / 08.87,

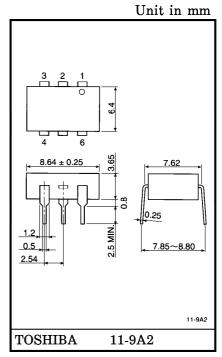
Certificate No. 68383

Maximum Operating Insulation Voltage: 630VpK Highest Permissible Over Voltage: 6000VpK

(Note)When a VDE0884 approved type is needed, please designate the "Option (D4)"

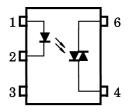
• Structural Parameter

	7.62mm pitch standard type			
Creepage Distance	7.0mm (Min.)			
Clearance	7.0mm (Min.)			
Insulation Thickness	0.5mm (Min.)			



Weight: 0.44g

PIN CONFIGURATION (TOP VIEW)



1 : ANODE 2 : CATHODE

3 : N.C.

4: TERMINAL 1 6: TERMINAL 2

MAXIMUM RATINGS (Ta = 25°C)

	•					
	Forward Current	${ m I_F}$	50	mA		
	Forward Current Derating (Tai	ΔI _F /°C	-0.7	mA/°C		
日日	Peak Forward Current	$I_{\mathbf{FP}}$	1	A		
	(100µs pulse, 100pps)					
	Reverse Voltage	v_{R}	5	V		
	Junction Temperature	$T_{ m j}$	125	°C		
	Off-State Output Terminal Vol	$v_{ m DRM}$	400	V		
١.	On State DWS Comment	Ta = 25°C	Im (DMG)	100	mA	
O R	On-State RMS Current	Ta=70°C		50		
ĮŢ.	On-State Current Derating (Ta	ΔI _T /°C	-1.1	mA/°C		
ECT	Peak On-State Current	I _{TP}	2	A		
ET	$(100\mu s \text{ pulse}, 120\text{pps})$					
	Peak Nonrepetitive Surge Curr	ITSM	1.2	A		
	$(P_W = 10 \text{ms}, DC = 10\%)$		1,2			
	Junction Temperature	$\mathrm{T_{j}}$	115	$^{\circ}\mathrm{C}$		
Storage Temperature Range			$\mathrm{T_{stg}}$	-55~125	°C	
Operating Temperature Range			$T_{ m opr}$	-40~100	°C	
Lead Soldering Temperature (10s)			T_{sol}	260	°C	
Isolation Voltage (AC, 1 min., R.H.≤60%) (Note 1)			$BV_{\mathbf{S}}$	5000	V _{rms}	

(Note 1) Device considered a two terminal device: Pins 1, 2 and 3 shorted together pin 4 and 6 shorted together.

RECOMMENDED OPERATING CONDITIONS

CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT
Supply Voltage	v_{AC}		1	120	Vac
Forward Current	$I_{\mathbf{F}}$	15	20	25	mA
Peak On-Stage Current	I_{TP}	_	_	1	A
Operating Temperature	$T_{ m opr}$	-25	_	85	$^{\circ}\mathrm{C}$

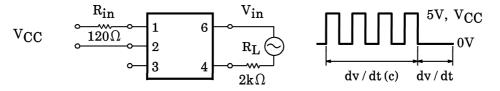
ELECTRICAL CHARACTERISTICS (Ta = 25°C)

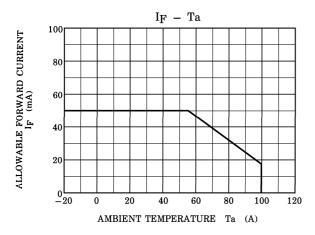
	CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
	Forward Voltage	$ m V_{ m F}$	$I_{\mathbf{F}} = 10 \text{mA}$	1.0	1.15	1.3	V
LE	Reverse Current	$I_{ m R}$	$V_R = 5V$	_	_	10	μ A
_	Capacitance	C_{T}	V=0, f=1MHz		30	_	pF
R (Peak Off-State Current	$I_{ m DRM}$	$V_{ m DRM} = 400 V$	_	10	100	nA
	Peak On-State Voltage	$V_{ extbf{TM}}$	I _{TM} =100mA	_	1.7	3.0	V
$_{ m I}$	Holding Current	${ m I_H}$	_	_	0.6	_	mA
TEC	Critical Rate of Rise of Off-State Voltage	dv / dt	V _{in} =120V, Ta=85°C (Note 2)	200	500	_	V / μs
DE	Critical Rate of Rise of Commutating Voltage	dv / dt (c)	V _{in} =30V _{rms} , I _T =15mA (Note 2)	_	0.2	_	V / μs

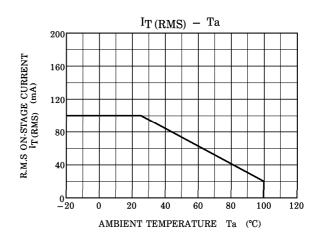
COUPLED ELECTRICAL CHARACTERISTICS (Ta = 25°C)

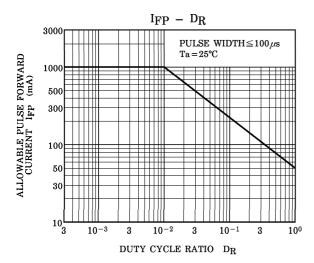
CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Trigger LED Current	$I_{ extbf{FT}}$	$V_{\mathrm{T}}=3V$	_	5	10	mA
Capacitance (Input to Output)	c_{S}	$V_S=0$, $f=1MHz$	_	0.8	_	pF
Isolation Resistance	$R_{\mathbf{S}}$	$V_S = 500V, R.H. \le 60\%$	1×10^{12}	10^{14}	_	Ω
Isolation Voltage	$_{ m BV_S}$	AC, 1 minute	5000	_	_	V
		AC, 1 second, in oil	_	10000	_	$V_{ m rms}$
		DC, 1 minute, in oil	_	10000	_	Vdc

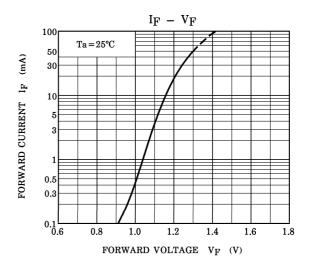
(Note 2) dv/dt TEST CIRCUIT

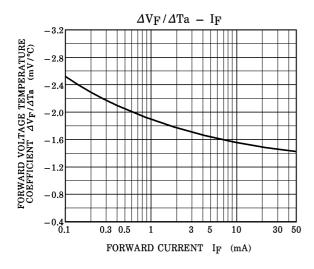


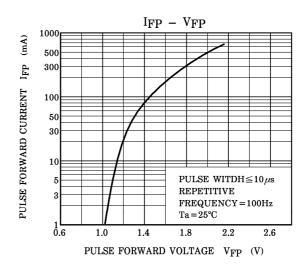


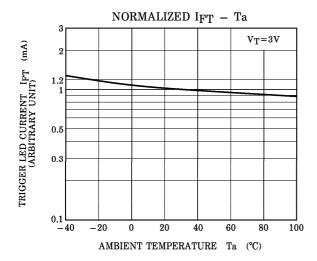


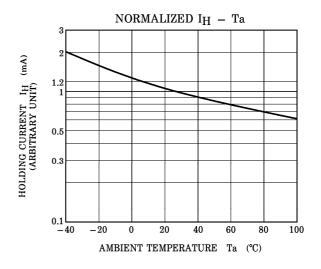


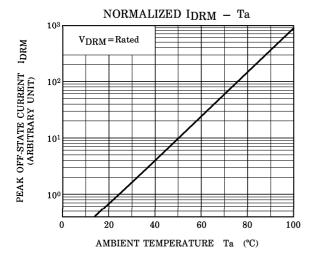


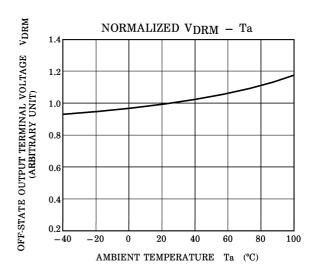


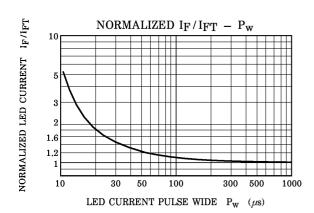












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