### 8-INPUT MULTIPLEXER WITH 3-STATE OUTPUTS

The MC54/74F251 is a high-speed 8-input digital multiplexer. It provides, in one package, the ability to select one bit of data from up to eight sources. It can be used as a universal function generator to generate any logic function of four variables. Both assertion and negation outputs are provided.

- Multifunctional Capacity
- On-Chip Select Logic Decoding
- Inverting and Noninverting 3-State Outputs

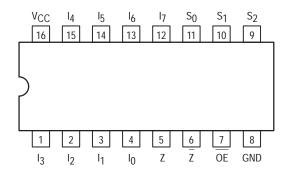
### **FUNCTIONAL DESCRIPTION**

This device is a logical implementation of a single-pole, 8-position switch with the switch position controlled by the state of three Select inputs,  $S_0$ ,  $S_1$ ,  $S_2$ . Both assertion and negation outputs are provided. The Output Enable input (OE) is active LOW. When it is activated, the logic function provided at the output is:

$$Z = \overline{OE} \bullet (I_0 \bullet \overline{S_0} \bullet \overline{S_1} \bullet \overline{S_2} + I_1 \bullet S_0 \bullet \overline{S_1} \bullet \overline{S_2} + I_2 \bullet \overline{S_0} \bullet S_1 \bullet \overline{S_2} + I_3 \bullet S_0 \bullet S_1 \bullet S_2 + I_4 \bullet \overline{S_0} \bullet S_1 \bullet S_2 + I_5 \bullet S_0 \bullet \overline{S_1} \bullet S_2 + I_6 \bullet S_0 \bullet S_1 \bullet S_2 + I_7 \bullet S_0 \bullet S_1 \bullet S_2 + I_8 \bullet \overline{S_0} \bullet S_1 \bullet \overline{S_0} \bullet S_1 \bullet S_2 + I_8 \bullet \overline{S_0} \bullet S_1 \bullet \overline{S_0} \bullet S_1 \bullet \overline{S_0} \bullet S_1 \bullet S_2 + I_8 \bullet \overline{S_0} \bullet S_1 \bullet \overline{S_0} \bullet \overline{S_0} \bullet S_1 \bullet \overline{S_0} \bullet \overline{S$$

When the Output Enable is HIGH, both outputs are in the high impedance (high Z) state. This feature allows multiplexer expansion by tying the outputs of up to 128 devices together. When the outputs of the 3-state devices are tied together, all but one device must be in the high impedance state to avoid high currents that would exceed the maximum ratings. The Output Enable signals should be designed to ensure there is no overlap in the active LOW portion of the enable voltages.

### **CONNECTION DIAGRAM**



### MC54/74F251

### 8-INPUT MULTIPLEXER WITH 3-STATE OUTPUTS

**FAST™ SCHOTTKY TTL** 



J SUFFIX CERAMIC CASE 620-09



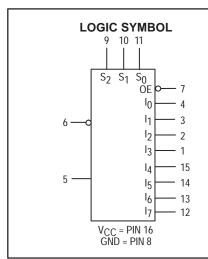
N SUFFIX PLASTIC CASE 648-08



D SUFFIX SOIC CASE 751B-03

### **ORDERING INFORMATION**

MC54FXXXJ Ceramic MC74FXXXN Plastic MC74FXXXD SOIC



LAST SHIP 30/09/99

**RESISTANTIANT** 31/03/99

# ORDER 31/03/99

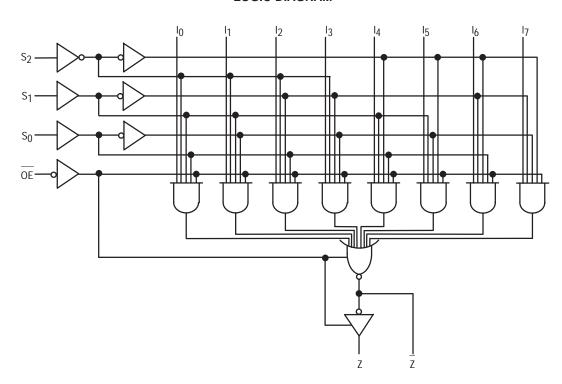
### **FUNCTION TABLE**

	Inp	Outputs			
OE	S <sub>2</sub>	s <sub>1</sub>	s <sub>0</sub>	Z	Z
Н	Х	Х	Х	Z	Z
L	L	L	L	Ī <sub>0</sub>	Ι <sub>0</sub>
L	L	L	Н	ī <sub>1</sub>	l <sub>1</sub>
L	L	Н	L	Ī <sub>2</sub>	l <sub>2</sub>
L	L	Н	Н	Ī <sub>3</sub>	l <sub>3</sub>
L	Н	L	L	- I <sub>4</sub>	l <sub>4</sub>
L	Н	L	Н	- I <sub>5</sub>	l <sub>5</sub>
L	Н	Н	L	Ī <sub>6</sub>	I <sub>6</sub>
L	Н	Н	Н	- l <sub>7</sub>	l <sub>7</sub>

H = HIGH Voltage Level

- L = LOW Voltage Level
- X = Don't Care
  Z = High Impedance

### **LOGIC DIAGRAM**



### **GUARANTEED OPERATING RANGES**

Symbol	Parameter		Min	Тур	Max	Unit
VCC	Supply Voltage	54, 74	4.5	5.0	5.5	V
TA	Operating Ambient Temperature Range	54	-55	25	125	°C
		74	0	25	70	
loh	Output Current — High	54, 74			-3.0	mA
l <sub>OL</sub>	Output Current — Low	54, 74			24	mA

## ST SHIP 30/09/9

### BUY

### DC CHARACTERISTICS OVER OPERATING TEMPERATURE RANGE (unless otherwise specified)

				Limits					
Symbol	Parameter		Min	Тур	Max	Unit	Test Co	nditions	
VIH	Input HIGH Voltage		2.0			V	Guaranteed Input	HIGH Voltage	
V <sub>IL</sub>	Input LOW Voltage				0.8	V	Guaranteed Input	Guaranteed Input LOW Voltage	
VIK	Input Clamp Diode Voltage				-1.2	V	$I_{IN} = -18 \text{ mA}$	V <sub>CC</sub> = MIN	
Vон	Output HIGH Voltage	54, 74	2.4	3.4		V	$I_{OH} = -3.0 \text{ mA}$	V <sub>CC</sub> = 4.50 V	
		74	2.7	3.4		V	$I_{OH} = -3.0 \text{ mA}$	V <sub>CC</sub> = 4.75 V	
V <sub>OL</sub>	Output LOW Voltage			0.35	0.5	V	I <sub>OL</sub> = 24 mA	V <sub>CC</sub> = MIN	
lozh	Output Off Current — HIGH				50	μΑ	V <sub>OUT</sub> = 2.7 V	V <sub>CC</sub> = MAX	
lozL	Output Off Current — LOW				-50	μΑ	V <sub>OUT</sub> = 0.5 V	V <sub>CC</sub> = MAX	
ΊΗ	Input HIGH Current				20	μΑ	V <sub>IN</sub> = 2.7 V	V <sub>CC</sub> = MAX	
					100	μΑ	V <sub>IN</sub> = 7.0 V		
Ίμ	Input LOW Current				-0.6	mA	V <sub>IN</sub> = 0.5 V	V <sub>CC</sub> = MAX	
los	Output Short Circuit Current (Note 2)		-60		-150	mA	V <sub>OUT</sub> = 0 V	V <sub>CC</sub> = MAX	
Icc	Power Supply Current			15	22	mA	<u>I<sub>n</sub>, S<sub>n</sub> = 4.5 V</u> OE = GND	V <sub>CC</sub> = MAX	
				16	24		OE, I <sub>n</sub> = 4.5 V	V <sub>CC</sub> = MAX	

### NOTES

### **AC CHARACTERISTICS**

A0 011	IANACIENIOTICO							
		54/	54/74F 54F		74F			
1		T <sub>A</sub> =	T <sub>A</sub> = +25°C V <sub>CC</sub> = +5.0 V		T <sub>A</sub> = -55 °Cto +125°C V <sub>CC</sub> = 5.0 V ±10%		$T_A = 0^{\circ}C \text{ to } 70^{\circ}C$ $V_{CC} = 5.0 \text{ V} \pm 10\%$	
		C <sub>L</sub> =	C <sub>L</sub> = 50 pF		C <sub>L</sub> = 50 pF		C <sub>L</sub> = 50 pF	
Symb	pol Parameter	Min	Max	Min	Max	Min	Max	Unit
tPLH	Propagation Delay	4.0	8.0	3.5	9.5	4.0	9.0	ns
tPHL	$S_n$ to $\overline{Z}_n$	3.2	7.5	3.2	9.5	3.2	8.5	
tPLH	Propagation Delay	4.5	13	3.5	16.5	4.5	14	ns
tPHL	S <sub>n</sub> to Z <sub>n</sub>	4.5	9.0	3.0	10.5	4.0	10.5	
tPLH	Propagation Delay	3.0	5.7	2.5	8.0	3.0	7.0	ns
tPHL	$I_n$ to $\overline{Z}$	1.5	4.0	1.5	6.0	1.5	5.0	
tPLH	Propagation Delay	4.0	9.5	3.5	11.5	4.0	10.5	ns
tPHL	I <sub>n</sub> to Z	3.0	6.5	3.0	7.5	3.0	7.5	
tPZH	Output Enable Time	3.0	7.0	3.0	9.5	3.0	8.0	ns
tPZL	OE to Z	3.0	8.5	3.0	10.5	3.0	9.5	
tPHZ	Output Disable Time	3.0	6.5	3.0	8.5	3.0	7.5	ns
tPLZ	$\overline{OE}$ to $\overline{Z}$	2.0	4.5	2.0	8.0	2.0	5.5	
t <sub>PZH</sub>	Output Enable Time	4.0	9.0	4.0	10	4.0	10	ns
tPZL	OE to Z	3.5	8.0	3.5	10	3.5	9.0	
tPHZ	Output Disable Time	3.0	6.0	3.0	7.0	3.0	7.0	ns
t <sub>PLZ</sub>	OE to Z	2.0	4.5	2.0	8.0	2.0	5.5	

<sup>1.</sup> For conditions shown as MIN or MAX, use the appropriate value specified under guaranteed operating ranges.

<sup>2.</sup> Not more than one output should be shorted at a time, nor for more than 1 second.

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