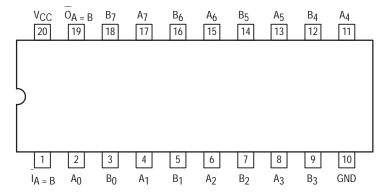


# **8-BIT IDENTITY COMPARATOR**

The MC54/74F521 is an expandable 8-bit comparator. It compares two words of up to eight bits each and provides a LOW output when the two words match bit for bit. The expansion input I  $_{A}$  =  $_{B}$  also serves as an active-LOW enable input.

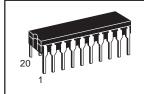
- Compares Two 8-Bit Words in 6.5 ns Typical
- Expandable to Any Word Length
- 20-Pin Package

# **CONNECTION DIAGRAM (TOP VIEW)**

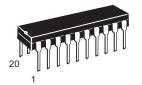


# MC54/74F521

# 8-BIT IDENTITY COMPARATOR FAST™ SCHOTTKY TTL



J SUFFIX CERAMIC CASE 732-03



N SUFFIX PLASTIC CASE 738-03



DW SUFFIX SOIC CASE 751D-03

### **ORDERING INFORMATION**

MC54FXXXJ Ceramic MC74FXXXN Plastic MC74FXXXDW SOIC

## **GUARANTEED OPERATING RANGES**

Symbol	Parameter		Min	Тур	Max	Unit
VCC	Supply Voltage	54, 74	4.5	5.0	5.5	V
TA	Operating Ambient Temperature Range	54	-55	25	125	°C
		74	0	25	70	
lOH	Output Current — High	54,74			-1.0	mA
lOL	Output Current — Low	54, 74			20	mA

# **LOGIC DIAGRAM** $A_0$ Во В<sub>1</sub> $A_2$ B<sub>2</sub> $A_3$ $O_{A = B}$ $\mathsf{A}_4$ $B_4$ $A_5$ B<sub>5</sub> $\mathsf{A}_{6}$ В6 Α7 В7 $I_A = B$

LOGIC SYMBOL В7  $I_A = B$ 18 A7 17 В6 16 15 A<sub>6</sub> 14 B<sub>5</sub> A<sub>5</sub> 13 B<sub>4</sub> 12  $\mathsf{A}_4$ 11  $O_{A = B}$ 9 Вз 8 A3 В2 7 6 A<sub>2</sub> B<sub>1</sub> 5 4 A<sub>1</sub> В0 3  $A_0$ 

V<sub>CC</sub> = PIN 20 GND = PIN 10

NOTE:

This diagram is provided only for the understanding

of logic operations and should not be used to estimate propagation delays.

			Limits						
Symbol	DI PARAMETER		Min	Тур	Max	Unit	Test Conditions		
VIH	Input HIGH Voltage		2.0			V	Guaranteed Input HIGH Voltage		
V <sub>IL</sub>	Input LOW Voltage				0.8	V	Guaranteed Input LOW Voltage		
VIK	Input Clamp Diode Voltage				-1.2	V	I <sub>IN</sub> = – 18 mA	VCC = MIN	
Vон	Output HIGH Voltage	54, 74	2.5	3.4		V	I <sub>OH</sub> = - 1.0 mA	V <sub>CC</sub> = 4.5 V	
		74	2.7	3.4		V	I <sub>OH</sub> = - 1.0 mA	V <sub>CC</sub> = 4.75 V	
V <sub>OL</sub>	Output LOW Voltage			0.35	0.5	V	I <sub>OL</sub> = 20 mA	V <sub>CC</sub> = MIN	
					20	μА	V <sub>IN</sub> = 2.7 V	VCC = MAX	
I <sub>IH</sub>	Input HIGH Current				100	μΑ	V <sub>IN</sub> = 7.0 V	7	
I <sub>IL</sub>	Input LOW Current				-0.6	mA	V <sub>IN</sub> = 0.5 V	V <sub>CC</sub> = MAX	
los	Output Short Circuit Current (Note 2)  Power Supply Current		- 60		-150	mA	V <sub>OUT</sub> = 0 V	V <sub>CC</sub> = MAX	
ICC				21	32	mA	$I_A = B = GND$	V <sub>CC</sub> = MAX	

- 1. For conditions shown as MIN or MAX, use the appropriate value specified under guaranteed operating ranges.
- 2. Not more than one output should be shorted at a time, nor for more than 1 second.

### **FUNCTION TABLE**

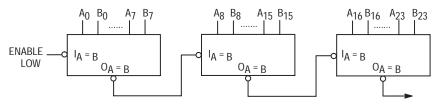
	Output			
Î <sub>A</sub> = B	A, B	O <sub>A</sub> = B		
L	A = B*	L		
L	A ≠ B	Н		
Н	A = B*	Н		
Н	A ≠ B	Н		

H = HIGH Voltage Level L = LOW Voltage Level

# **AC CHARACTERISTICS**

		54/74F		54F		74F			
		T <sub>A</sub> = +25°C			T <sub>A</sub> = -55°C to +125°C		T <sub>A</sub> = 0°C to +70°C		
		V <sub>CC</sub> = +5.0 V			$V_{CC}$ = 5.0 V $\pm$ 10%		$V_{CC}$ = 5.0 V $\pm$ 10%		
		C <sub>L</sub> = 50 pF			C <sub>L</sub> = 50 pF		C <sub>L</sub> = 50 pF		
Symbol	Parameter	Min	Тур	Max	Min	Max	Min	Max	Unit
tPLH	Propagation Delay	2.5	6.5	10	2.5	15	2.5	11	ns
<sup>t</sup> PHL	$A_n$ or $B_n$ to $O_A = B$	3.0	6.5	10	3.0	12	3.0	11	
<sup>t</sup> PLH	Propagation Delay	2.5	4.5	6.5	2.5	8.5	2.5	7.5	ns
<sup>t</sup> PHL	$I_A = B$ to $O_A = B$	3.5	5.0	9.0	3.5	10	3.5	10	

# Ripple Expansion



# **Parallel Expansion**

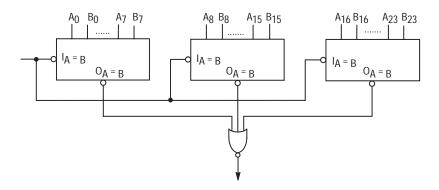


Figure 1. Applications

 $<sup>*</sup>A_0 = B_0, A_1 = B_1, A_2 = B_2, etc.$ 

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