Silicon N Channel MOS FET UHF Power Amplifier

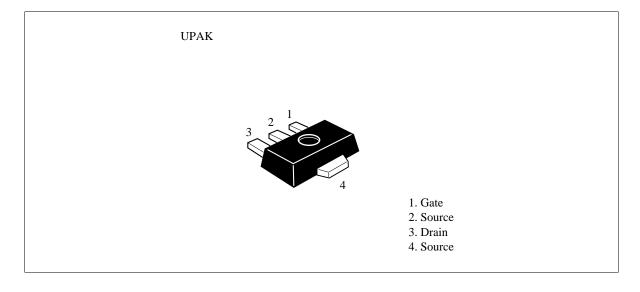
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ADE-208-675A (Z) 2nd. Edition Mar. 2001

Features

- High power output, High gain, High efficiency $PG=8.0dB,\,Pout=31dBm,\,\eta D=57\,\,\%\,min.\,(f=836.5MHz)$
- Compact package capable of surface mounting

Outline



This Device is sensitive to Electro Static Discharge. An Adequate handling procedure is requested.



Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Ratings	Unit	
Drain to source voltage	V _{DSS}	10	V	
Gate to source voltage	V _{GSS}	±6	V	
Drain current	I _D	0.7	A	
Drain peak current	Note1 D(pulse)	1.4	A	
Channel dissipation	Pch Note2	3	W	
Channel temperature	Tch	150	°C	
Storage temperature	Tstg	-45 to +150	°C	

Note: 1. $PW \le 10ms$, duty cycle $\le 50 \%$

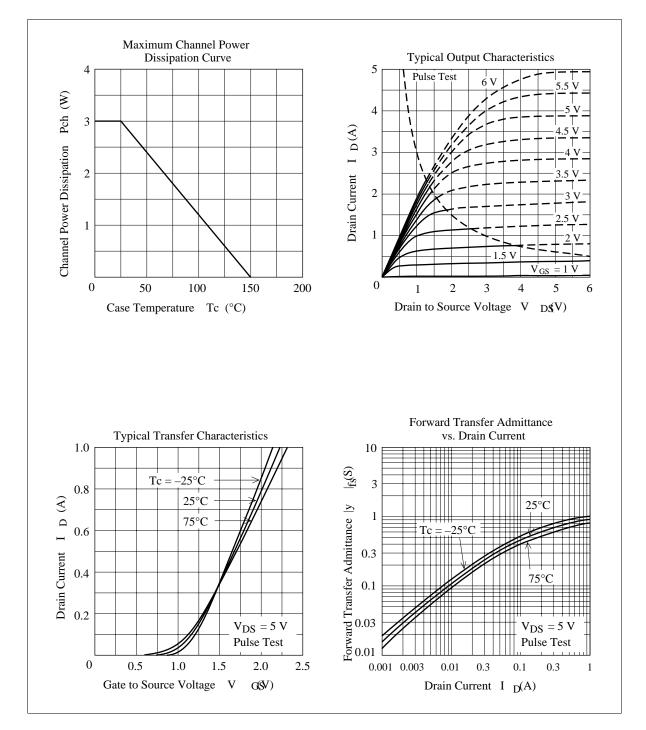
2. Value at Tc = 25° C

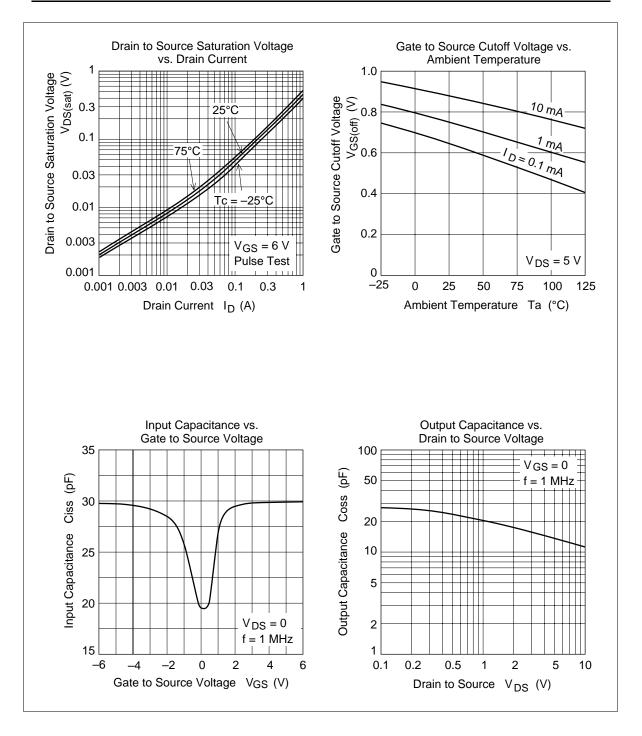
Electrical Characteristics (Ta = 25°C)

Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Zero gate voltage drain current	I _{DSS}	_	—	100	μΑ	$V_{\rm DS} = 10 \text{V}, V_{\rm GS} = 0$
Gate to source leak current	I _{GSS}	—	_	±5.0	μΑ	$V_{GS} = \pm 6 V, V_{DS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	0.4	_	1.2	V	$I_{\rm D} = 3 \text{ mA}, V_{\rm DS} = 5 \text{ V}$
Input capacitance	Ciss	—	27	—	pF	$V_{GS} = 2 V, V_{DS} = 0, f = 1 MHz$
Output capacitance	Coss	_	13	_	pF	$V_{DS} = 5 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$
Output Power	Pout	31	—	—	dBm	$V_{DS} = 4.7 V, f = 836.5 MHz$ Pin = 23 dBm
Drain Rational	ηD	57	_	_	%	V_{DS} = 4.7 V, f =836.5 MHz Pin = 23 dBm

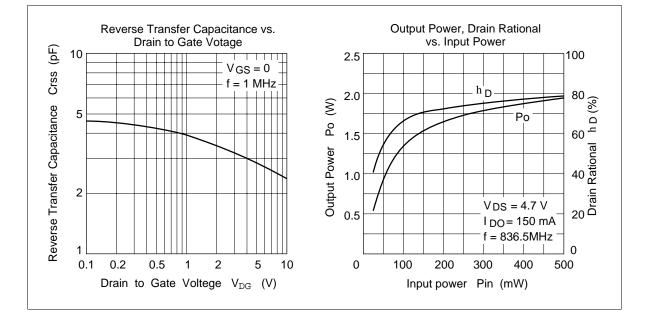
Note: 1. Marking is "HX".

Main Characteristics

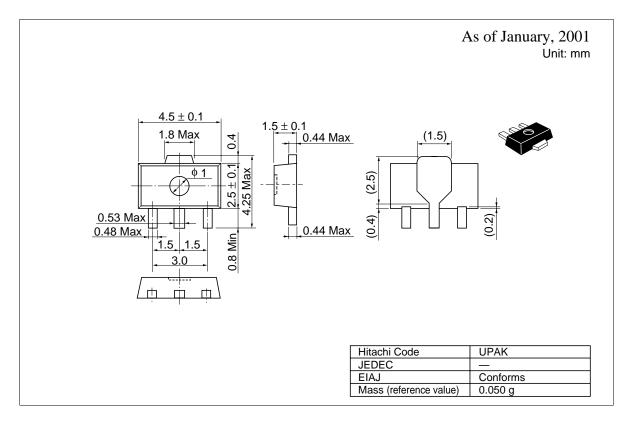




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Package Dimensions



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