

PROPRIETARY Data Sheet March 2002 FN5042.1

PRISM 5GHz VCO



The ISL3084 is part of the PRISM 3 chipset, a 5GHz SiGe Monolithic VCO circuit design to simplify and

reduce the cost and size of wireless transceivers.

A fully integrated VCO requiring no external elements such as inductors or varactors greatly simplifies low cost local oscillator synthesized applications.

Included in this differential design are a low-power standby function, a stable process and temperature biasing operation.

The ISL3084 is housed in a 16 lead MLF package well suited for PCMCIA and miniPCI board applications.

Features

- Fully integrated / no external varactors or resonators required
- · Isolation output buffer / reduced load pulling
- · Differential design / reduced spurs
- · Digitally controlled power down mode

Applications

- Systems targeting IEEE802.11b, 11Mbps standard
- Only required VCO function for Intersil PRISM3 chip set
- · WLAN applications.
- · PCMCIA wireless transceivers
- · TDMA packet protocol radios

Simplified Block Diagram

