MOS FET Power Amplifier Module for DCS 1800 Handy Phone

HITACHI

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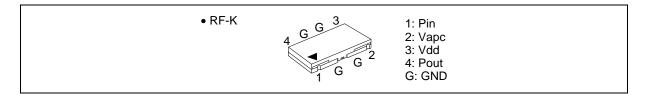
Application

For DCS 1800 class1 1710 to 1785 MHz.

Features

- 3stage amplifier : 0 dBm input
- Lead less thin & small package : 2 mm Max & 0.2cc
- High efficiency : 40% Typ at 32.5 dBm
- Wide gain control range : 70 dB Typ
- Low voltage operation : 3.5 V

Pin Arrangement



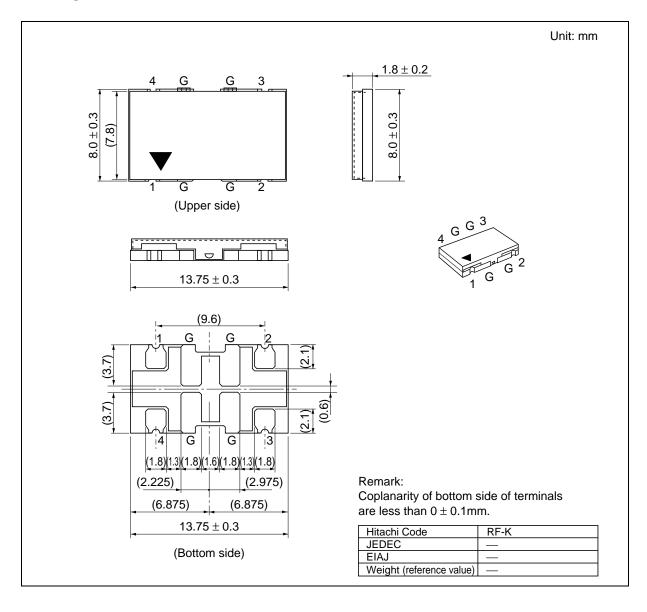
Absolute Maximum Ratings (Tc = 25°C)

ltem	Symbol	Rating	Unit	
Supply voltage	V _{DD}	8	V	
Supply current	I _{DD}	2	A	
V _{APC} voltage	V _{APC}	4	V	
Input power	Pin	10	mW	
Operating case temperature	Tc (op)	-30 to +100	°C	
Storage temperature	Tstg	-30 to +100	°C	
Output power	Pout	3	W	

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Frequency range	f	1710		1785	MHz	
Control voltage range	V	0.5		2.2	V	
Drain cutoff current	I _{DS}			100	μΑ	$V_{DD} = 8 \text{ V}, V_{APC} = 0 \text{ V}$
Total efficiency	η_{τ}	35	40	_	%	Pin = 0 dBm, V_{DD} = 3.5 V, Pout = 32.5 dBm (at APC controlled), R_L = Rg = 50 Ω, Tc = 25°C
2nd harmonic distortion	2nd H.D.	_	-45	-35	dBc	-
3rd harmonic distortion	3rd H.D.	_	-45	-35	dBc	-
Input VSWR	VSWR (in)	—	1.5	3		-
Output power (1)	Pout (1)	32.5	33.0	_	dBm	Pin = 0 dBm, $V_{_{DD}}$ = 3.5 V, $V_{_{APC}}$ = 2.2 V, $R_{_{L}}$ = Rg = 50 Ω, Tc = 25°C
Output power (2)	Pout (2)	31	31.5	_	dBm	Pin = 0 dBm, $V_{_{DD}}$ = 3.0 V, $V_{_{APC}}$ = 2.2 V, $R_{_{L}}$ = Rg = 50 Ω, Tc = 85°C
Isolation	_	_	-36	-33	dBm	Pin = 0 dBm, $V_{_{DD}}$ = 3.5 V, $V_{_{APC}}$ = 0.5 V, $R_{_{L}}$ = Rg = 50 Ω, Tc = 25°C
Switching time	tr, tf	_	1	2	μs	Pin = 0 dBm, V_{DD} = 3.5 V, Pout = 32.5 dBm, R_L = Rg = 50 Ω, Tc = 25°C
Stability	_	No parasitic — oscillation		_	$ \begin{array}{l} \mbox{Pin}=0 \mbox{ dBm}, \mbox{ V}_{_{DD}}=3 \mbox{ to } 5.1 \mbox{ V}, \\ \mbox{Pout}\leq 32.5 \mbox{ dBm} \mbox{ (at APC controlled)}, \\ \mbox{Rg}=50 \ \Omega, \mbox{ t}=20 \mbox{ sec.}, \mbox{ Tc}=25^{\circ}\mbox{C}, \\ \mbox{Output VSWR}=6:1 \mbox{ All phases} \end{array} $	

Electrical Characteristics (Tc = 25°C)

Package Dimensions



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