# 2SK2119

# Silicon N-Channel MOS FET

# **HITACHI**

November 1996

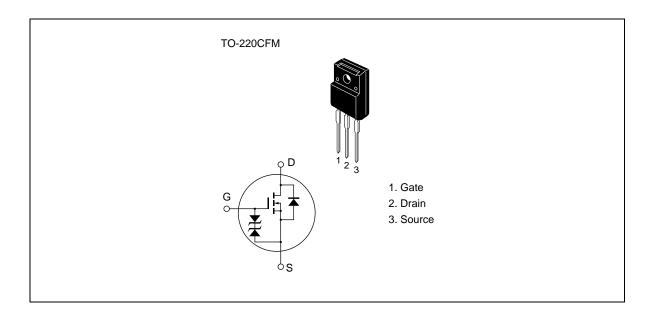
#### **Application**

High speed power switching

#### **Features**

- Low on-resistance
- High speed switching
- Low drive current
- 4 V gate drive device can be driven from 5 V source
- Suitable for Switching regulator, DC-DC converter
- Avalanche ratings

#### **Outline**



## 2SK2119

## **Absolute Maximum Ratings** (Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	60	V
Gate to source voltage	V <sub>GSS</sub>	±20	V
Drain current	I <sub>D</sub>	25	A
Drain peak current	I <sub>D(pulse)</sub> *1	100	A
Body to drain diode reverse drain current	I <sub>DR</sub>	25	A
Avalanche current	I_*3	25	A
Avalanche energy	E_AR *3	53	mJ
Channel dissipation	Pch*2	30	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1 %

- 2. Value at Tc = 25  $^{\circ}$ C
- 3. Value at Tch = 25 °C, Rg  $\geq$  50  $\Omega$

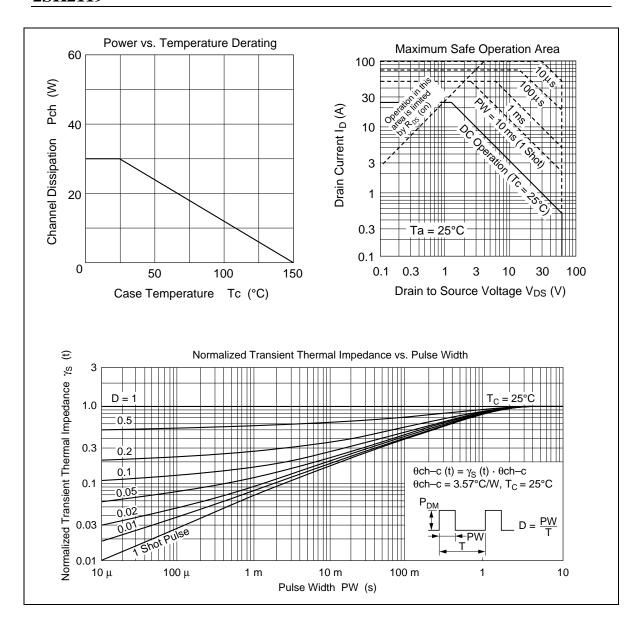
## **Electrical Characteristics** (Ta = 25°C)

Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	60	_	_	V	$I_{D} = 10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±20	_	_	V	$I_{_{G}} = \pm 100 \ \mu A, \ V_{_{DS}} = 0$
Gate to source leak current	I <sub>GSS</sub>	_		±10	μΑ	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	l I <sub>DSS</sub>	_	_	250	μΑ	$V_{DS} = 50 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.0	_	2.25	V	$I_{D} = 1 \text{ mA}, V_{DS} = 10 \text{ V}$
Static drain to source on state resistance	$R_{\scriptscriptstyle DS(on)}$	_	0.03	0.04	Ω	$I_{D} = 15 \text{ A}$ $V_{GS} = 10 \text{ V}^{*1}$
		_	0.043	0.06	Ω	$I_{D} = 15 \text{ A}$ $V_{GS} = 4 \text{ V}^{*1}$
Forward transfer admittance	y <sub>fs</sub>	12	21	_	S	$I_{D} = 15 \text{ A}$ $V_{DS} = 10 \text{ V}^{*1}$
Input capacitance	Ciss	_	1450	_	pF	$V_{DS} = 10 \text{ V}$ $V_{GS} = 0$ $f = 1 \text{ MHz}$
Output capacitance	Coss	_	655	_	pF	
Reverse transfer capacitance	Crss	_	195	_	pF	
Turn-on delay time	$\mathbf{t}_{\text{d(on)}}$	_	20	_	ns	$I_{D} = 15 \text{ A}$ $V_{GS} = 10 \text{ V}$ $R_{L} = 2 \Omega$
Rise time	t <sub>r</sub>	_	110		ns	
Turn-off delay time	t <sub>d(off)</sub>	_	225		ns	
Fall time	t <sub>f</sub>	_	145	_	ns	
Body to drain diode forward voltage	$V_{DF}$	_	1.2	_	V	$I_{F} = 25 \text{ A}, V_{GS} = 0$
Body to drain diode reverse recovery time	t <sub>rr</sub>		100		ns	$I_F = 25 \text{ A}, V_{GS} = 0,$ diF / dt = 50 A / $\mu s$

Note 1. Pulse Test

See characteristic curve of 2SK1910.

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