## 2SK2120

Silicon N-Channel MOS FET

# HITACHI

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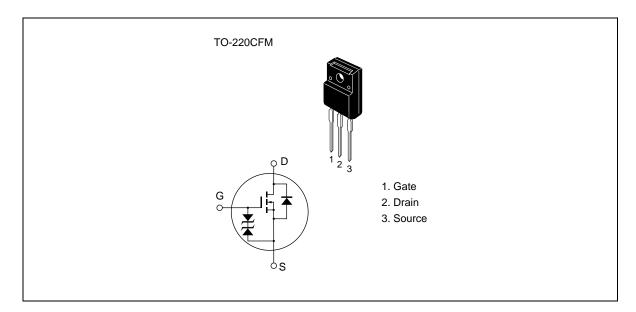
#### Application

High speed power switching

#### Features

- Low on-resistance
- High speed switching
- Low drive current
- 4 V gate drive device can be driven from 5 V source
- Suitable for Switching regulator, DC-DC converter
- Avalanche ratings

#### Outline



### 2SK2120

### **Absolute Maximum Ratings** (Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>dss</sub>	60	V
Gate to source voltage	V <sub>gss</sub>	±20	V
Drain current	I <sub>D</sub>	40	А
Drain peak current	+1 D(pulse)	160	А
Body to drain diode reverse drain current	I <sub>DR</sub>	40	А
Avalanche current	*³	40	А
Avalanche energy	<b>E</b> <sub>AR</sub> * <sup>3</sup>	137	mJ
Channel dissipation	Pch* <sup>2</sup>	35	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes 1.  $PW \le 10 \ \mu s$ , duty cycle  $\le 1 \ \%$ 

2. Value at Tc = 25 °C

3. Value at Tch = 25 °C, Rg  $\ge$  50  $\Omega$ 

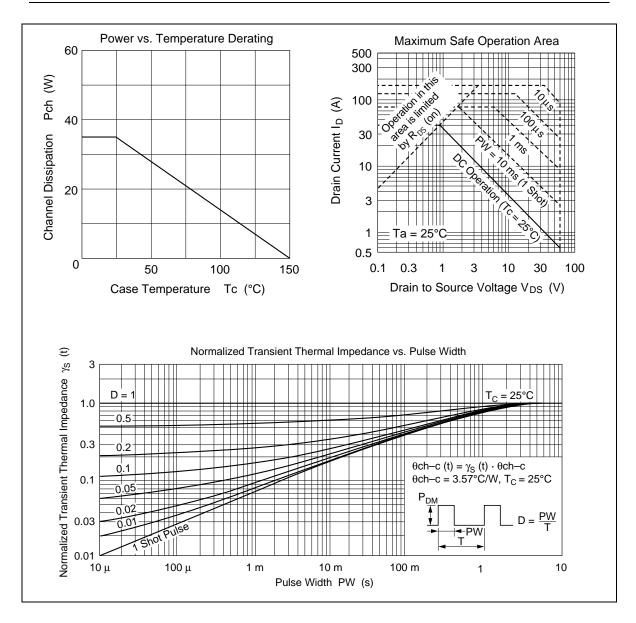
### **Electrical Characteristics** (Ta = 25°C)

Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{\scriptscriptstyle (BR)DSS}$	60		_	V	$I_{_{D}}$ = 10 mA, $V_{_{GS}}$ = 0
Gate to source breakdown voltage	$V_{\scriptscriptstyle (BR)GSS}$	±20	—	_	V	$I_{g} = \pm 200 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I <sub>GSS</sub>	_	_	±10	μA	$V_{_{GS}} = \pm 16 \text{ V}, \text{ V}_{_{DS}} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	_	_	250	μA	$V_{_{DS}} = 50 \text{ V}, \text{ V}_{_{GS}} = 0$
Gate to source cutoff voltage	$V_{\text{GS(off)}}$	1.0	_	2.25	V	$I_{\rm D} = 1 \text{ mA}, V_{\rm DS} = 10 \text{ V}$
Static drain to source on state resistance	$R_{\scriptscriptstyle DS(on)}$	_	0.018	0.022	Ω	$I_{_{ m D}} = 20 \text{ A}$ $V_{_{ m GS}} = 10 \text{ V}^{*1}$
		_	0.023	0.028	Ω	$I_{_{ m D}} = 20 \text{ A}$ $V_{_{ m GS}} = 4 \text{ V}^{*1}$
Forward transfer admittance	y <sub>fs</sub>	22	35		S	$I_{_{\rm D}} = 20 \text{ A}$ $V_{_{\rm DS}} = 10 \text{ V}^{*^1}$
Input capacitance	Ciss		3530	_	pF	$V_{_{DS}} = 10 V$ $V_{_{GS}} = 0$ f = 1 MHz
Output capacitance	Coss	—	1480	—	pF	—
Reverse transfer capacitance	Crss	_	300	_	pF	
Turn–on delay time	t <sub>d(on)</sub>	_	33	_	ns	$I_{_{D}} = 20 \text{ A}$ $V_{_{GS}} = 10 \text{ V}$ $R_{_{L}} = 1.5 \Omega$
Rise time	t,	_	155	_	ns	—
Turn–off delay time	t <sub>d(off)</sub>	_	450	_	ns	_
Fall time	t <sub>r</sub>		220	_	ns	_
Body to drain diode forward voltage	$V_{dF}$	—	1.2	—	V	$I_{_{\rm F}}$ = 40 A, $V_{_{\rm GS}}$ = 0
Body to drain diode reverse recovery time	t <sub>rr</sub>	—	120	—	ns	$I_{_{\rm F}}$ = 40 A, $V_{_{\rm GS}}$ = 0, diF / dt = 50 A / µs
Note 1. Pulse Test						

Note 1. Pulse Test

See characteristic curve of 2SK1911.

#### 2SK2120



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# HITACHI

#### Hitachi, Ltd.

Semiconductor & IC Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100, Japan Tel: Tokyo (03) 3270-2111 Fax: (03) 3270-5109

#### For further information write to:

Hitachi America, Ltd. Semiconductor & IC Div. 2000 Sierra Point Parkway Brisbane, CA. 94005-1835 U S A Tel: 415-589-8300 Fax: 415-583-4207 Hitachi Europe GmbH Electronic Components Group Continental Europe Dornacher Straße 3 D-85622 Feldkirchen München Tel: 089-9 91 80-0 Fax: 089-9 29 30 00 Hitachi Europe Ltd. Electronic Components Div. Northern Europe Headquarters Whitebrook Park Lower Cookham Road Maidenhead Berkshire SL6 8YA United Kingdom Tel: 0628-585000 Fax: 0628-778322 Hitachi Asia Pte. Ltd. 16 Collyer Quay #20-00 Hitachi Tower Singapore 0104 Tel: 535-2100 Fax: 535-1533

Hitachi Asia (Hong Kong) Ltd. Unit 706, North Tower, World Finance Centre, Harbour City, Canton Road Tsim Sha Tsui, Kowloon Hong Kong Tel: 27359218 Fax: 27306071

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