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Renesas Technology Corp. Customer Support Dept. April 1, 2003



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Remember to give due consideration to safety when making your circuit designs, with appropriate
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Silicon N Channel MOS FET High Speed Power Switching

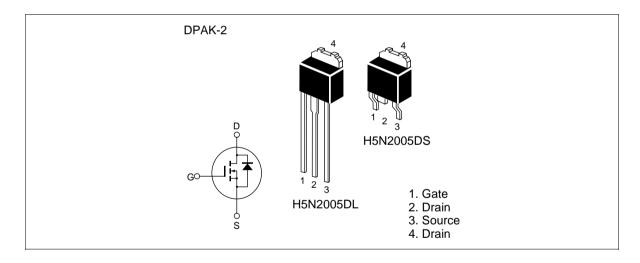


ADE-208-1373 (Z) Target Specification 1st. Edition Mar. 2001

Features

- Low on-resistance
- Low drive current
- High speed switching

Outline



Absolute Maximum Ratings (Ta = 25^{\circ}C)

Item	Symbol	Ratings	Unit	
Drain to source voltage	$V_{\scriptscriptstyle DSS}$	200	V	
Gate to source voltage	$V_{\sf GSS}$	±30	V	_
Drain current	I _D	(6)	Α	
Drain peak current	I Note 1	(24)	Α	
Body-drain diode reverse drain current	I _{DR}	(6)	Α	
Body-drain diode reverse drain peak current	I _{DR (pulse)} Note 1	(24)	А	
Channel dissipation	Pch Note 2	25	W	
Channel to case thermal impedance	θ ch-c	5	°C/W	
Channel temperature	Tch	150	°C	
Storage temperature	Tstg	-55 to +150	°C	

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

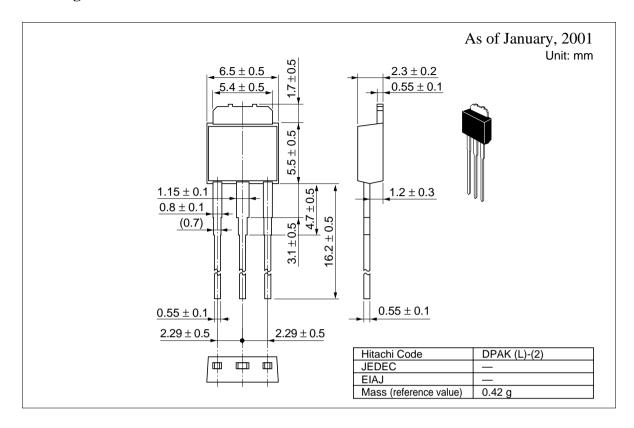
2. Value at Tc = 25°C

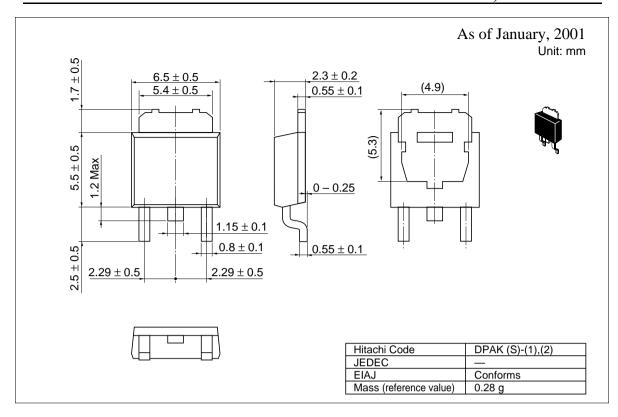
Electrical Characteristics (Ta = 25^{\circ}C)

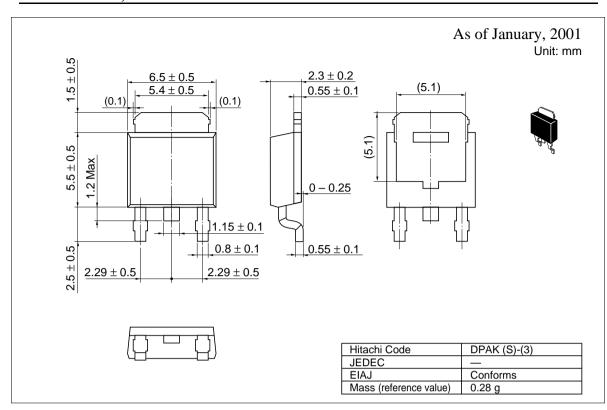
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	200	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source leak current	I _{GSS}	_	_	±0.1	μΑ	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I _{DSS}	_	_	1	μΑ	$V_{DS} = 200 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	(3.0)	_	(4.5)	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Static drain to source on state resistance	R _{DS(on)}	_	(0.52)	(0.65)	Ω	$I_D = 3 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note 4}}$
Forward transfer admittance	y _{fs}	(2.0)	(3.4)	_	S	$I_D = 3 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note 4}}$
Input capacitance	Ciss	_	(300)	_	pF	V _{DS} = 25 V
Output capacitance	Coss		(50)	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	(14)	_	pF	f = 1 MHz
Total Gate charge	Qg	_	(9.5)	_	nC	$V_{DD} = 160 \text{ V}$
Gate to source charge	Qgs	_	(1.8)	_	nC	V _{GS} = 10 V
Gateto drain charge	Qgd	_	(5.2)	_	nC	I _D = 6 A
Turn-on delay time	td(on)	_	(19)	_	ns	$I_D = 3 A$
Rise time	tr	_	(16)	_	ns	V _{GS} = 10 V
Turn-off delay time	td(off)	_	(44)	_	ns	$R_L = 33.3 \Omega$
Fall time	tf	_	(12)	_	ns	Rg = 10 Ω
Body-drain diode forward voltage	V_{DF}	_	(1.0)	(1.5)	V	$I_F = 6 A, V_{GS} = 0$
Body-drain diode reverse recovery time	trr	_	(90)	_	ns	$I_F = 6 \text{ A}, V_{GS} = 0$
Body-drain diode reverse recovery charge	Qrr		(300)		nC	diF/dt = 100 A/us

Note: 4. Pulse test

Package Dimensions







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