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Silicon N Channel MOS FET High Speed Power Switching



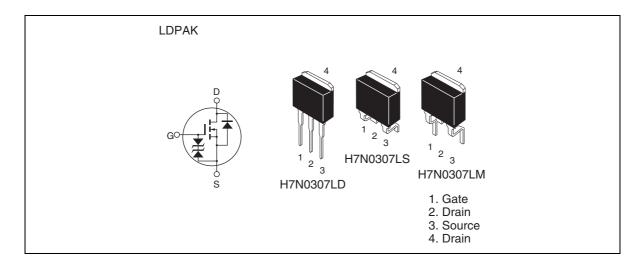
ADE-208-1516E(Z)

6th. Edition Aug. 2002

#### **Features**

- Low on-resistance
- $R_{DS(on)} = 4.6 \text{ m}\Omega \text{ typ.}$
- Low drive current
- 4.5 V gate drive device can be driven from 5 V source

#### **Outline**



## **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Ratings	Unit	
Drain to source voltage	V <sub>DSS</sub> 30		V	
Gate to source voltage	V <sub>GSS</sub>	±20	V	
Drain current	I <sub>D</sub>	60	Α	
Drain peak current	Note 1	240	Α	
Body-drain diode reverse drain current	I <sub>DR</sub>	60	Α	
Channel dissipation	Pch Note 2	90	W	_
Channel to Case Thermal Impedance	θch-c	1.39	°C/W	
Channel to Ambient Thermal Impedance	θch-a	89	°C/W	
Channel temperature	Tch	150	°C	
Storage temperature	Tstg	-55 to +150	°C	

Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1 %

2. Value at Tc = 25°C

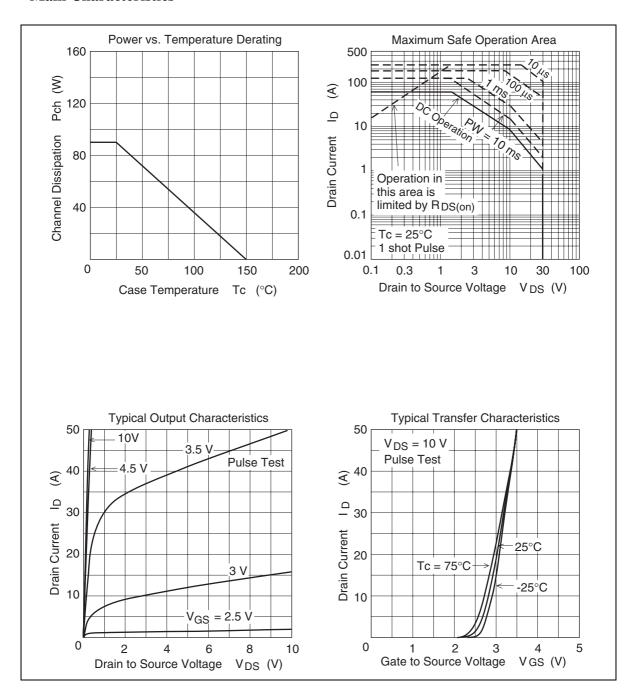
#### **Electrical Characteristics**

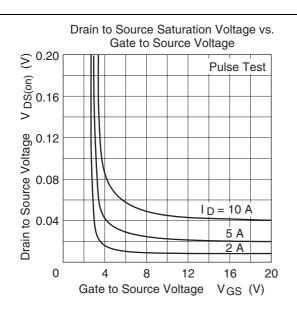
 $(Ta = 25^{\circ}C)$ 

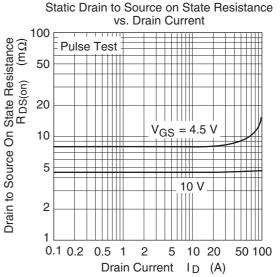
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{_{(BR)DSS}}$	30	_	_	V	$I_{D} = 10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	$V_{\text{(BR)GSS}}$	±20	_	_		$I_{G} = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I <sub>GSS</sub>	_	_	±10	μΑ	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	_	_	10	μΑ	$V_{DS} = 30 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{\text{GS(off)}}$	1.0	_	2.5	V	$I_D = 1 \text{ mA}, V_{DS} = 10 \text{ V}^{*1}$
Static drain to source on state	$R_{\scriptscriptstyle DS(on)}$	_	4.6	5.8	mΩ	$I_D = 30 \text{ A}, V_{GS} = 10 \text{ V}^{*1}$
resistance		_	8.0	11.5	mΩ	$I_D = 30 \text{ A}, V_{GS} = 4.5 \text{ V}^{*1}$
Forward transfer admittance	ly <sub>fs</sub> l	40	65	_	S	$I_D = 30 \text{ A}, V_{DS} = 10 \text{V}^{*1}$
Input capacitance	Ciss	_	2500	_	pF	V <sub>DS</sub> = 10 V
Output capacitance	Coss	_	650	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	350	_	pF	f = 1 MHz
Total gate charge	Qg	_	40	_	nc	V <sub>DD</sub> = 10 V
Gate to source charge	Qgs	_	7	_	nc	$V_{GS} = 10 \text{ V}$
Gate to drain charge	Qgd		8	_	nc	$I_{D} = 60 \text{ A}$
Turn-on delay time	t <sub>d(on)</sub>	_	20	_	ns	$V_{GS} = 10 \text{ V}, I_{D} = 30 \text{A}$
Rise time	t <sub>r</sub>		300	_	ns	$R_L = 0.33 \Omega$
Turn-off delay time	$t_{\text{d(off)}}$		70		ns	$R_g = 4.7 \Omega$
Fall time	t <sub>f</sub>	_	20	_	ns	<del>_</del>
Body-drain diode forward voltage	V <sub>DF</sub>	_	0.92	_	V	$I_{F} = 60 \text{ A}, V_{GS} = 0$
Body-drain diode reverse recovery time	t <sub>rr</sub>	_	60	_	ns	$I_{F} = 60 \text{ A}, V_{GS} = 0$ diF/ dt = 50 A/ $\mu$ s

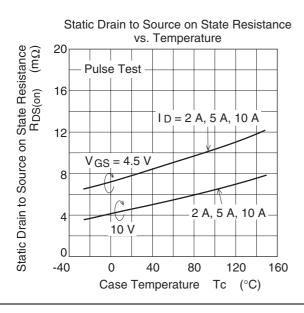
Notes: 1. Pulse test

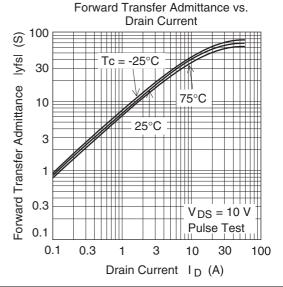
#### **Main Characteristics**

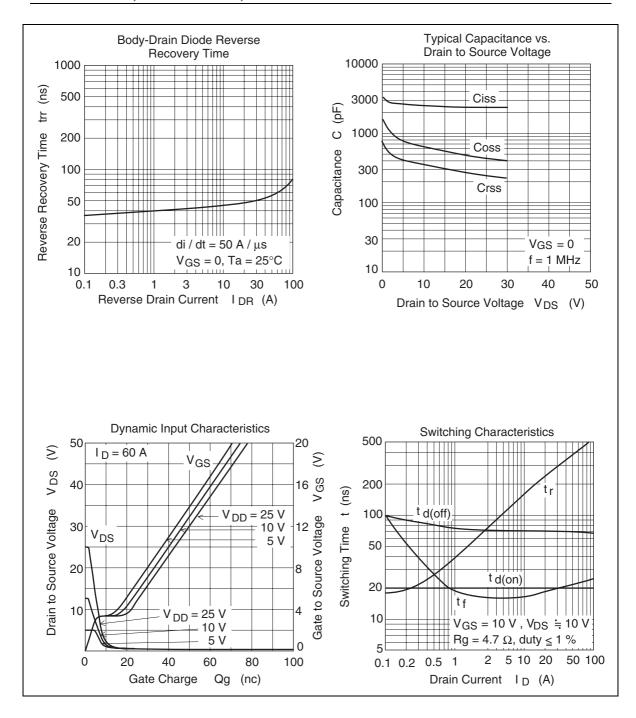


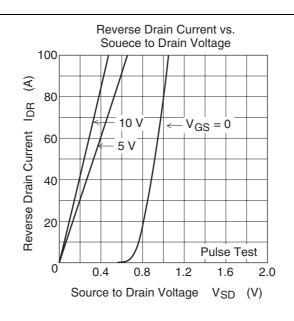


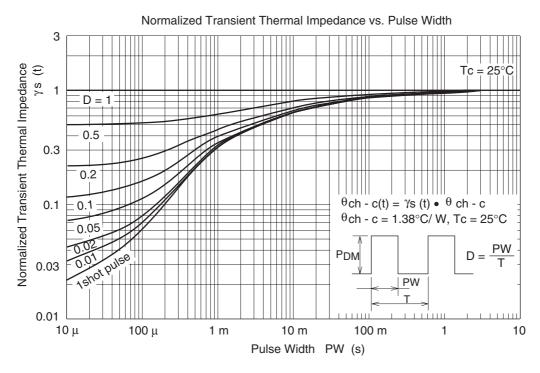


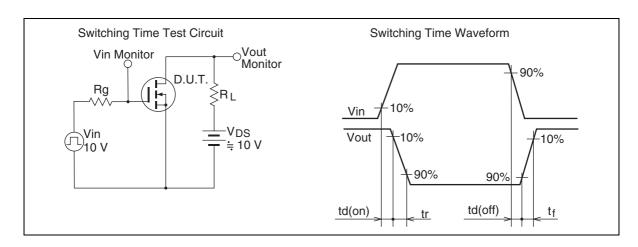




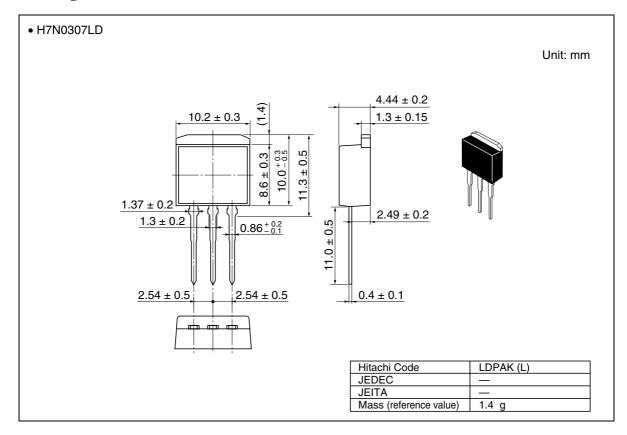


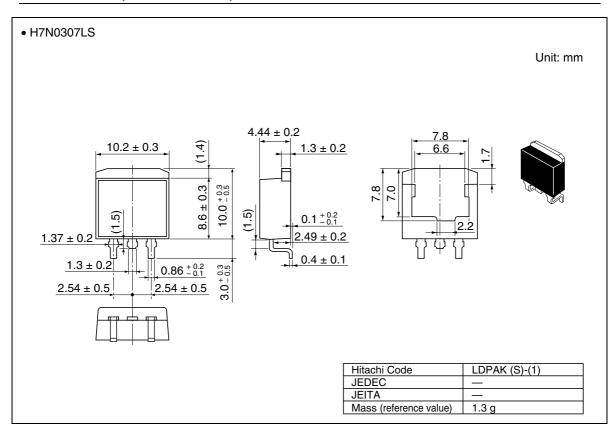


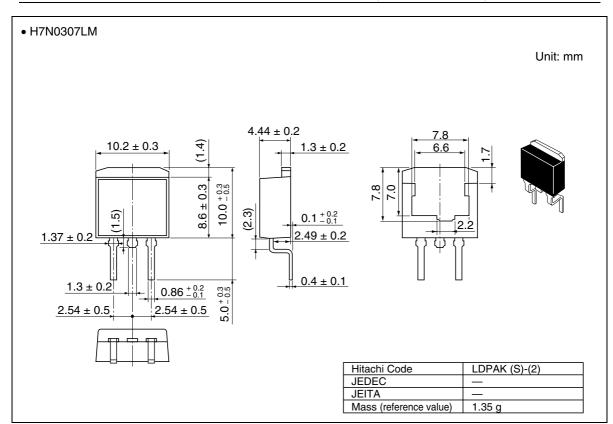




### **Package Dimensions**







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