HVL375CM

Variable Capacitance Diode for VCO

REJ03G0228-0100Z
Rev.1.00
Apr 28, 2004

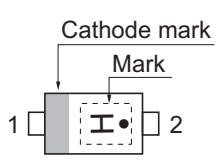
Features

- Narrow terminal Capacitance deviation.
- Low series resistance. (r_s = 1.1 Ω max)
- Good C-V linearity.
- Thin Extremely small Flat Package (TEFP) is suitable for surface mount design.

Ordering Information

Type No.	Laser Mark	Package Code
HVL375CM	H	TEFP

Pin Arrangement



1. Cathode
2. Anode

Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Reverse voltage	V_R	10	V
Junction temperature	Tj	125	°C
Storage temperature	Tstg	–55 to +125	°C

Electrical Characteristics

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse current	I_{R1}	—	—	10	nA	V_R = 10 V
	I_{R2}	—	—	100		V_R = 10 V, Ta = 60°C
Capacitance	C_1	15.0	—	16.5	pF	V_R = 1 V, f = 1 MHz
	C_3	5.0		6.0		V_R = 3 V, f = 1 MHz
	C_4	3.3	—	4.0		V_R = 4 V, f = 1 MHz
Capacitance ratio	n	4.0	—	—	—	C_1 / C_4
Series resistance	r_S	—	—	1.1	Ω	V_R = 2 V, f = 470 MHz

Notes: 1. Please do not use the soldering iron due to avoid high stress to the TEFP package.
2. The material of lead is exposed for cutting plane. There for, soldering nature of lead tip part is considered as unquestioned. Please kindly consider soldering nature.

Main Characteristic

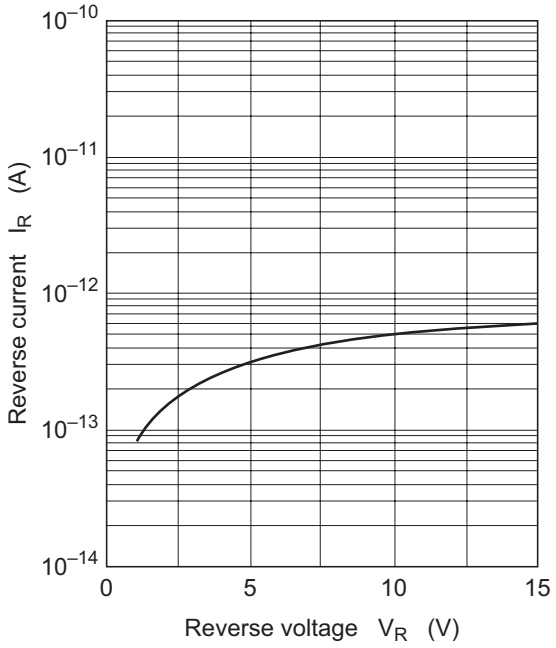


Fig.1 Reverse current vs. Reverse voltage

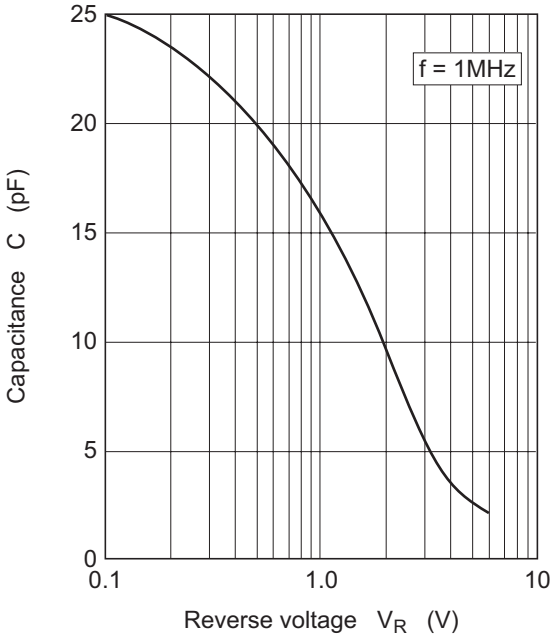


Fig.2 Capacitance vs. Reverse voltage

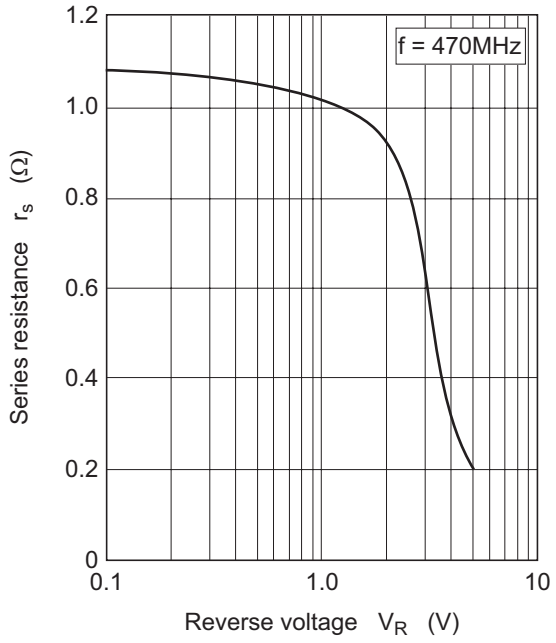


Fig.3 Series resistance vs. Reverse voltage

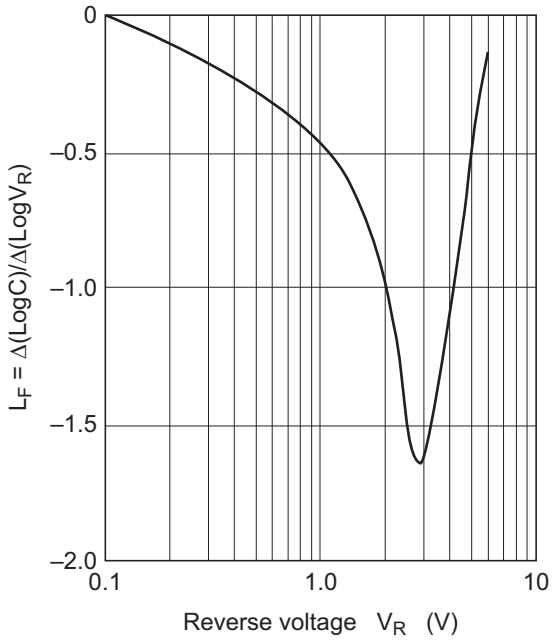
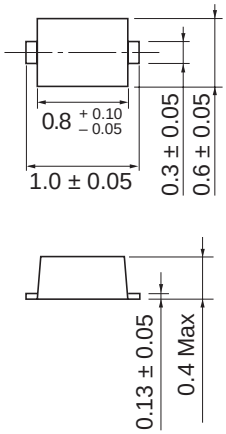


Fig.4 L_F vs. Reverse voltage

Package Dimensions

As of January, 2003

Unit: mm



Package Code	TEFP
JEDEC	—
JEITA	—
Mass (reference value)	0.0006 g

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