

QUICKSWITCH® PRODUCTS HIGH-SPEED CMOS 18-BIT BUS EXCHANGE SWITCH

IDTQS3162209

FEATURES:

- Enhanced N channel FET with no inherent diode to Vcc
- Low propagation delay
- TTL-compatible input and output levels
- Undershoot clamp diodes on all switch and control pins
- Available in 48-pin SSOP and TSSOP Packages

APPLICATIONS

- Resource sharing
- Crossbar switching
- Hot-swapping, hot-docking
- Bus isolation
- Voltage translation (5V to 3.3V)

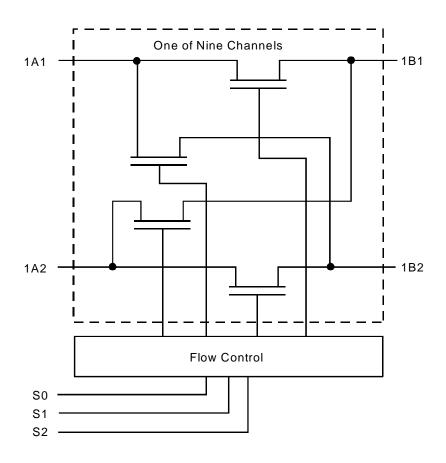
DESCRIPTION:

The QS3162209 provides a set of 18 high-speed CMOS TTL-compatible bus-exchange switches. This device operates as an 18-bit bus switch or a 9-bit bus exchanger, which provides data exchanging between the four signal ports through the data-select (S0-S2) terminals.

The QS3162209 adds an internal 25 Ω series termination resistor to reduce reflection noise in high speed applications

The QS3162209 is characterized for operation at -40°C to +85°C.

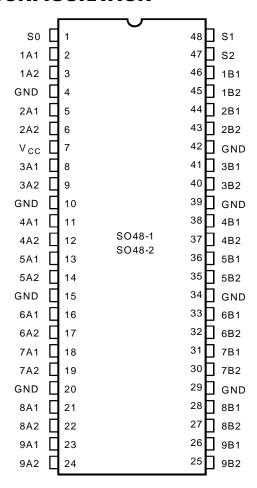
FUNCTIONAL BLOCK DIAGRAM



INDUSTRIAL TEMPERATURE RANGE

NOVEMBER 1999

PIN CONFIGURATION



SSOP/ TSSOP TOP VIEW

ABSOLUTE MAXIMUM RATINGS (1)

Symbol	Description	Max.	Unit
VTERM ⁽²⁾	Supply Voltage to Ground	- 0.5 to +7	V
VTERM ⁽³⁾	DC Switch Voltage Vs	- 0.5 to +7	V
VTERM ⁽³⁾	DC Input Voltage VIN	- 0.5 to +7	V
VAC	AC Input Voltage (pulse width ≤20ns)	-3	V
Гоит	DC Output Current	120	mA
Рмах	Maximum Power Dissipation (T _A = 85°C)	0.85	W
Tstg	Storage Temperature	- 65 to +150	°C

NOTES:

- Stresses greater than those listed under ABSOLUTE MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.
- 2. Vcc Terminals.
- 3. All terminals except Vcc.

CAPACITANCE

 $(TA = +25^{\circ}C, f = 1.0MHz, VIN = 0V, VOUT = 0V)$

Pins	Тур.	Max. (1)	Unit
Control Inputs	4	5	pF
Quickswitch Channels (Switch OFF)	7.5	9	pF

NOTE:

1. This parameter is guaranteed but not production tested.

PIN DESCRIPTION

Pin Names	I/O	Description
1An - 9An	I/O	Bus A
1Bn - 9Bn	I/O	Bus B
So - S2	I	Data Select

FUNCTION TABLE(1)

S ₂	S ₁	S ₀	xA1	xA2	Function	
L	L	L	Z	Z	Disconnect	
L	L	Η	xB1	Z	xA1 to xB1	
L	Н	L	xB2	Z	xA1 to xB2	
L	Н	Н	Z	хВ1	xA2 to xB1	
Н	L	L	Z	xB2	xA2 to xB2	
Н	L	Н	Z	Z	Disconnect	
Н	Н	L	xB1	xB2	xA1 to xB1, xA2 to xB2	
Н	Н	Н	xB2	xB1	xA1 to xB2, xA2 to xB1	

NOTE:

- 1. H = HIGH Voltage Level
 - L = LOW Voltage Level
 - Z = High-Impedence

DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE

Following Conditions Apply Unless Otherwise Specified:

Industrial: TA = -40°C to +85°C, Vcc = 5.0V \pm 10%

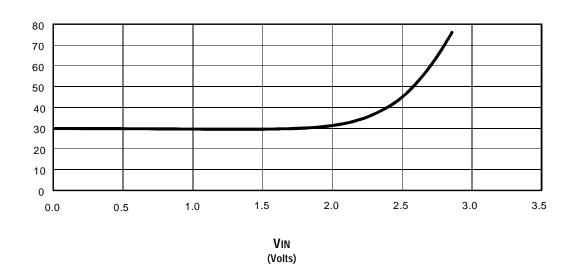
Symbol	Parameter	Test Conditions	Min.	Typ. ⁽¹⁾	Max.	Unit
ViH	Input HIGH Voltage	Guaranteed Logic HIGH for Control Pins	2	_	-	V
VIL	Input LOW Voltage	Guaranteed Logic LOW for Control Pins	_	_	0.8	V
lin	Input Leakage Current (Control Inputs)	0V ≤ VIN ≤ Vcc, Control Inputs	_	0.01	±1	μА
loz	Off-State Current (Hi-Z)	0V ≤ Vouт ≤ Vcc, Switches OFF	_	0.01	±1	μΑ
Ron	Switch ON Resistance	Vcc = Min., VIN = 0V, ION = 30mA	22	30	42	Ω
Ron	Switch ON Resistance	Vcc = Min., VIN = 2.4V, ION = 15mA	22	37	50	Ω
V P	Pass Voltage (2)	Vcc = 5V, IoυT = -5μA	3.7	4	4.2	V

NOTES:

- 1. Typical values are at Vcc = 5.0V, Ta = 25°C.
- 2. Pass voltage is guaranteed but not production tested.

TYPICAL ON RESISTANCE vs Vin AT Vcc = 5V





POWER SUPPLY CHARACTERISTICS

Symbol	Parameter	Test Conditions ⁽¹⁾	Max.	Unit
Icco	Quiescent Power Supply Current	Vcc = Max., Vin = GND or Vcc, f = 0	3	μΑ
Δlcc	Power Supply Current per Control Input HIGH (2)	Vcc = Max., Vin = 3.4V, f = 0	1.5	mA
ICCD	Dynamic Power Supply Current per MHz ⁽³⁾	Vcc = Max., A and B pins open	0.25	mA/MHz
		Control Input Toggling at 50% Duty Cycle		

NOTES:

- 1. For conditions shown as Min. or Max., use the appropriate values specified under DC Electrical Characteristics.
- 2. Per TLL driven input (VIN = 3.4V, control inputs only). A and B pins do not contribute to ∆Icc.
- 3. This current applies to the control inputs only and represents the current required to switch internal capacitance at the specified frequency. The A and B inputs generate no significant AC or DC currents as they transition. This parameter is guaranteed but not production tested.

SWITCHING CHARACTERISTICS OVER OPERATING RANGE

 $T_A = -40^{\circ}C \text{ to } +85^{\circ}C, V_{CC} = 5.0V \pm 10\%$

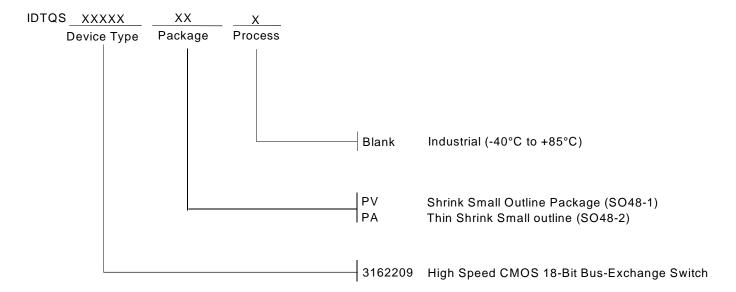
CLOAD = 50pF, RLOAD = 500Ω unless otherwise noted.

Symbol	Parameter	Min. ⁽¹⁾	Тур.	Max.	Unit
tplh	Data Propagation Delay (2,4)	_	_	1.25 ⁽³⁾	
t PHL	xAn to xBn, xBn to xAn				ns
tpzl	Switch Turn-on Delay	1 5	_	7.5	
tpzh	Sn to xAn, xBn	1.5		7.5	ns
tplz	Switch Turn-off Delay ⁽²⁾	1 5	_	6.8	
tphz	Sn to xAn, xBn	1.5		0.0	ns

NOTES:

- 1. Minimums are guaranteed but not production tested.
- 2. This parameter is guaranteed but not production tested.
- 3. The time constant for the switch alone is of the order of 1.25ns for CL = 50pF.
- 4. The bus switch contributes no propagation delay other than the RC delay of the ON resistance of the switch and the load capacitance. Since this time constant is much smaller than the rise and fall times of typical driving signals, it adds very little propagation delay to the system. Propagation delay of the bus switch, when used in a system, is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

ORDERING INFORMATION





CORPORATE HEADQUARTERS 2975 Stender Way Santa Clara, CA 95054 for SALES: 800-345-7015 or 408-727-6116 fax: 408-492-8674 www.idt.com*

*To search for sales office near you, please click the sales button found on our home page or dial the 800# above and press 2.

The IDT logo, QuickSwitch, and SynchroSwitch are registered trademarks of Integrated Device Technology, Inc.