



QUICKSWITCH® PRODUCTS 3.3V 16-BIT BUS SWITCH FOR HOT SWAP APPLICATIONS (HOTSWITCH™)

IDTQS32XVH245

FEATURES:

- N channel FET switches with no parasitic diode to Vcc
 - No DC path to Vcc or GND
 - 5V tolerant in OFF and ON state
- 5V tolerant I/Os
- Low RON - 4Ω typical
- Flat RON characteristics from 0 - 5V
- Rail-to-rail switching 0 - 5V
- Bidirectional dataflow with near-zero delay: no added ground bounce
- Excellent RON matching between channels
- Vcc operation: 2.3V to 3.6V
- High bandwidth - up to 500MHz
- LVTTTL-compatible control Inputs
- Undershoot Clamp Diodes on all switch and control Inputs
- Low I/O capacitance, 5pF typical
- Available in 40-pin QVSOP package

APPLICATIONS:

- PCI/Compact PCI hot-swapping
- 10/100 Base-T, Ethernet LAN switch
- Low distortion analog switch
- Replaces mechanical relay
- ATM 25/155 switching

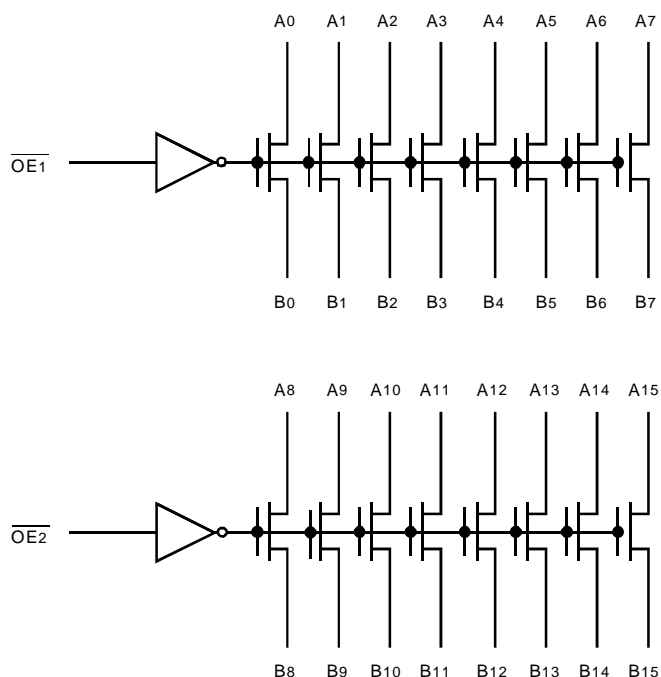
DESCRIPTION:

The QS32XVH245 HotSwitch 16-bit bus switch is specially designed for a hot-swapping environment. The QS32XVH245 has very low ON resistance, resulting in under 250ps propagation delay through the switch. The switches can be turned ON under the control of the LVTTTL-compatible Output Enable (\overline{OE}) signal for bidirectional data flow with no added delay or ground bounce. In the OFF and ON states, the switches are 5V-tolerant. In the OFF state, the switches offer very high impedance at the terminals.

The combination of near-zero propagation delay, high OFF impedance, and over-voltage tolerance makes the QS32XVH245 ideal for hot-swapping applications. The low ON resistance of the QS32XVH245 makes it ideal for PCI and Compact PCI hot-swapping environments.

The QS32XVH245 is characterized for operation from -40°C to +85°C.

FUNCTIONAL BLOCK DIAGRAM

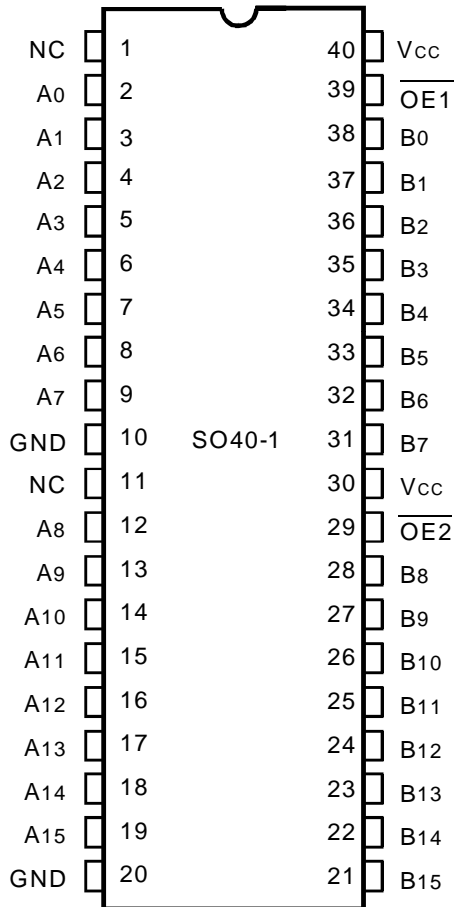


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INDUSTRIAL TEMPERATURE RANGE

OCTOBER 2001

PIN CONFIGURATION



QVSOP
TOP VIEW

ABSOLUTE MAXIMUM RATINGS⁽¹⁾

Symbol	Description	Max	Unit
VTERM ⁽²⁾	Supply Voltage to Ground	-0.5 to +4.6	V
VTERM ⁽³⁾	DC Switch Voltage V _S	-0.5 to +5.5	V
VTERM ⁽³⁾	DC Input Voltage V _{IN}	-0.5 to +5.5	V
VAC	AC Input Voltage (pulse width ≤20ns)	-3	V
VOUT	DC Output Current	120	mA
P _{MAX}	Maximum Power Dissipation	0.6	W
T _{STG}	Storage Temperature	-65 to +150	°C

NOTES:

- Stresses greater than those listed under ABSOLUTE MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.
- V_{CC} terminals.
- All terminals except V_{CC}.

CAPACITANCE (T_A = +25°C, F = 1MHz, V_{IN} = 0V, V_{OUT} = 0V)

Symbol	Parameter ⁽¹⁾	Typ.	Max.	Unit
C _{IN}	Control Inputs	3	5	pF
C _{I/O}	Quickswitch Channels (Switch OFF)	5	6	pF

NOTE:

- This parameter is guaranteed but not production tested.

PIN DESCRIPTION

Pin Names	I/O	Description
$\overline{OE}x$	I	Output Enable
A _x	I/O	Bus A
B _x	I/O	Bus B

FUNCTION TABLE⁽¹⁾

$\overline{OE}1$	$\overline{OE}2$	A ₀ - A ₇	A ₈ - A ₁₅	Function
H	H	Z	Z	Disconnect
L	H	B ₀ - B ₇	Z	Connect
H	L	Z	B ₈ - B ₁₅	Connect
L	L	B ₀ - B ₇	B ₈ - B ₁₅	Connect

NOTE:

- H = HIGH Voltage Level
L = LOW Voltage Level
Z = High Impedance

DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE

Following Conditions Apply Unless Otherwise Specified:

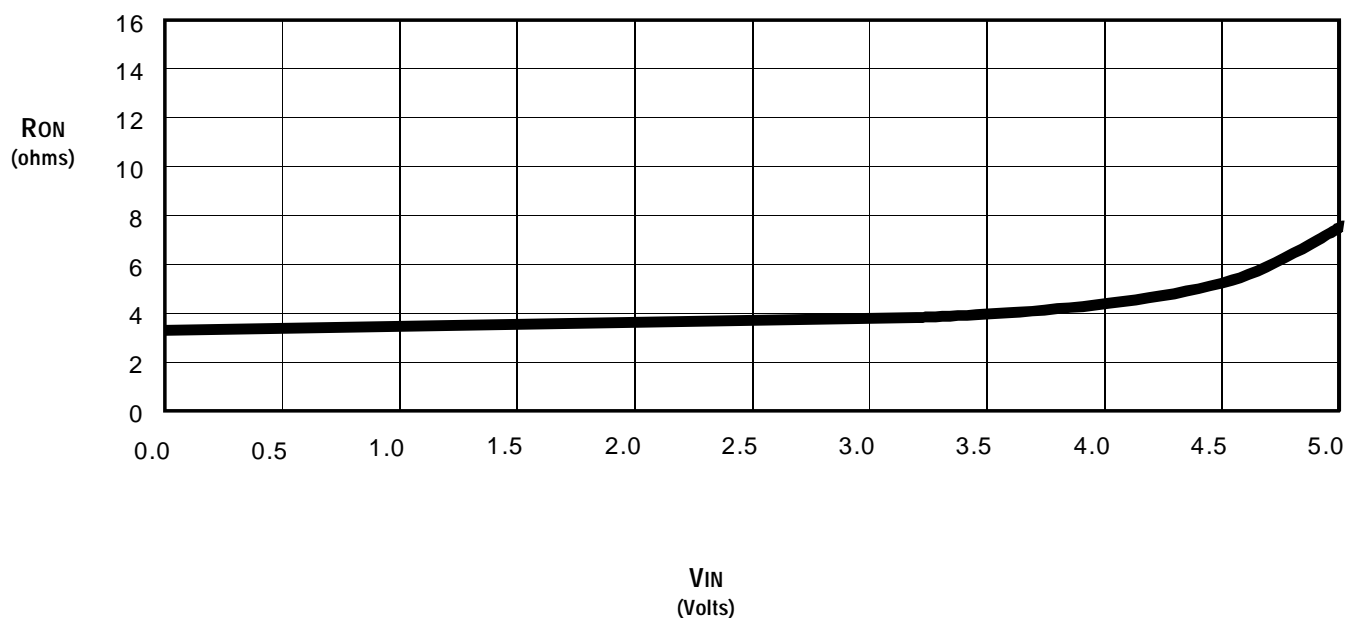
Industrial: $T_A = -40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$, $V_{CC} = 3.3\text{V} \pm 0.3\text{V}$

Symbol	Parameter	Test Conditions	Min.	Typ. ⁽¹⁾	Max.	Unit
V_{IH}	Input HIGH Voltage	Guaranteed Logic HIGH for Control Inputs	2	—	—	V
V_{IL}	Input LOW Voltage	Guaranteed Logic LOW for Control Inputs	—	—	0.8	V
I_{IN}	Input Leakage Current (Control Inputs)	$0\text{V} \leq \overline{OE} \leq V_{CC}$	—	—	± 1	μA
I_{OZ}	Off-State Current (Hi-Z)	$0\text{V} \leq A, B \leq V_{CC}$, Switches OFF	—	—	± 1	μA
R_{ON}	Switch ON Resistance ⁽²⁾	$V_{CC} = \text{Min}$, $V_{IN} = 0\text{V}$, $I_{ON} = 30\text{mA}$	—	4	6	Ω
		$V_{CC} = \text{Min}$, $V_{IN} = 2.4\text{V}$, $I_{ON} = 15\text{mA}$	—	5	8	

NOTES:

- Typical values are at $V_{CC} = 3.3\text{V}$ and $T_A = 25^{\circ}\text{C}$.
- R_{ON} guaranteed but not production tested.

TYPICAL ON RESISTANCE vs V_{IN} AT $V_{CC} = 3.3\text{V}$



POWER SUPPLY CHARACTERISTICS

Symbol	Parameter	Test Conditions ⁽¹⁾	Max.	Unit
I _{CCQ}	Quiescent Power Supply Current	V _{CC} = Max., V _{IN} = GND or V _{CC} , f = 0	6	mA
ΔI _{CC}	Power Supply Current ^(2,3) per Input HIGH	V _{CC} = 3.6V, V _{IN} = 3V, f = 0 per Control Input	30	μA
I _{CCD}	Dynamic Power Supply Current per MHz ⁽⁴⁾	V _{CC} = 3.6V, A and B Pins Open, per Control Input Toggling @ 50% Duty Cycle	0.25	mA/MHz

NOTES:

- For conditions shown as Min. or Max., use the appropriate values specified under DC Electrical Characteristics.
- Per LVTTTL-driven-control-input. A and B pins do not contribute to ΔI_{CC}.
- This parameter is guaranteed but not tested.
- This parameter represents the current required to switch internal capacitance at the specified frequency. The A and B inputs do not contribute to the Dynamic Power Supply Current. This parameter is guaranteed but not production tested.

SWITCHING CHARACTERISTICS OVER OPERATING RANGE

T_A = -40°C to +85°C, V_{CC} = 3.3V ± 0.3V

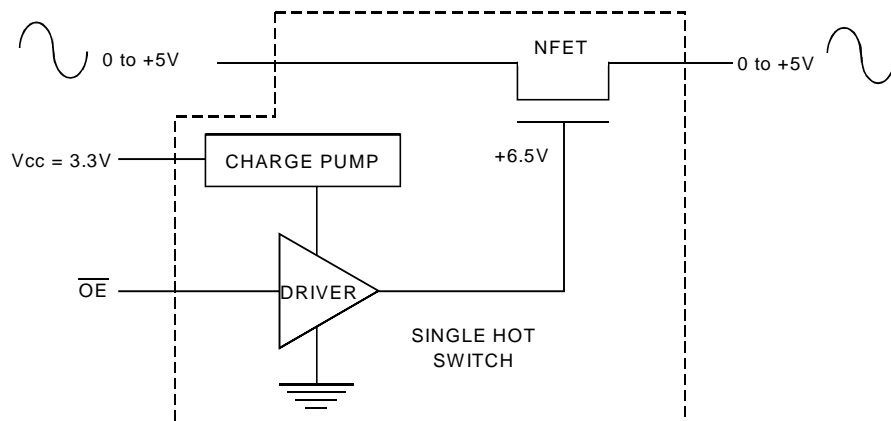
C_{LOAD} = 50pF, R_{LOAD} = 500Ω, unless otherwise noted

Symbol	Parameter	Min. ⁽³⁾	Typ.	Max.	Unit
t _{PLH} t _{PHL}	Data Propagation Delay ^(1,2) A _x to/from B _x	—	—	0.25	ns
t _{PZL} t _{PZH}	Switch Turn-On Delay $\overline{\text{OEx}}$ to A _x /B _x	1.5	—	9	ns
t _{PLZ} t _{PHZ}	Switch Turn-Off Delay ⁽¹⁾ $\overline{\text{OEx}}$ to A _x /B _x	1.5	—	8	ns
f _{OEx}	Operating Frequency - Enable ^(1,4)	—	—	1	MHz

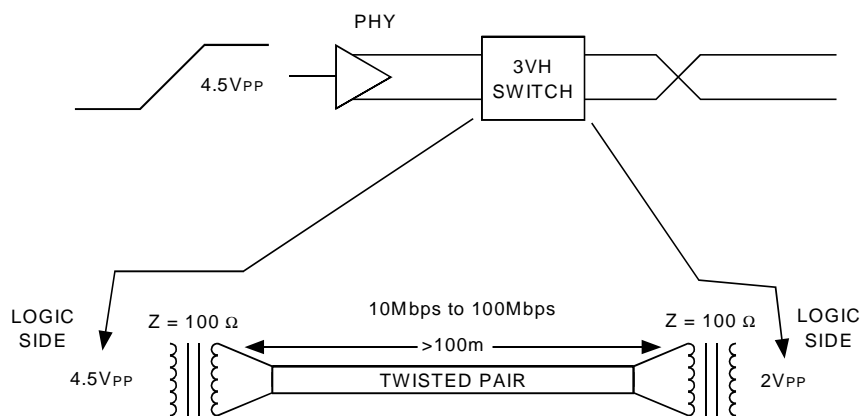
NOTES:

- This parameter is guaranteed but not production tested.
- The bus switch contributes no propagation delay other than the RC delay of the ON resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns at C_L = 50pF. Since this time constant is much smaller than the rise and fall times of typical driving signals, it adds very little propagation delay to the system. Propagation delay of the bus switch, when used in a system, is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.
- Minimums are guaranteed but not production tested.
- Maximum toggle frequency for $\overline{\text{OEx}}$ control input.

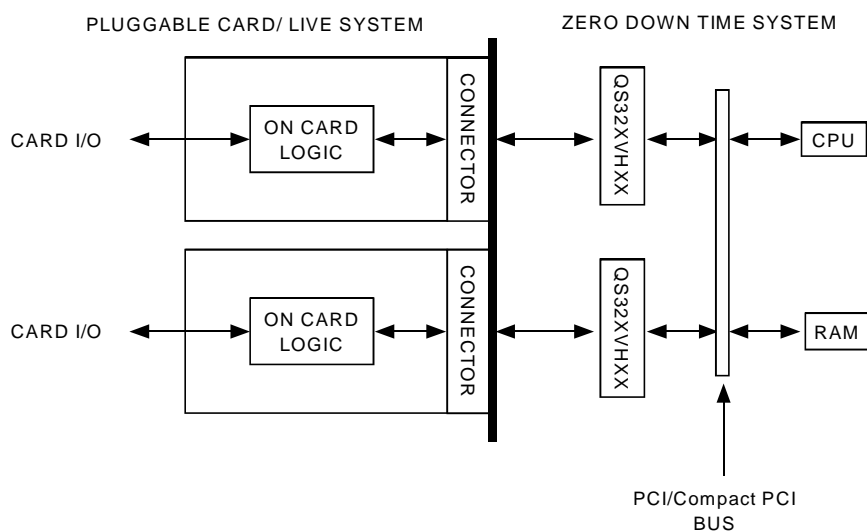
SOME APPLICATIONS FOR HOTSWITCH PRODUCTS



Rail-to-Rail Switching



Fast Ethernet Data Switching (LAN Switch)



Hot-Swapping: PCI / Compact PCI

ORDERING INFORMATION

IDTQS	<u>XXXXX</u>	<u>XX</u>		
	Device Type	Package		
			Q2	40-Pin QVSOP (SO40-1)
			32XVH245	3.3V 16-Bit Bus Switch for Hot Swap Applications



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