## FEATURES:

- $\quad \mathrm{N}$ channel FET switches with no parasitic diode to Vcc
- No DC path to Vcc or GND
- 5 V tolerant in OFF and ON state
- 5 V tolerant I/Os
- Low Ron - $4 \Omega$ typical
- Flat RoN characteristics from 0-5V
- Rail-to-rail switching 0-5V
- Bidirectional dataflow with near-zero delay: no added ground bounce
- Excellent Ron matching between channels
- Vcc operation: 2.3V to 3.6 V
- High bandwidth - up to 500 MHz
- LVTTL-compatible control Inputs
- Undershoot Clamp Diodes on all switch and control Inputs
- Low I/O capacitance, 4 pF typical
- Available in QSOP and SOIC packages


## APPLICATIONS:

- PCI/Compact PCI hot-swapping
- 10/100 Base-T, Ethernet LAN switch
- Low distortion analog switch
- Replaces mechanical relay
- ATM 25/155 switching


## DESCRIPTION:

The QS3VH125 HotSwitch Quad bus switch is specially designed for a hot-swapping environment. The QS3VH125 has very low ON resistance, resulting in under 250ps propagation delay through the switch. The switches can be turned ON under the control of individual LVTTLcompatible active low Output Enable signals for bidirectional data flow with no added delay or ground bounce. In the OFF and ON states, the switches are 5V-tolerant. In the OFF state, the switches offer very high impedence at the terminals.

The combination of near-zero propagation delay, high OFF impedance, and over-voltage tolerance makes the QS3VH125 ideal for hot-swapping applications.

The QS3VH125 is characterized for operation from $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$.

FUNCTIONAL BLOCK DIAGRAM


## PIN CONFIGURATION



SOIC TOP VIEW

## ABSOLUTE MAXIMUM RATING(1)

| Symbol | Description | Max. | Unit |
| :---: | :--- | :---: | :---: |
| VTERM $^{(2)}$ | Supply Voltage to Ground | -0.5 to 4.6 | V |
| VTERM $^{(3)}$ | DC Switch Voltage Vs | -0.5 to 5.5 | V |
| VTERM $^{(3)}$ | DC Input Voltage VIN | -0.5 to 5.5 | V |
| VAC $^{2}$ | AC Input Voltage (pulse width $\leq 20 \mathrm{~ns})$ | -3 | V |
| Vout | DC Output Current | 120 | mA |
| TsTG | Storage Temperature | -65 to 150 | ${ }^{\circ} \mathrm{C}$ |

NOTES:

1. Stresses greater than those listed under ABSOLUTE MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.
2. Vcc terminals.
3. All terminals except Vcc.

CAPACITANCE $\left(\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}, \mathrm{f}=1 \mathrm{MHz}, \mathrm{Vin}=\mathrm{OV}\right.$, $\left.\mathrm{Vout}=\mathrm{OV}\right)$

| Symbol | Parameter(1) | Typ. | Max. | Unit |
| :--- | :--- | :---: | :---: | :---: |
| CIN | Control Inputs | 3 | 5 | pF |
| $\mathrm{C} / 0$ | Quickswitch Channels (Switch OFF) | 4 | 6 | pF |

NOTE:

1. This parameter is guaranteed but not production tested.

## PIN DESCRIPTION

| Pin Names | $\mathrm{I} / 0$ | Description |
| :---: | :---: | :--- |
| $1 \mathrm{~A}-4 \mathrm{~A}$ | $\mathrm{I} / \mathrm{O}$ | Bus A |
| $1 \mathrm{Y}-4 \mathrm{Y}$ | $\mathrm{I} / 0$ | Bus Y |
| $\overline{1 O E}-\overline{4 O E}$ | I | Output Enable |

FUNCTION TABLE (1)

| $\overline{\mathbf{O E}}$ | $\mathbf{A}$ | $\mathbf{Y}$ | Function |
| :---: | :---: | :---: | :--- |
| L | H | H | Connect |
| L | L | L | Connect |
| $H$ | X | X | Disconnect |

## NOTE:

1. $\mathrm{H}=\mathrm{HIGH}$ Voltage Level

L = LOW Voltage Level
X = Don't Care

## DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE

Following Conditions Apply Unless Otherwise Specified:
Industrial: $\mathrm{TA}=-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}, \mathrm{Vcc}=3.3 \mathrm{~V} \pm 0.3 \mathrm{~V}$

| Symbol | Parameter | Test Conditions | Min. | Typ. ${ }^{(1)}$ | Max. | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| VIH | Input HIGH Voltage | Guaranteed Logic HIGH for Control Inputs | 2 | - | - | V |
| VIL | Input LOW Voltage | Guaranteed Logic LOW for Control Inputs | - | - | 0.8 | V |
| In | Input Leakage Current (Control Inputs) | $\mathrm{OV} \leq \mathrm{VIN} \leq \mathrm{Vcc}$ | - | - | $\pm 1$ | $\mu \mathrm{A}$ |
| IOz | Off-State Current (Hi-Z) | OV $\leq$ Vout $\leq \mathrm{Vcc}$, Switches OFF | - | - | $\pm 1$ | $\mu \mathrm{A}$ |
| Ron | Switch ON Resistance ${ }^{(2)}$ | $\mathrm{VcC}=\mathrm{Min} ., \mathrm{VIN}=0 \mathrm{~V}$, ION $=30 \mathrm{~mA}$ | - | 4 | 6 | $\Omega$ |
|  |  | $\mathrm{VCC}=\mathrm{Min} ., \mathrm{VIN}=2.4 \mathrm{~V}$, $\mathrm{ION}=15 \mathrm{~mA}$ | - | 5 | 8 |  |

## NOTES:

1. Typical values are at $\mathrm{Vcc}=3.3 \mathrm{~V}$ and $\mathrm{TA}=25^{\circ} \mathrm{C}$.
2. Ron guaranteed but not production tested.

TYPICAL ON RESISTANCE vs Vin AT Vcc = 3.3V


## POWER SUPPLY CHARACTERISTICS

| Symbol | Parameter | Test Conditions ${ }^{(1)}$ | Max. | Unit |
| :--- | :--- | :--- | :---: | :---: |
| ICCQ | Quiescent Power Supply Current | VCC = Max., VIN $=$ GND or Vcc, $f=0$ | 3 | mA |
| $\Delta \mathrm{ICC}$ | Power Supply Current ${ }^{2,3)}$ per Input HIGH | VCC = Max., VIN = 3V, $\mathrm{f}=0$ per Control Input | 30 | $\mu \mathrm{~A}$ |
| ICCD | Dynamic Power Supply Current per MHz ${ }^{(4)}$ | VCC = Max., A and Y Pins Open, Control Inputs Toggling @ $50 \%$ Duty <br> Cycle | 0.25 | $\mathrm{~mA} / \mathrm{MHz}$ |

## NOTES:

1. For conditions shown as Min. or Max., use the appropriate values specified under DC Electrical Characteristics.
2. Per LVTLL driven input. A and $Y$ pins do not contribute to $\Delta l c c$.
3. This parameter is guaranteed but not tested.
4. This parameter represents the current required to switch internal capacitance at the specified frequency. The $A$ and $Y$ inputs do not contribute to the Dynamic Power Supply Current. This parameter is guaranteed but not production tested.

## SWITCHING CHARACTERISTICS OVER OPERATING RANGE

TA $=-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$

| Symbol | Parameter | Min. ${ }^{(3)}$ | Typ. | Max. | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: |
| tpLH tPHL | Data Propagation Delay ${ }^{(1,2)}$ A to Y | - | - | 0.25 | ns |
| $\begin{aligned} & \text { tpzH } \\ & \text { tPZL } \end{aligned}$ | Switch Turn-On Delay $\overline{\mathrm{OE}}$ to $\mathrm{nA} / \mathrm{nY}$ | 1.5 | - | 9 | ns |
| $\begin{aligned} & \text { tPHz } \\ & \text { tPLZ } \end{aligned}$ | Switch Turn-Off Delay ${ }^{(1)}$ $\overline{\mathrm{OE}}$ to $\mathrm{nA} / \mathrm{nY}$ | 1.5 | - | 8 | ns |
| foE | Operating Frequency - Enable ${ }^{(1,4)}$ | - | - | 1 | MHz |

## NOTES:

1. This parameter is guaranteed but not production tested.
2. The bus switch contributes no propagation delay other than the RC delay of the ON resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25 ns at $\mathrm{CL}=50 \mathrm{pF}$. Since this time constant is much smaller than the rise and fall times of typical driving signals, it adds very little propagation delay to the system. Propagation delay of the bus switch, when used in a system, is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.
3. Minimums are guaranteed but not production tested.
4. Maximum toggle frequency for $\overline{\mathrm{OE}}$ control input.

## SOME APPLICATIONS FOR HOTSWITCH PRODUCTS

## RAIL-TO-RAILSWITCHING



FAST ETHERNET DATA SWITCHING (LAN SWITCH)


## HOT SWAPPING: PCI/COMPACT PCI



## ORDERING INFORMATION


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800-345-7015 or 408-727-6116 fax: 408-492-8674 www.idt.com
for Tech Support: logichelp@idt.com (408) 654-6459

