

QUICKSWITCH® PRODUCTS HIGH-PERFORMANCE CMOS ANALOG FOUR-CHANNEL SPST SWITCH WITH INDIVIDUAL ENABLES

IDTQS4A101

FEATURES:

- Low ON resistance: $rDS(ON) = 5\Omega$
- Wide bandwidth: 1.4GHz (-3dB point)
- Crosstalk:
 -122dB at 50KHz, -80dB at 5MHz, -65dB at 30MHz
- Off-isolation:
 -100dB at 50KHz, -75dB at 5MHz, -45dB at 30MHz
- Single 5V supply
- Bidirectional signal flow
- TTL-compatible control inputs
- Ultra-low quiescent current: 3µA
- Switch turn on time of 6.5ns

APPLICATIONS

- High-speed video signal switching/routing
- Audio signal switching/routing
- Data acquisition
- ATE systems
- Telecomm routing
- Token Ring transceivers
- High-speed networking

FUNCTIONAL BLOCK DIAGRAM

DESCRIPTION:

The QS4A101 is a high-performance CMOS analog four-channel SPST switch with individual enables. This device provides a set of four high-speed CMOS switches connecting inputs to outputs. The low ON resistance of the QS4A101 allows inputs to be connected to outputs with low insertion loss and high bandwidth.

The QS4A101, with 1.4GHz bandwidth, is ideal for high-performance video signal switching, audio signal switching, and telecomm routing applications. Low power dissipation makes this device ideal for battery operated and remote instrumentation applications.

The QS4A101 is offered in the QSOP package which has several advantages over conventional packages such as PDIP and SOIC, including:

- Reduced signal delays due to denser component packaging on circuit boards
- Reduced system noise due to less pin inductance

The QS4A101 is characterized for operation at -40°C to +85°C.



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INDUSTRIAL TEMPERATURE RANGE

AUGUST 2000

PIN CONFIGURATION



QSOP TOP VIEW

ABSOLUTE MAXIMUM RATINGS (1)

Symbol	Description	Max.	Unit
VTERM ⁽²⁾	Supply Voltage to Ground	– 0.5 to +7	V
Vterm ⁽³⁾	DC Switch Voltage Vs	– 0.5 to +7	V
_	Analog Input Voltage	– 0.5 to +7	V
Vterm ⁽³⁾	DC Input Voltage VIN	– 0.5 to +7	V
VAC	AC Input Voltage (pulse width ≤20ns)	-3	V
Ιουτ	DC Output Current	120	mA
Рмах	Maximum Power Dissipation	0.7	W
Tstg	Storage Temperature	– 65 to +150	°C

NOTES:

- Stresses greater than those listed under ABSOLUTE MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.
- 2. Vcc Terminals.
- 3. All terminals except Vcc.

PIN DESCRIPTION

Pin Names	I/O	Description
A1 - A4	I/O	Port A
B1 - B4	I/O	Port B
E1 - E4	I	Port Switch Enable

FUNCTION TABLE⁽¹⁾

Ē	А	В	Function
L	Н	Н	Connect
L	L	L	Connect
Н	Х	Х	Disconnect

NOTE:

1. H = HIGH Voltage Level

L = LOW Voltage Level

X = Don't Care

POWER SUPPLY CHARACTERISTICS

Symbol	Parameter	Test Conditions ⁽¹⁾	Max.	Unit
Icc	Supply Current	Vcc = Max., VIN = GND or Vcc	3	μA

DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE

Following Conditions Apply Unless Otherwise Specified: Industrial: TA = -40°C to +85°C, Vcc = $5.0V \pm 5\%$

Symbol	Parameter	Test Conditions	Min.	Typ. ⁽¹⁾	Max.	Unit
Analog Switch						
Vin	Analog Signal Range ⁽²⁾		-0.5	1	Vcc - 1	V
rds(on)	Drain-source ON resistance (2,3)	Vcc = Min., VIN = 0V, ION = 30mA	_	5	7	Ω
		Vcc = Min., VIN = 2.4V, ION = 15mA	_	13	17	
IC(OFF)	Channel Off Leakage Current	A = Vcc or 0V,	—	1	—	nA
		B = 0V or Vcc, Ē = Vcc				
IC(ON)	Channel On Leakage Current	A = B = 0V	_	1	—	nA
		(each channel is turned on sequentially)				
Digital Conti	rol					
Vih	Input HIGH Voltage	Guaranteed Logic HIGH for Control Pins	2	-	—	V
VIL	Input LOW Voltage	Guaranteed Logic LOW for Control Pins	-	-	0.8	V
Dynamic Ch	Dynamic Characteristics					
ton(Ē)	Enable Turn-On Time	$R_L = 1K\Omega$, $C_L = 100pF$	0.5	—	6.5	ns
	Ē to A, B	(See figure 9)				
toff(Ē)	Enable Turn-Off Time	$RL = 1K\Omega$, $CL = 100pF$	0.5	_	6	ns
	Ē to A, B	(See figure 9)				
tpd	Group Delay ^(2,4a)	$RL = 1K\Omega$, $CL = 100pF$	—	_	250	ps
f3dB	-3dB Bandwidth	VIN = 0 to 1V, 1Vp-p, RL = 75Ω	—	1.4	—	GHz
	Off-isolation	V_{IN} = 0 to 1V, 1Vp-p, RL = 75 Ω , f = 5.5MHz	—	-80	—	dB
Xtalk	Crosstalk	VIN = 0 to 1V, 1Vp-p, RL = 75Ω , f = 30MHz	-	-75	—	dB
C(OFF)	Switch Off Capacitance	\overline{E} = Vcc, VIN = VOUT = 0V	-	5	—	pF
C(ON)	Switch On Capacitance	$\overline{E} = 0V, VIN = V_{OUT} = 0V$	—	10	_	pF
Qci	Charge Injection		_	1.5	—	рС

NOTES:

1. Typical values are at Vcc = 5.0V, TA = $25^{\circ}C$.

2. Max value is guaranteed but not production tested.

3. Measured by voltage drop between A and C pins at indicated current through the switch ON resistance is determined by the lower of the voltages on the two (A, B) pins.

4. The bus switch contributes no group delay other than the RC delay of the ON resistance of the switch and load capacitance. Group delay of the bus switch, when used in a system, is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

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TYPICAL CHARACTERISTICS

OFF-ISOLATION AND CROSSTALK VS. FREQUENCY



1. Crosstalk = 20 log |Vo/Vs|

2. Off-isolation = 20 log |Vo/Vs|

OFF-ISOLATION AND CROSSTALK VS. FREQUENCY



NOTES:

1. Crosstalk = 20 log |Vo/Vs| 2. Off-isolation = 20 log |Vo/Vs|

OFF-ISOLATION AND CROSSTALK VS. FREQUENCY



NOTES:

1. Crosstalk = 20 log |Vo/Vs|

2. Off-isolation = 20 log |Vo/Vs|

INSERTION LOSS VS. FREQUENCY



1. Insertion Loss = 20 log |Vo/Vs|

TYPICAL CHARACTERISTICS (CONTINUED)



ON-RESISTANCE VS. VIN



NOTE: 1. Insertion Loss = 20 log |Vo/Vs|



RON LINK

TEST CIRCUITS

SWITCHING TIME



TEST CIRCUITS (CONTINUED)





NOTES:

- Insertion Loss = 20 log |Vo/Vs|
 All unused pins are grounded.

NOTES:

- Crosstalk = 20 log |Vo/Vs|
 All unused pins are grounded.

OFF-ISOLATION



1. Off-isolation = 20 log |Vo/Vs|

ORDERING INFORMATION





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