<u>TOSHIBA</u>

TOSHIBA Bipolar Digital Integrated Circuit Silicon Monolithic

TD62001APG,TD62001AFG,TD62002APG,TD62002AFG, TD62003APG,TD62003AFG,TD62004APG,TD62004AFG

7-channel Darlington Sink Driver

The TD62001APG/AFG Series are high-voltage, high-current darlington drivers comprised of seven NPN darlington pairs. All units feature integral clamp diodes for switching inductive loads.

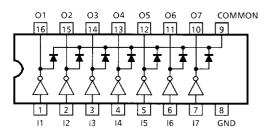
Applications include relay, hammer, lamp and display (LED) drivers.

Features

- Output current (single output): 500 mA (max)
- High sustaining voltage output: 50 V (min)
- Output clamp diodes
- Inputs compatible with various types of logic
- Package type
 - APG: DIP-16 pin (Pb free package) AFG: SOP-16 pin (Pb free package)

Туре	Input base resistor	Designation	
TD62001APG/AFG	External General purpose		
TD62002APG/AFG	10.5-kΩ + 7-V Zenner diode	14-V to 25-V PMOS	
TD62003APG/AFG	2.7 kΩ	TTL, 5-V CMOS	
TD62004APG/AFG	10.5 kΩ	6-V to 15-V PMOS, CMOS	

Pin Connection (top view)

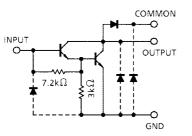


TD62001APG TD62002APG TD62003APG TD62004APG
TD62001AFG TD62002AFG TD62003AFG TD62004AFG
SOP16-P-225-1.27 Weight

DIP16-P-300-2.54A : 1.11 g (Typ.) SOP16-P-225-1.27 : 0.16 g (Typ.)

Schematics (each driver)

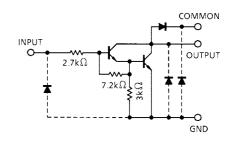
TD62001APG/AFG



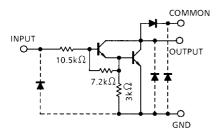
INPUT 10.5kΩ 7.2kΩ GND

TD62002APG/AFG

TD62003PAPG/AFG



TD62004APG/AFG



Note: The input and output parasitic diodes cannot be used as clamp diodes.

Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit	
Output sustaining voltage	V _{CE (SUS)}	–0.5 to 50	V	
Output current		I _{OUT}	500	mA/ch
Input voltage		V _{IN} (Note 1)	-0.5 to 30	V
Input current		I _{IN} (Note 2)	25	mA
Clamp diode reverse voltage	V _R	50	V	
Clamp diode forward current	١ _F	500	mA	
	APG		1.47	
Power dissipation	AFG	PD	0.625 (Note 3)	W
Operating temperature	T _{opr}	-40 to 85	°C	
Storage temperature	T _{stg}	T _{stg} –55 to 150		

Note 1: Except TD62001APG/AFG

Note 2: Only TD62001APG/AFG

Note 3: When mounted on a glass-epoxy PCB (30 mm \times 30 mm \times 1.6 mm, Cu area: 50%)

Recommended Operating Conditions (Ta = -40°C to 85°C)

Characteristi	ics	Symbol Condition		Min	Тур.	Max	Unit	
Output sustaining voltage	ng voltage V _{CE (SUS)}		0	—	50	V		
Output current	APG	Іоит	T _{pw} = 25 ms 7 circuits Ta = 85°C T _j = 120°C	Duty = 10%	0	_	370	mA/ch
				Duty = 50%	0	_	130	
	AFG			Duty = 10%	0	_	233	
	AFG			Duty = 50%	0	_	70	
Input voltage	Except TD62001APG/ AFG	V _{IN}			0	_	24	V
Input voltage (output on)	TD62002	Vin (on)	I _{OUT} = 400 mA h _{FE} = 800		14.5	_	24	v
	TD62003				2.8	_	24	
	TD62004				6.2	_	24	
	TD62001	VIN (OFF)			0	_	0.6	
Input voltage (output off)	TD62002				0	_	7.4	v
input voitage (output oir)	TD62003				0	_	0.7	
	TD62004				0	—	1.0	1
Input current	Only TD62001	I _{IN}			0	—	10	mA
Clamp diode reverse voltage		V _R			-	_	50	V
Clamp diode forward current		١ _F			_	_	350	mA
Device dia sin stien	APG		Ta = 85°C Ta = 85°C (Note)		_	_	0.76	w
Power dissipation	AFG	PD			_	—	0.325	

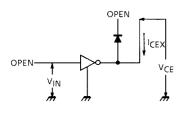
Note: When mounted on a glass-epoxy PCB (30 mm × 30 mm × 1.6 mm, Cu area: 50%)

Electrical Characteristics (Ta = 25°C unless otherwise noted)

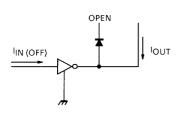
Characteristics	6	Symbol	Test Circuit	Test Condition		Min	Тур.	Max	Unit
Ooutput leakage current		ICEX	1	V _{CE} = 50 V, Ta = 25°C		_	—	50	μA
		ICEX		V _{CE} = 50 V, Ta = 85°C			—	100	
				I _{OUT} = 350 mA, I _{IN} = 500 μA			1.3	1.6	
Collector-emitter saturation voltage		V _{CE (sat)}	2	I _{OUT} = 200 mA, I _{IN} = 350 μA		_	1.1	1.3	V
				I _{OUT} = 100 mA, I _{IN} = 250 μA		_	0.9	1.1	
DC current transfer ratio		h _{FE}	2	V _{CE} = 2 V, I	V _{CE} = 2 V, I _{OUT} = 350 mA		—	—	
	TD62002		3	V _{IN} = 20 V, I _{OUT} = 350 mA		_	1.1	1.7	mA
Input current (output on)	TD62003	I _{IN (ON)}		V _{IN} = 2.4 V, I _{OUT} = 350 mA		_	0.4	0.7	
	TD62004			V _{IN} = 9.5 V, I _{OUT} = 350 mA		_	0.8	1.2	
Input current (output off)		I _{IN (OFF)}	4	I _{OUT} = 500 μA, Ta = 85°C		50	65	—	μA
	TD62002	VIN (ON)		V _{CE} = 2 V h _{FE} = 800	I _{OUT} = 350 mA		_	13.7	- V
	1002002		5		I _{OUT} = 200 mA		—	11.4	
Input voltage (output on)	TD62003				I _{OUT} = 350 mA		_	2.6	
input voltage (output on)			5		I _{OUT} = 200 mA		_	2.0	
	TD62004				I _{OUT} = 350 mA		_	4.7	
	1002004				I _{OUT} = 200 mA		_	4.4	
Clamp diode reverse current		I _R	6	V _R = 50 V, Ta = 25°C			_	50	μA
				V _R = 50 V, Ta = 85°C		-	—	100	μΛ
Clamp diode forward voltage		VF	7	I _F = 350 mA			—	2.0	V
Input capacitance		C _{IN}	_				15	—	pF
Turn-on delay		ton	8	V_{OUT} = 50 V, R _L = 125 Ω C _L = 15 pF		_	0.1	_	μS
Turn-off delay		toff	8	V_{OUT} = 50 V, R _L = 125 Ω C _L = 15 pF		_	0.2	_	μο

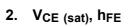
Test Circuit

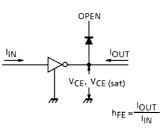
1. ICEX



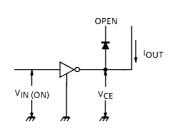
4. IIN (OFF)



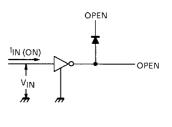




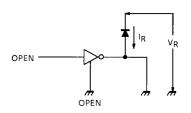
5. V_{IN (ON)}



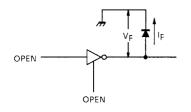
3. IIN (ON)



6. I_R

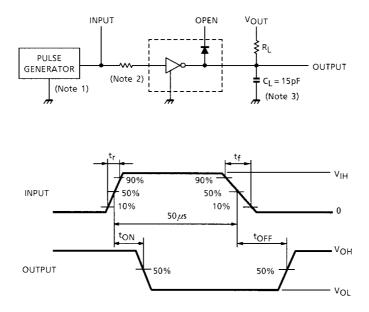


7. V_F



8. t_{ON}, t_{OFF}

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- Note 1: Pulse width 50 μ s, duty cycle 10% Output impedance 50 Ω , t_f ≤ 5 ns, t_f ≤ 10 ns
- Note 2: Input conditions are shown as following:

Input Condition

Type Number	R1	VIH
TD62001APG/AFG	2.7 kΩ	3 V
TD62002APG/AFG	0	13 V
TD62003APG/AFG	0	3 V
TD62004APG/AFG	0	8 V

Note 3: C_L includes probe and jig capacitance.

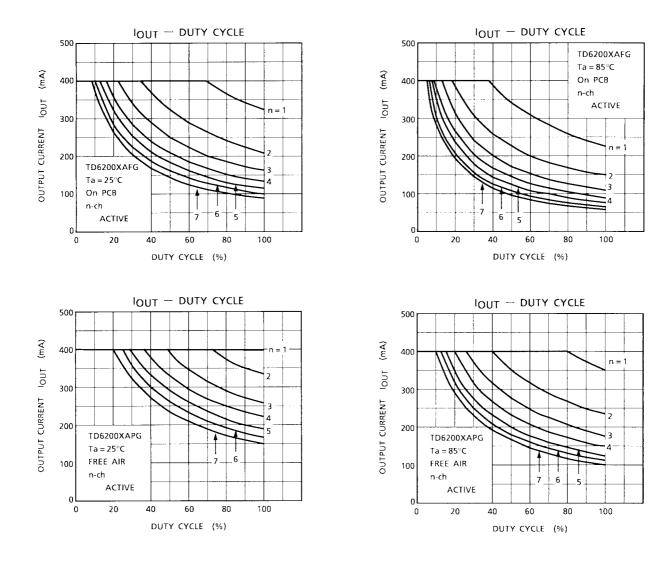
Precautions for Using

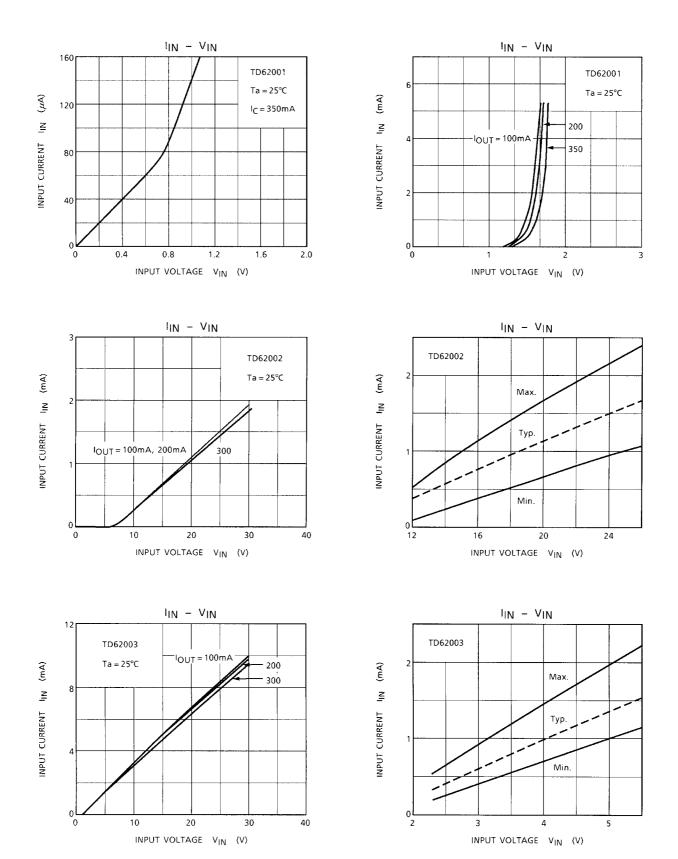
This IC does not include built-in protection circuits for excess current or overvoltage.

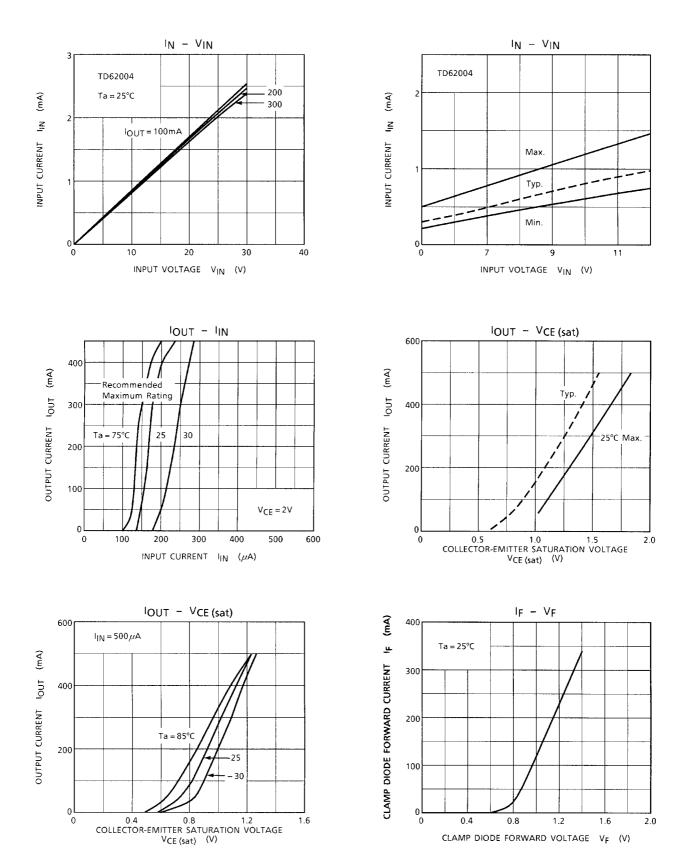
If this IC is subjected to excess current or overvoltage, it may be destroyed.

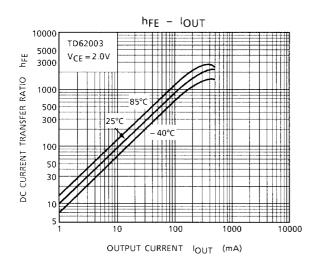
Hence, the utmost care must be taken when systems which incorporate this IC are designed.

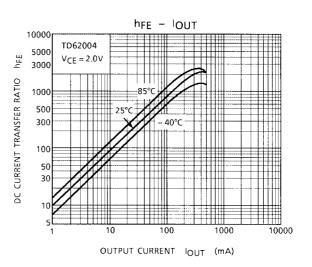
Utmost care is necessary in the design of the output line, COMMON and GND line since IC may be destroyed due to short–circuit between outputs, air contamination fault, or fault by improper grounding.

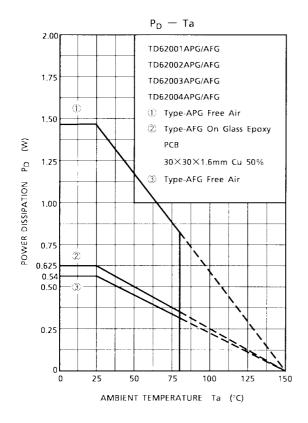








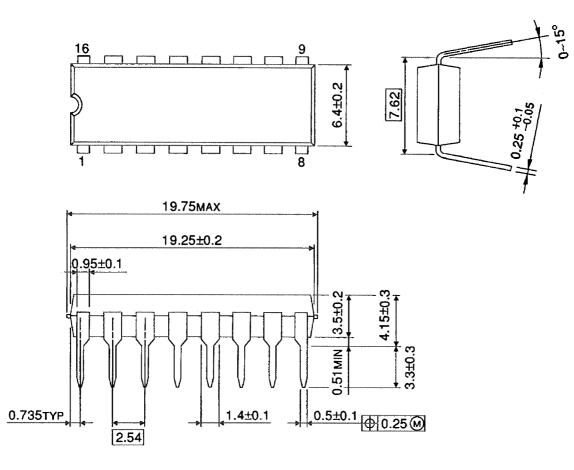




Package Dimensions

DIP16-P-300-2.54A

Unit : mm



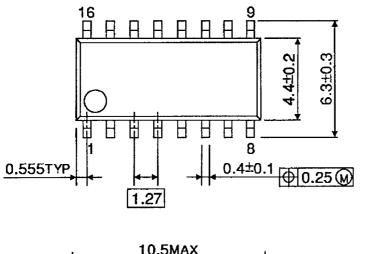
Weight: 1.11 g (Typ.)

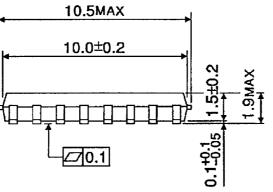
[5.715] (225mil)

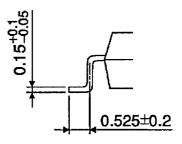
Package Dimensions

SOP16-P-225-1.27

Unit : mm







Weight: 0.16 g (Typ.)

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