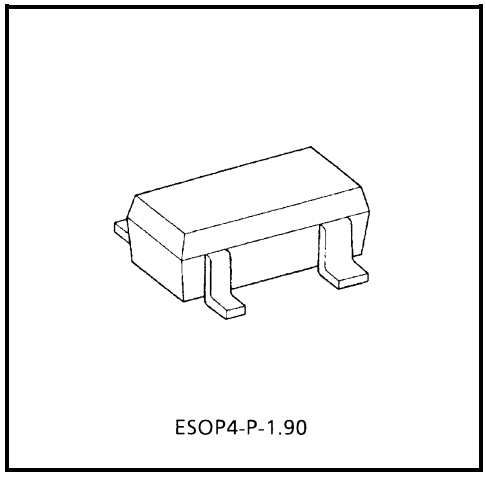
TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

TA4001F

VHF~UHF Wide Band Amplifier

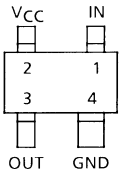
Features

- Band width: 2.4 GHz (typ.) (3dB down)
- High gain: $|S_{21}|^2$ = 12.5dB (typ.) (f = 500 MHz)
- 50 Ω Input and output impedance
- Small package

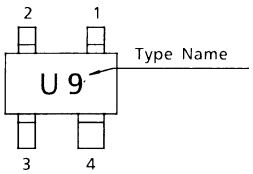


Weight: 0.013 g (typ.)

Pin Assignment (top view)



Marking



Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Supply voltage	V_{CC}	6	V
Total power dissipation	P_D (Note1)	300	mW
Operating temperature	T_{opr}	−40~85	°C
Storage temperature	T_{stg}	−55~125	°C

Note 1: When mounted glass epoxy of 2.5 cm^2 × 1.6 t

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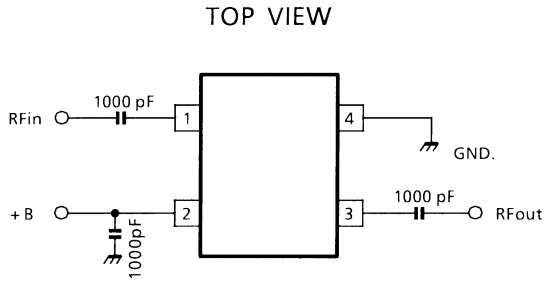
Electrical Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Circuit	Test Condition	Min	Typ.	Max	Unit
Circuit current	I_{CC}	—	V_{CC} = 5 V, non carrier	14	18	24	mA
Insertion gain	$\|S_{21}\|^2$	1	V_{CC} = 5 V, f = 500 MHz	9	12.5	16	dB
Band width	BW	1	V_{CC} = 5 V (Note 2)	1.9	2.4	—	GHz
Noise figure	NF	1	V_{CC} = 5 V, f = 500 MHz	—	5.2	7	dB
Input return loss	$\|S_{11}\|^2$	1	V_{CC} = 5 V, f = 500 MHz	—	−18	—	dB
Output return loss	$\|S_{22}\|^2$	1	V_{CC} = 5 V, f = 500 MHz	—	−21	—	dB
Isolation	$\|S_{12}\|^2$	1	V_{CC} = 5 V, f = 500 MHz	—	−22	—	dB
Maximum output level	P_O	1	V_{CC} = 5 V, f = 500 MHz, P_{in} = 0dBmW	—	2	—	dBmW

Note 2: BW is frequency of 3dB down from $|S_{21}|^2$ at 500 MHz.

Test Circuit 1



Notice

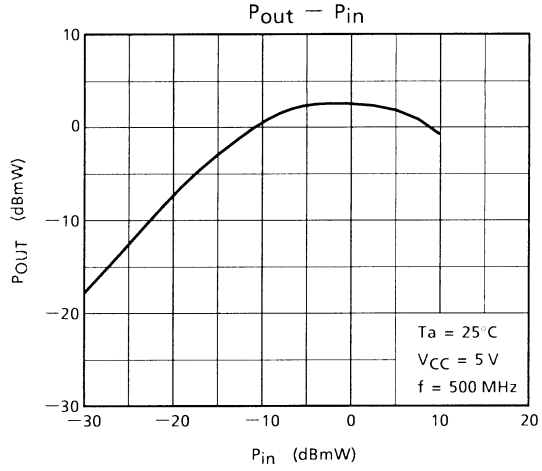
The circuits and measurements contained in this document are given only in the context of as examples of applications for these products.

Moreover, these example application circuits are not intended for mass production, since the high-frequency characteristics (the AC characteristics) of these devices will be affected by the external components which the customer uses, by the design of the circuit and by various other conditions.

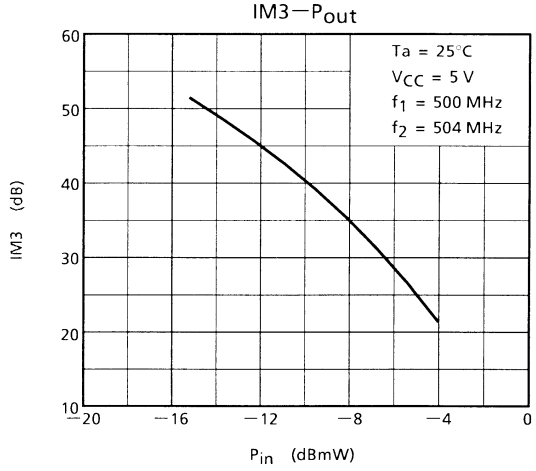
It is the responsibility of the customer to design external circuits which correctly implement the intended application, and to check the characteristics of the design.

TOSHIBA assume no responsibility for the integrity of customer circuit designs or applications.

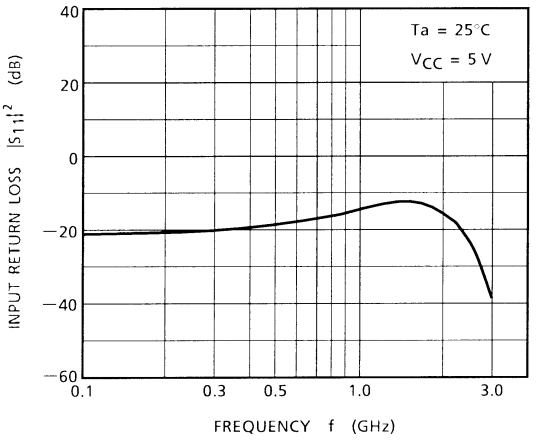
$P_{out} - P_{in}$



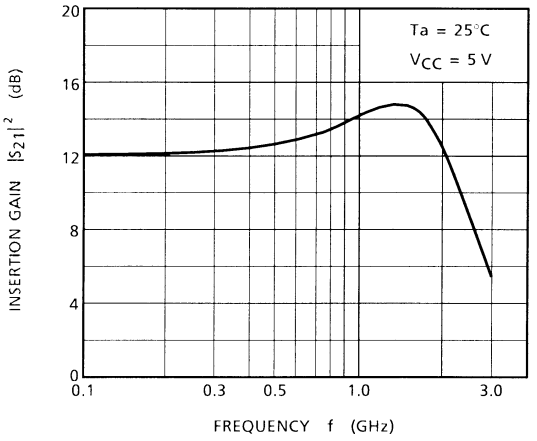
$IM3 - P_{out}$



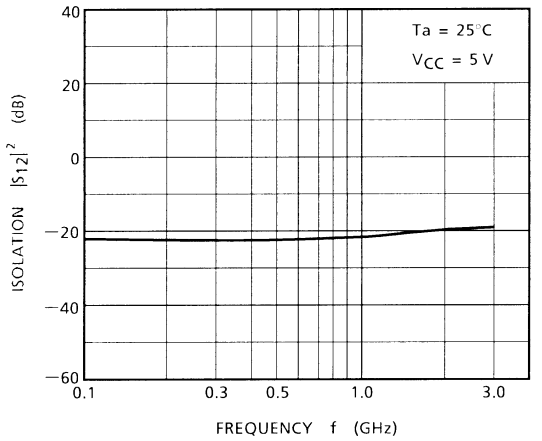
$|S_{11}|^2 - f$



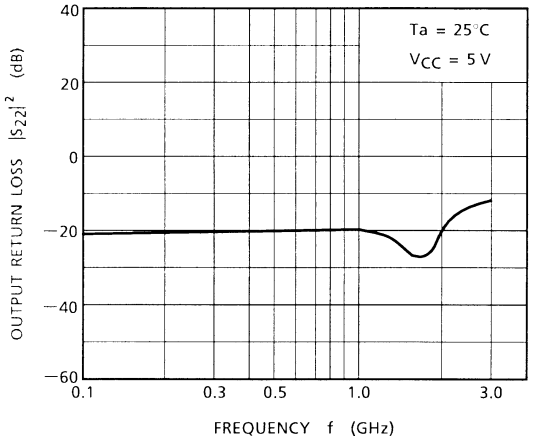
$|S_{21}|^2 - f$



$|S_{12}|^2 - f$

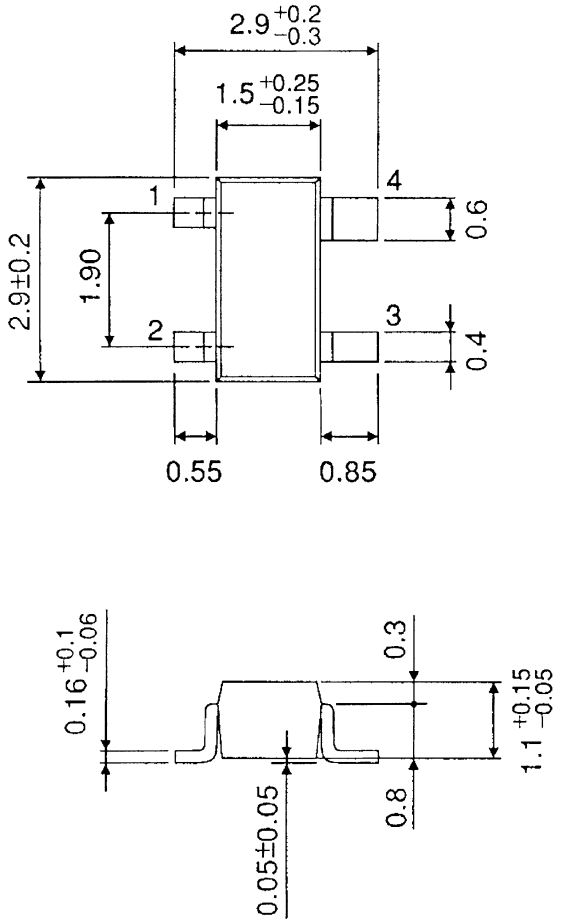


$|S_{22}|^2 - f$



Package Dimensions

ESOP4-P-1.90 Unit : mm



Weight : 0.013 g (Typ.)