TOSHIBA BIPOLAR DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

TD62064P,TD62064AP,TD62064F,TD62064AF TD62074P,TD62074AP,TD62074F,TD62074AF

4CH HIGH-CURRENT DARLINGTON SINK DRIVER

The TD62064P / AP / F / AF and TD62074P / AP / F / AF are high–voltage, high–current darlington drivers comprised of four NPN darlington pairs.

All units feature integral clamp diodes for switching inductive loads and all units of TD62074P / AP / F / AF feature uncommitted collectors and emitters for isolated darlington applications.

For proper operation, the substrate (SUB) must be connected to the most negative voltage.

Applications include relay, hammer, lamp and stepping moter drivers.

FEATURES

- Output current (single output) 1.5 A (Max)
- High sustaining voltage output
 V (Min) (TD62064P / F, 074P / F)
 V (Min) (TD62064AP / AF, 074AP / AF)

• Output clamp diodes : TD62064P / AP / F / AF

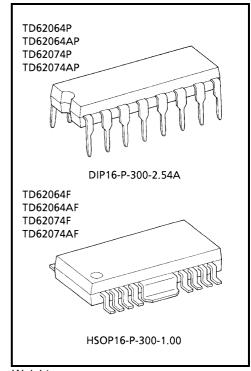
• Isolated darlington array: TD62074P/AP/F/AF

Input compatible with TTL and 5 V CMOS

• GND and SUB terminal = heat sink

• Package type-P, AP: DIP-16 pin

• Package type-F, AF: HSOP-16 pin

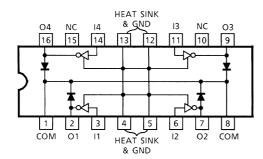


Weight

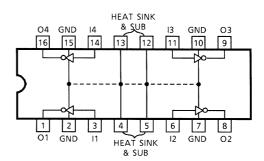
DIP16-P-300-2.54A : 1.11 g (Typ.) HSOP16-P-300-1.00 : 0.50 g (Typ.)

PIN CONNECTION (TOP VIEW)

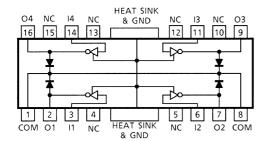
TD62064P / AP



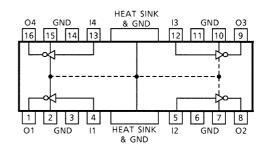
TD62074P / AP



TD62064F / AF

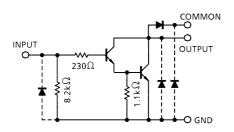


TD62074F / AF

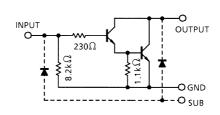


SCHEMATICS (EACH DRIVER)

TD62064P / AP / F / AF



TD62074P / AP / F / AF



Note: The input and output parasitic diodes cannot be used as clamp diodes.

MAXIMUM RATINGS (Ta = 25°C)

| CHARACTERISTIC | | SYMBOL | RATING | UNIT | |
|-----------------------|--------|-------------------------|---------------------|--------|--|
| Output Sustaining | P, F | Vo= (0110) | -0.5~35 | V | |
| Voltage | AP, AF | V _{CE} (SUS) | -0.5~50 | | |
| Output Current | | lout | 1.5 | A / ch | |
| Input Current | | I _{IN} | 50 | mA | |
| Input Voltage | | V _{IN} | −0.5~17 | V | |
| Clamp Diode | P, F | V _R (Note 1) | 35 | V | |
| Reverse Voltage | AP, AF | VR (Note 1) | 50 | | |
| Clamp Diode Forward C | urrent | I _F (Note 1) | 1.5 | A / ch | |
| 11-t1\/-1t | P, F | V _{SUB} | 35 | V | |
| Isolated Voltage | AP, AF | (Note 2) | 50 | | |
| D 0: : :: | P, AP | D- | 1.47 / 2.7 (Note 3) | W | |
| Power Dissipation | F, AF | P _D | 0.9 / 1.4 (Note 4) | | |
| Operating Temperature | | T _{opr} | -40~85 | °C | |
| Storage Temperature | | T _{stg} | -55~150 | °C | |

Note 1: TD62064P / AP / F / AF Note 2: TD62074P / AP / F / AF

Note 3: On Glass Epoxy PCB (50 × 50 × 1.6 mm Cu 50%) Note 4: On Glass Epoxy PCB (60 × 30 × 1.6 mm Cu 30%)



RECOMMENDED OPERATING CONDITIONS (Ta = -40~85°C)

| CHARACTERISTIC | | SYMBOL | TEST CONDITION | | MIN | TYP. | MAX | UNIT | |
|--------------------------------|----------------|---------------------------------------|--|-------------|-----|------|------|---------|--|
| Output Sustaining Voltage | P, F | V _{CE} (SUS) | | 0 | _ | 35 | V | | |
| | AP, AF | | | 0 | _ | 50 | | | |
| | | | DC1 Circuit, Ta = 2 | 25°C | 0 | _ | 1250 | 1 | |
| Output Current | P, AP (Note 1) | Іоит | T _{pw} = 25 ms 4 Circuits T _j = 120°C Ta = 85°C | Duty = 10 % | 0 | _ | 1250 | mA / ch | |
| | | | | Duty = 50 % | 0 | _ | 390 | | |
| | F, AF (Note 2) | | | Duty = 10 % | 0 | _ | 907 | | |
| | | | | Duty = 50 % | 0 | _ | 172 | | |
| | , | | | 0 | _ | 8 | V | | |
| Input Voltage | (Output On) | V _{IN (ON)} | I _{OUT} = 1.25 A | 2.5 | _ | 8 | V | | |
| | (Output Off) | V _{IN (OFF)} | | 0 | _ | 0.4 | V | | |
| Input Current | | I _{IN} | | | 0 | _ | 20 | mA | |
| Clamp Diode Reverse Voltage | P, F | V- | TD62064P / AP / F / AF | | 0 | _ | 35 | · v | |
| | AP, AF | V_{R} | 1D02004P / AP / F | 0 | _ | 50 | | | |
| Clamp Diode Forward Current | | l _F | | | | _ | 1.25 | Α | |
| Isolation Voltage | P, F | \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ | TD62074P / AP / F / AF | | | _ | 35 | V | |
| | AP, AF | V_{SUB} | 1D62074P / AP / F | | _ | 50 | | | |
| Power Dissipation | P, AP | Б | Ta = 85°C | (Note 1) | _ | _ | 1.4 | w | |
| | F, AF | P_{D} | Ta = 85°C | (Note 2) | _ | _ | 0.7 | | |

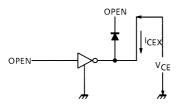
Note 1: On Glass Epoxy PCB ($50 \times 50 \times 1.6$ mm Cu 50%) Note 2: On Glass Epoxy PCB ($60 \times 30 \times 1.6$ mm Cu 30%)

ELECTRICAL CHARACTERISTICS (Ta = 25°C)

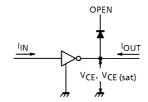
| CHARACTERIS | STIC | SYMBOL | TEST CIR- CUIT | TEST CONDITION | | MIN | TYP. | MAX | UNIT | |
|--------------------------------------|--|-----------------------|----------------------|---|--------------------|-------------------------------|------|------|------|------|
| Output Leakage Current | AP, AF | · I _{CEX} | 1 | V _{CE} = 50 V, Ta = 25°C | | _ | _ | 50 | μA | |
| | | | | V _{CE} = 50 V, Ta = 85°C | | - | _ | 500 | | |
| | P, F | | | V _{CE} = 35 V, Ta = 25°C | | _ | _ | 50 | μΑ | |
| | Р, Г | | | V _{CE} = 35 V, Ta = 85°C | | - | _ | 500 | | |
| Collector-Emitter Seturat | 0 11 1 5 11 0 1 11 11 11 | | 2 | I _{OUT} = 1.25 A, I _{IN} = 2 mA | | - | _ | 1.6 | V | |
| Collector-Emitter Saturation Voltage | | V _{CE (sat)} | | I _{OUT} = 0.75 A, I _{IN} = 935 μA | | - | _ | 1.25 | | |
| DC Current Transfer Beti | 0 | h _{FE} | 2 | ., |) \/ | I _{OUT} = 1.0 A | - | 800 | _ | _ |
| DC Current Transfer Rati | DC Current Transfer Ratio | | 2 | V _{CE} = 2 | . V | I _{OUT} = 0.25 A | _ | 1500 | _ | |
| Input Voltage (Output On | ut Voltage (Output On) V _{IN (ON)} 3 I _{OUT} | | I _{OUT} = | _{OUT} = 1.25 A, I _{IN} = 2 mA | | - | _ | 2.4 | V | |
| | AP, AF | · I _R | 4 | V _R = 50 V, Ta = 25°C | | _ | _ | 50 | | |
| Clamp Diode Leakage | | | | V _R = 50 V, Ta = 85°C | | - | _ | 100 | | |
| Current | F | | | V _R = 35 V, Ta = 25°C | | _ | _ | 50 | μA | |
| | Г | | | V _R = 35 V, Ta = 85°C | | - | _ | 100 | | |
| Clamp Diode Forward Vo | ltage | V _F | 5 | I _F = 1.25 A | | - | _ | 2 | V | |
| Input Capacitance | | C _{IN} | 6 | V _{IN} = 0 V, f = 1MHz | | - | 15 | _ | pF | |
| Turn-On Delay | P, F | 4 | | | V _{OUT} = | = 35 V, R _L = 29 Ω | | 0.1 | | · µs |
| | AP, AF | t _{ON} | 7 | C _L = 15 pF | V _{OUT} = | = 50 V, R _L = 42 Ω | _ | 0.1 | _ | |
| Turn-Off Delay | P, F | + |] ′ | 15 pF | V _{OUT} = | = 35 V, R _L = 29 Ω | | 1.0 | | |
| | AP, AF | toff | | V _{OUT} | | = 50 V, R _L = 42 Ω | | 1.0 | | |

TEST CIRCUIT

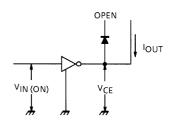
1. ICEX



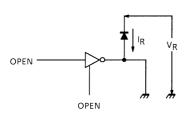
2. V_{CE (sat)}, h_{FE}



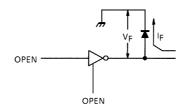
3. V_{IN (ON)}



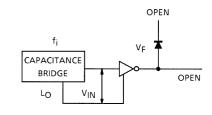
4. I_R



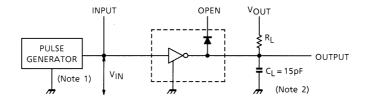
5. V_F

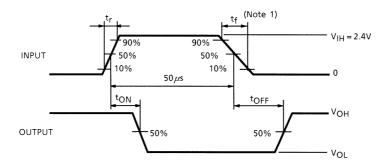


6. C_{IN}



7. ton, toff





Note 1: Pulse Width 50 µs, Duty Cycle 10%

Output Impedance 50 Ω , $t_f \le 5$ ns, $t_f \le 10$ ns

Note 2: C_I includes probe and jig capacitance

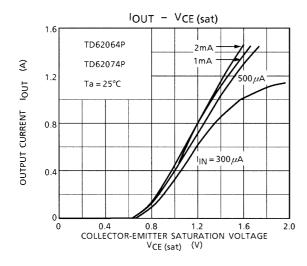
PRECAUTIONS for USING

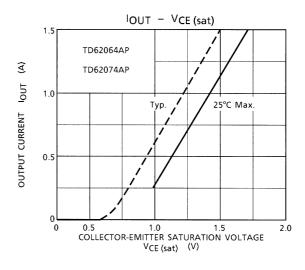
(1) This IC does not include built-in protection circuits for excess current or overvoltage. If this IC is subjected to excess current or overvoltage, it may be destroyed. Hence, the utmost care must be taken when systems which incorporate this IC are designed. Utmost care is necessary in the design of the output line, COMMON and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.

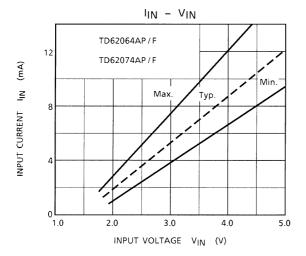
(2) When using TD62064P/AP/F/AF to drive an inductive load (such as a motor, solenoid, or relay), Toshiba recommend you use diodes (pins 1 and 8) to absorb the counter electromotive force generated when driving an inductive load.

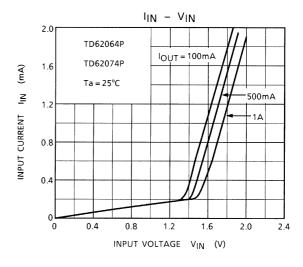
5

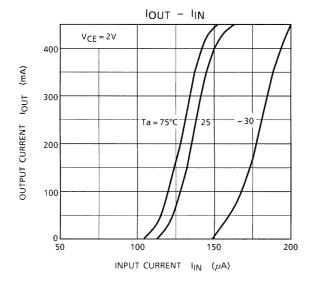
When using TD62074P/AP/F/AF to drive an inductive load (such as a motor, solenoid, or relay), Toshiba recommend you connect diodes externally to absorb the counter electromotive force generated when driving an inductive load.

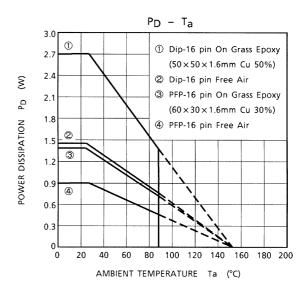


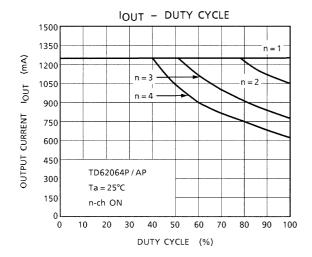


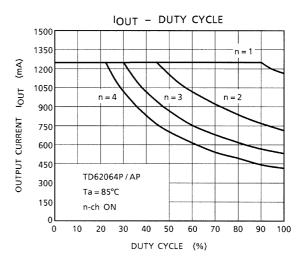


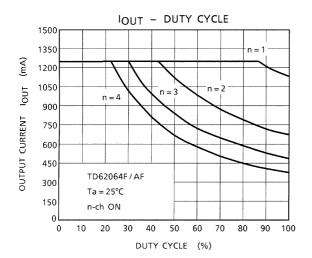


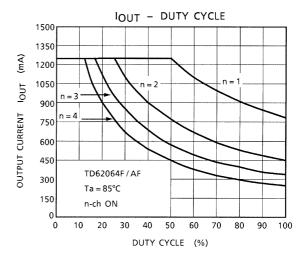








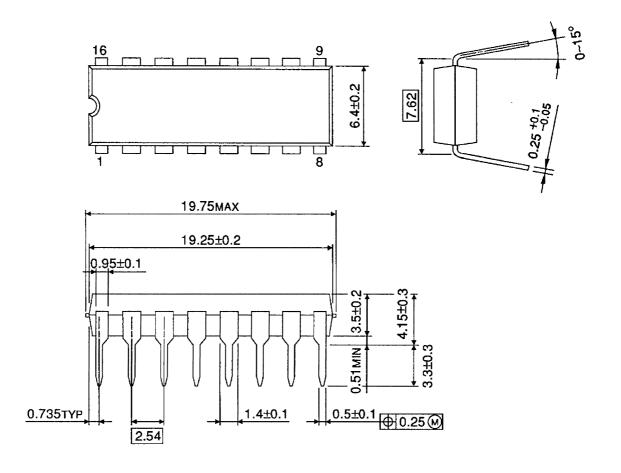




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PACKAGE DIMENSIONS

DIP16-P-300-2.54A Unit: mm

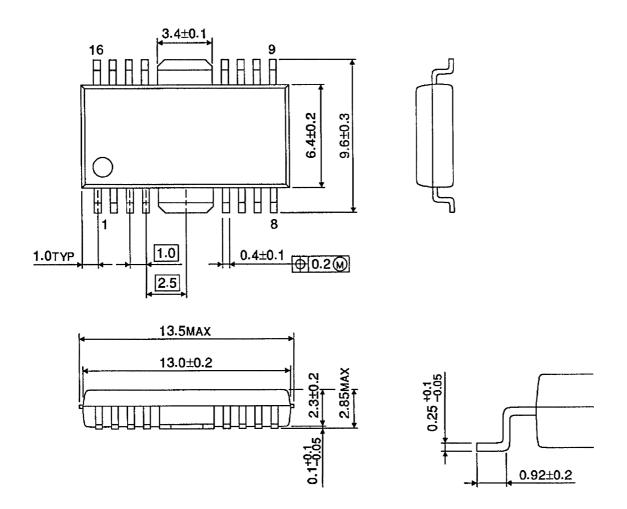


Weight: 1.11 g (Typ.)

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PACKAGE DIMENSIONS

HSOP16-P-300-1.00 Unit: mm



9

Weight: 0.50 g (Typ.)

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000707EBA

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