TOSHIBA BIPOLAR DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

# TD62781AP,TD62781F,TD62781AF TD62782AP,TD62782F,TD62782AF

#### 8CH HIGH-VOLTAGE SOURCE DRIVER

The TD62781AP / F / AF Series are comprised of eight source current Transistor Array.

These drivers are specifically designed for fluorescent display applications.

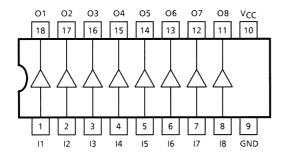
Applications include relay, hammer and lamp drivers.

#### **FEATURES**

- High output voltage Type-AP, AF:  $V_{OUT} = 60 \text{ V (Min)}$ Type-F :  $V_{OUT} = 35 \text{ V (Min)}$
- Output current (single output) IOUT = -50 mA / ch (Max)
- Pull-down resistors / each output
- Single supply voltage
- Input compatible with various types of logic

TYPE	DESIGNATION				
TD62781AP / F / AF	TTL, 5 V CMOS				
TD62782AP / F / AF	6~15 V PMOS CMOS				

# PIN CONNECTION (TOP VIEW)

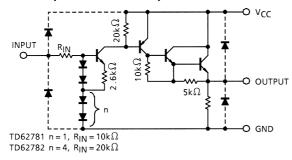


# TD62781AP TD62782AP DIP18-P-300-2.54D TD62781AF TD62781AF TD62782AF TD62782AF TD62782AF SOP18-P-375-1.27

Weight

DIP18-P-300-2.54D: 1.47 g (Typ.) SOP18-P-375-1.27: 0.41 g (Typ.)

#### **SCHEMATICS (EACH DRIVER)**



Note: The input and output parasitic diodes cannot be used as clamp diodes.

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# **MAXIMUM RATINGS (Ta = 25°C)**

CHARACTE	ERISTIC	SYMBOL	RATING	UNIT	
Supply Voltage	AP / AF	V <sub>CC</sub>	60	V	
	F	v CC	35	V	
Output Voltage		V <sub>OUT</sub>	V <sub>CC</sub>	V	
Output Current		lout	-50	mA / ch	
Input Voltage		V <sub>IN</sub>	20	V	
Power Dissipation	AP	P <sub>D</sub> (Note)	1.47	W	
	F/AF	FD (Note)	0.96	VV	
Operating Temperature	)	T <sub>opr</sub>	-40~85	°C	
Storage Temperature		T <sub>stg</sub>	-55~150	°C	

Note: Delated above 25  $^{\circ}$ C in the proportion 11.7 mW /  $^{\circ}$ C (AP Type), 7.7 mW /  $^{\circ}$ C (F, AF Type).

# RECOMMENDED OPERATING CONDITIONS (Ta = -40~85°C)

CHARACTERISTIC		SYMBOL	CONDITION	MIN	TYP.	MAX	UNIT	
Supply Voltage	TD62781AP, TD62781AF		V <sub>CC</sub>	_	4.5	_	55	V
	TD62781F				4.5	_	35	
	TD62782AP, TD62782AF				6.0	_	55	
	TD62782F	TD62782F			6.0	_	35	
Output Vol	tage		V <sub>OUT</sub>	_	0	_	V <sub>CC</sub>	V
Output Cur	Output Current		I <sub>OUT</sub>	_	0	_	-40	mA / ch
Input Voltage	TD62781		V <sub>IN</sub>	_	0	_	7	· V
	TD62782				0	_	15	
Power Dissipation ——		AP	D-	_	_	_	0.52	W
		AF/F	P <sub>D</sub>	_	_	_	0.35	VV



# **ELECTRICAL CHARACTERISTICS (Ta = 25°C)**

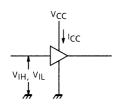
CHARACTERISTIC		SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN	TYP.	MAX	UNIT	
Input	"H" Level	TD62781	V <sub>IH</sub>	1	_	2.0	_	_	٧
		TD62782				4.5	_	_	
Voltage	"L" Level	TD62781	V	1		0	_	0.8	V
	L Level	TD62782	V <sub>IL</sub> 1	'	_	0	_	2.0	V
Input Current	"∐" Lovol	TD62781		2	V <sub>IN</sub> = 2.4 V	_	40	75	μА
	"H" Level	TD62782	I <sub>IH</sub>	2	V <sub>IN</sub> = 7.5 V	_	170	250	
Output Cui	rrent	ent "H" Level I <sub>OL</sub> 3 — — 200		_	μΑ				
Output Voltage		"H" Level	V <sub>OH</sub>	4	I <sub>OUT</sub> = -40 mA, V <sub>IN</sub> = V <sub>IH MIN</sub> .	V <sub>CC</sub> -2.5	V <sub>CC</sub> -1.7	_	V
		"L" Level	V <sub>OL</sub>		I <sub>OUT</sub> = 0, V <sub>IN</sub> = V <sub>IL MIN</sub> .	_	50	250	mV
Supply Current		ICC (ON)	1	$V_{CC}$ = 55 V, $V_{IN}$ = $V_{IH\ MIN}$ . (Note)	_	_	20	mΛ	
		I <sub>CC (OFF)</sub>		$V_{CC}$ = 55 V, $V_{IN}$ = $V_{IL\ MAX}$ . (Note)	_	_	1	mA	
Turn-On Delay		t <sub>ON</sub>	- 5	V <sub>CC</sub> = 55 V, C <sub>L</sub> = 15 pF	_	0.2	_	116	
Turn-Off Delay		tOFF		(Note)	_	6.0	_	μs	

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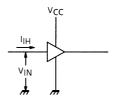
Note:  $V_{CC} = 35 \text{ V for Type-F}$ 

#### **TEST CIRCUIT**

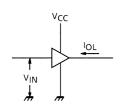
## 1. VIH, VIL, ICC



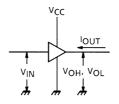
#### 2. I<sub>IH</sub>



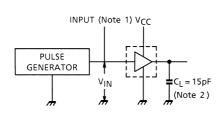
#### 3. I<sub>OL</sub>

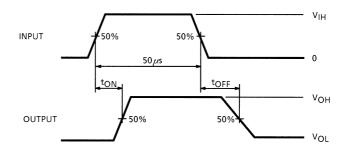


# 4. V<sub>OH</sub>, V<sub>OL</sub>



#### 5. ton, toff





Note 1: Pulse Width 50 µs, Duty Cycle 10%

Output Impedance 50  $\Omega$ ,  $t_r \le 100$  ns,  $t_f \le 100$  ns

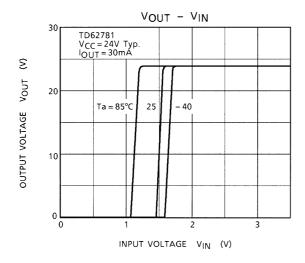
Note 2: C<sub>L</sub> includes probe and jig capacitance.

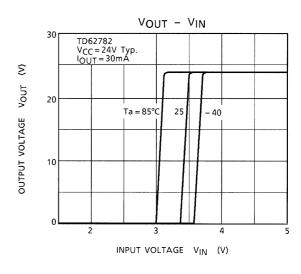
#### **PRECAUTIONS for USING**

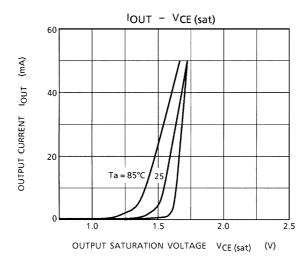
This IC does not integrate protection circuits such as overcurrent and overvoltage protectors.

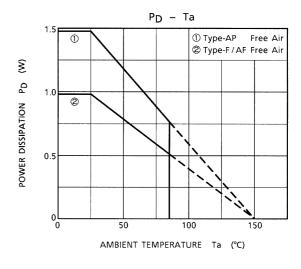
Thus, if excess current or voltage is applied to the IC, the IC may be damaged. Please design the IC so that excess current or voltage will not be applied to the IC.

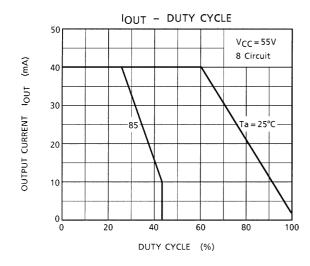
Utmost care is necessary in the design of the output line,  $V_{\rm CC}$  and GND line since IC may be destroyed due to short–circuit between outputs, air contamination fault, or fault by improper grounding.







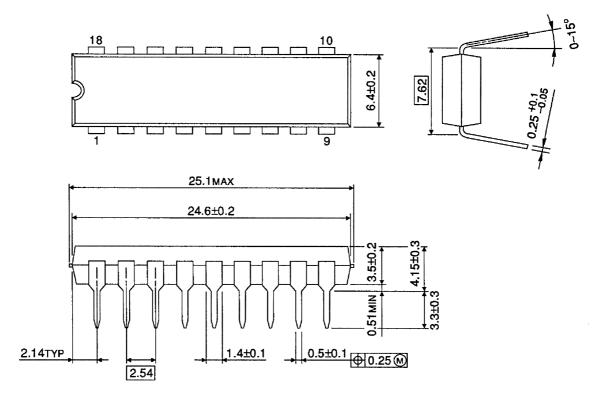




## **PACKAGE DIMENSIONS**

DIP18-P-300-2.54D

Unit: mm

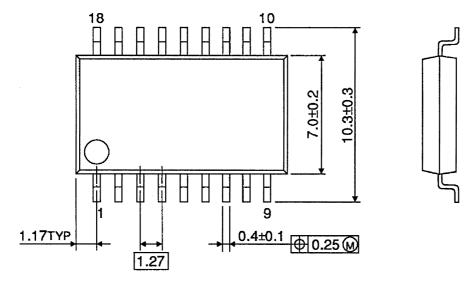


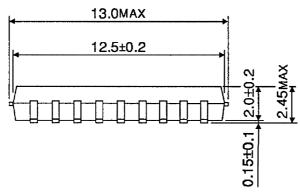
Weight: 1.47 g (Typ.)

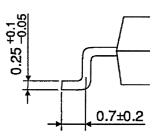
## **PACKAGE DIMENSIONS**

SOP18-P-375-1.27

Unit: mm







Weight: 0.41 g (Typ.)

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