#### TOSHIBA BIPOLAR DIGITAL INTEGRATED CIRCUIT MULTI CHIP

# TD62M8603F

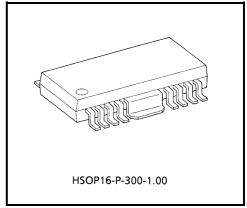
## 8CH LOW SATURATION VOLTAGE SOURCE DRIVER

 $\rm TD62M8603F$  is Multi Chip IC incorporates 8 low saturation discrete (2SA1203) transistors.

This IC is suitable for a battery use motor drive and LED display module applications.

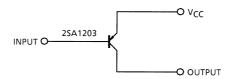
## **FEATURES**

- Suitable for Motor drive circuit and LED display module
- External Bias Resistor
- Low Saturation Voltage  $V_{CE~(sat)} = 0.10~V~(Typ.)~at~I_{C} = -0.5~A$   $V_{CE~(sat)} = 0.20~V~(Max.)~at~I_{C} = -1.5~A$
- HSOP16 (1 mm pitch) power small package sealed

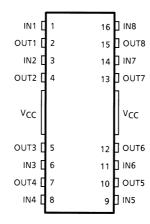


Weight: 0.50 g (Typ.)

### **BLOCK DIAGRAM**



# **PIN CONNECTION (TOP VIEW)**



# MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT	
Supply Voltage	V <sub>CC</sub>	-30	V	
Breakdown Voltage	V <sub>CBO</sub>	-30	٧	
	V <sub>CEO</sub>	-30		
	V <sub>EBO</sub>	-5		
Output Current	Io	-1.5	A / ch	
	I <sub>O (PEAK)</sub>	-3.0 (Note)		
Base Current	Ι <sub>Β</sub>	-0.3	Α	
Power Dissipation	PD	900	mW	
Junction Temperature	Tj	150	°C	
Operating Temperature	T <sub>opr</sub>	-40~85	°C	
Storage Temperature	T <sub>stg</sub>	-55~150	°C	

Note: T = 10 ms single pulse

# **ELECTRICAL CHARACTERISTICS (Ta = 25°C)**

CHARACTERISTIC	SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN	TYP.	MAX	UNIT
Current Gain	h <sub>FE (1)</sub>		$V_{CE} = -2 \text{ V}, I_{C} = -0.5 \text{ A}$	160	_	320	_
	h <sub>FE (2)</sub>		$V_{CE} = -2V$ , $I_{C} = -1.5 A$	50	100	_	
Saturation Voltage	V <sub>CE (sat)</sub>	-	$I_C = -0.5 \text{ A}, I_B = -10 \text{ mA}$	_	-0.1	-0.50	V
			$I_C = -1.5 \text{ A}, I_B = -30 \text{ mA}$	_	_	-2.0	
Transition Frequency	f <sub>T</sub>	_	$V_{CE} = -2 \text{ V}, I_{C} = -0.5 \text{ A}$	_	120	_	MHz
Leakage Current	l <sub>OL</sub>	_	V <sub>CC</sub> = -30 V	_	0	-5	μA
Base-Emitter Forward Voltage	V <sub>BE</sub>	_	$V_{CE} = -2 \text{ V}, I_{C} = -0.5 \text{ A}$	_	_	-1.0	٧

## PRECAUTIONS for USING

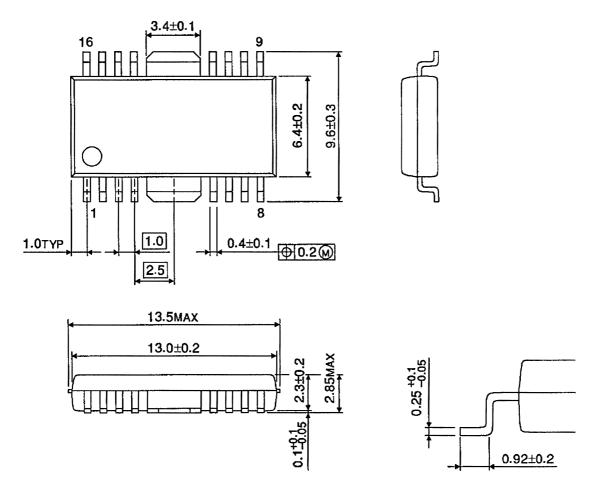
This IC does not integrate protection circuits such as overcurrent and overvoltage protectors. Thus, if excess current or voltage is applied to the IC, the IC may be damaged. Please design the IC so that excess current or voltage will not be applied to the IC.

Utmost care is necessary in the design of the output line,  $V_{CC}$  and GND line since IC may be destroyed due to short–circuit between outputs, air contamination fault, or fault by improper grounding.

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# **PACKAGE DIMENSIONS**

HSOP16-P-300-1.00 Unit: mm



Weight: 0.50 g (Typ.)

# RESTRICTIONS ON PRODUCT USE

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