Unit in mm

TENTATIVE·RESTRICTIVE DATA

TOSHIBA AC SWITCH OPTICALLY ISOLATED AC SWITCH

'SA3100J

: 0.8mm (Min.)

R.M.S. On-State Current $: I_{T(RMS)} = 0.1 \sim 3A$ Repetitive Peak Off-State Voltage

 $: V_{DRM} = 400, 600V$

Isolation Voltage between Input to Output: 3000VAC (t=1min.)

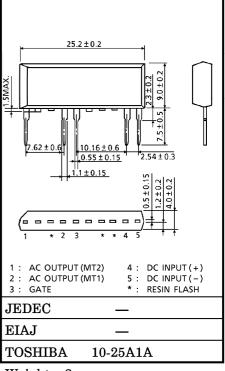
Thickness of Inner Insulation Material Creepage Distances, Clearances for Insulation

between Input and Output Side : 6mm (Min.)

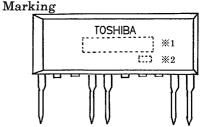
TTL drive is Available

MAXIMUM RATINGS (Ta = 25°C)

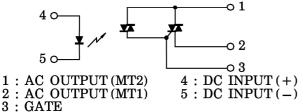
	CHARACTERISTIC			RATING	UNIT	
	Control Input Curre	I _{F (IN)}	50	mA		
PUT	Forward Current De (Ta≥53°C)	⊿I _F /°C	-0.7	mA/°C		
INI	Peak Forward Curre (100μs pulse, 100pp	I_{FP}	1	A		
	Reverse Voltage		v_{R}	5	V	
	Repetitive Peak	TSA3100G	Vanas	400	v	
	Off-State Voltage TSA310		$V_{ m DRM}$	600	, ,	
Ŀ	Nominal AC Line	TSA3100G	V. ~	80~125	v	
OUTPU	Voltage (Note 1)	TSA3100J	V_{AC}	80~250		
	R.M.S On-State Cur (Sine Waveform, R.)	I _{T(RMS)}	0.1~3	A		
	Peak One Cycle Sur	I _{TSM}	30 (50Hz)	_		
	Current (Non-Repeti		33 (60Hz)	A		
	I ² t Limit Value	${ m I}^2{ m t}$	4.5	A^2 s		
Оре	erating Frequency Ra	f	45~65	Hz		
Оре	erating Temperature	$T_{ m opr}$	-40~100	°C		
Storage Temperature Range			$\mathrm{T_{stg}}$	-40~100	$^{\circ}\mathrm{C}$	
	Isolation Voltage (Input to Output) Note 2			3000	V	



Weight: 2g



EQUIVALENT CIRCUIT



NUMBER		SYMBOL	MARK			
※ 1	TYPE	TSA3100G	TYPE	TSA3100G		
×1	TIPE	TSA3100J	TIPE	TSA3100J		
※2		Month (Staring from) Alphabet A Year (Last Number of the Christian era	3B : Fe	ple January 1993 February 1993 December 1993		

(The cutted pins near by Pin No.1 & No.3 is connecting in electrically with output terminal)

Note 1: When the voltage larger than applied AC voltage is applied to the device such as 2

phase motor and others, please derating for this maximum rating value. TEST CONDITION...AC, t=60s, $RH \le 60\%$

Note 3: Soldering of printed wiring board should be used under 260°C and 10 seconds.

ELECTRICAL CHARACTERISTICS (Ta = 25°C)

	CHARACT	ERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
UT	Forward Vo	oltage	$V_{\mathbf{F}}$	$I_{ m F} = 10 { m mA}$	1.0	1.15	1.3	V
NPI	Reverse Cu	rrent	$I_{\mathbf{R}}$	$V_R = 5V$	1	_	10	μ A
	Capacitance	e	$\mathrm{c_{T}}$	V_T =0 V , f=1 M Hz	1	20	_	pF
	Peak Off-St	ate Current	$I_{ m DRM}$	$V_{ m DRM}$ = Rated	ı	_	10	μ A
	Peak On-St	ate Voltage	$ m V_{TM}$	$I_{TM} = 4.5A$	1	_	1.5	V
	Holding Cu	rrent	$I_{\mathbf{H}}$	V _D =6V, Beginning Current=1A		_	25	mA
PUT	Critical Rat Off-State V	te of Rise of oltage	dv/dt	$V_{ m DRM} = { m Rated}$	_	2000	_	V/μs
OUT	Critical Rat	te of Rise of ng Voltage	(dv / dt) c	$V_D = 400V, -di/dt = 30A/ms$	1	30	_	V/μs
	Thermal	Junction to Lead	$R_{ ext{th}}$ $_{(j-\ell)}$	AC	_	_	20	°C/W
	Resistance	Junction to Ambient	R _{th (j-a)}	AC	_	_	85	°C/W

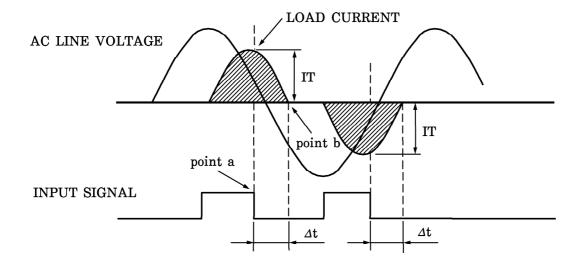
COUPLED ELECTRICAL CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Trigger LED Current	I_{FT}	$V_D=6V, R_L=20\Omega$	_		10	mA
Capacitance (Input to output)	c_{S}	$V_S=0V, f=1MHz$	1	0.5	_	pF
Isolation Resistance	$R_{\mathbf{S}}$	V=500V, RH≦60%	10^{9}	_	_	Ω
Turn-off Time	${ m t_{off}}$	OUTPUT : Sine Waveform		_	3/4	cycle

<REMARK>

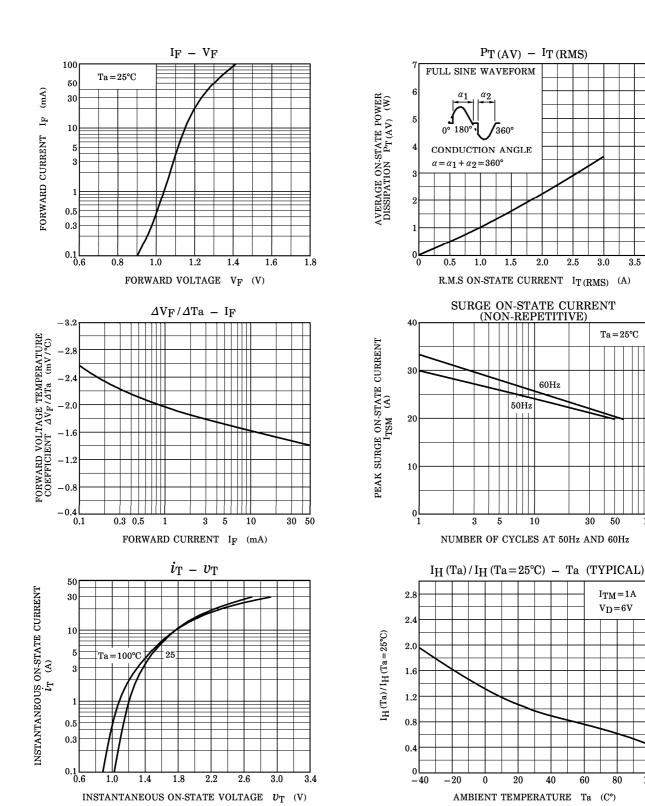
PHASE CONTROL APPLICATION

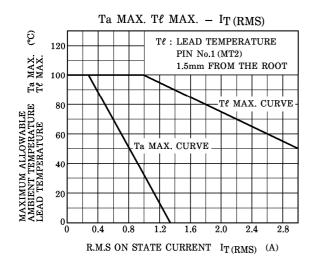
In case of using in phase control application. Δt must be at least 1ms (Δt : The time starting from the end of INPUT SIGNAL "point a" to the point at which load current become ZERO "point b"). And, Load current "IT" at "point a" must be at least double the maximum Holding Current (IH) specification in each operating temperature.

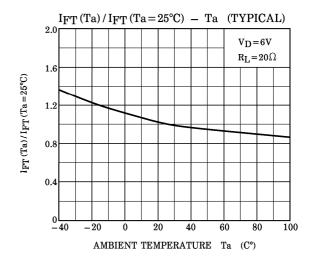


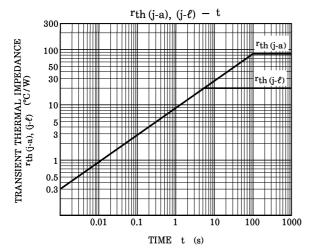
100

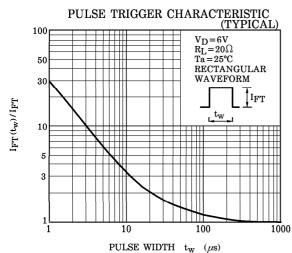
100











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