Unit in mm

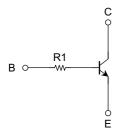
TOSHIBA Transistor Silicon NPN Epitaxial Type (PCT process) (Bias Resistor built-in Transistor)

## **RN1110FT**, **RN1111FT**

Switching, Inverter Circuit, Interface Circuit and **Driver Circuit Applications.** 

- High-density mount is possible because of devices housed in very thin TESM packages.
- Incorporating a bias resistor into a transistor reduces parts count. Reducing the parts count enable the manufacture of ever more compact equipment and save assembly cost.
- Wide range of resistor values are available to use in various circuit designs.
- Complementary to RN2110FT, RN2111FT

## **Equivalent Circuit and Bias Resistor Values**



## **Maximum Ratings (Ta = 25°C)**

Characteristics	Symbol	Rating	Unit
Collector-base voltage	$V_{CBO}$	50	٧
Collector-emitter voltage	$V_{CEO}$	50	٧
Emitter-base voltage	V <sub>EBO</sub>	5	٧
Collector current	IC	100	mA
Collector power dissipation	P <sub>C</sub> (Note)	100	mW
Junction temperature	Tj	150	°C
Storage temperature range	T <sub>stg</sub>	-55~150	°C

Note: Total rating

1.2±0.05 0.8±0.05 0.59±0.05 1. BASE 2. EMITTER TESM 3. COLLECTOR **JEDEC** EIAJ TOSHIBA

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## Electrical Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current		I <sub>CBO</sub>	$V_{CB} = 50 \text{ V}, I_{E} = 0$	_	_	100	nA
Emitter cut-off current		I <sub>EBO</sub>	$V_{EB} = 5 \text{ V}, I_{C} = 0$	_	_	100	nA
DC current gain		h <sub>FE</sub>	$V_{CE} = 5 \text{ V}, I_{C} = 1 \text{ mA}$	120	_	700	
Collector-emitter saturation voltage		V <sub>CE (sat)</sub>	$I_C = 5 \text{ mA}, I_B = 0.25 \text{ mA}$	_	0.1	0.3	V
Transition frequency		f <sub>T</sub>	V <sub>CE</sub> = 10 V, I <sub>C</sub> = 5 mA	_	250	_	MHz
Collector output capacitance		C <sub>ob</sub>	V <sub>CB</sub> = 10 V, I <sub>E</sub> = 0, f = 1 MHz	_	3	6	pF
Input resistor	RN1110FT	- R1	_	3.29	4.7	6.11	kΩ
	RN1111FT			7	10	13	

