Preliminary

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

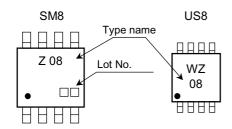
TC7WZ08FU, TC7WZ08FK

2 Input and Gate

Features

- High output drive: ±24 mA (min) @VCC = 3 V
- Super high speed operation: tpd 2.4 ns (typ.) @VCC = 5 V, 50 pF
- Operation voltage range: $V_{CC (opr)} = 1.65 \sim 5.5 \text{ V}$
- Latch-up performance: ±500 mA or more
- ESD performance: ±200 V or more (EIAJ) ±2000 V or more (MIL)
- Power down protection is provided on all inputs and outputs.
- Matches the performance of TC74LCX series when operated at 3.3 V VCC.

Marking



TC7WZ08FU SSOP8-P-0.65 (SM8) TC7WZ08FK SSOP8-P-0.50A (US8)

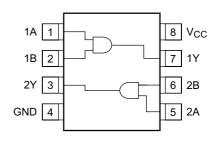
Weight

SSOP8-P-0.65 : 0.02 g (typ.) SSOP8-P-0.50A : 0.01 g (typ.)

Maximum Ratings (Ta = 25°C)

Symbol	Rating	Unit
V_{CC}	-0.5~6	V
V_{IN}	-0.5~6	V
V _{OUT}	-0.5~6	V
I _{IK}	-20	mA
I _{OK}	-20	mA
I _{OUT}	±50	mA
I _{CC}	±50	mA
P_{D}	300 (SM8) 200 (US8)	mW
T _{stg}	-65~150	°C
TL	260	°C
	VCC VIN VOUT IIK IOK IOUT ICC PD Tstg	V _{CC} -0.5~6 V _{IN} -0.5~6 V _{OUT} -0.5~6 I _{IK} -20 I _{OK} -20 I _{OUT} ±50 I _{CC} ±50 P _D 300 (SM8) 200 (US8) T _{stg} -65~150

Pin Assignment (top view)



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In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability Handbook" etc..

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Truth Table

А	В	Υ
L	L	L
L	Н	L
Н	L	L
Н	Н	Н

Logic Diagram



Recommended Operating Conditions

Characteristics	Symbol	Rating	Unit	
Supply voltage	V _{CC}	1.65~5.5	V	
Supply voltage	VCC	1.5~5.5 (Note 1)	V	
Input voltage	V _{IN}	0~5.5	V	
Output voltage	V _{OUT}	0~5.5 (Note 2)	V	
		0~V _{CC} (Note 3)		
Operating temperature	T _{opr}	-40~85	°C	
		$0~20~(V_{CC} = 1.8~V \pm 0.15~V, \\ 2.5~V \pm 0.2~V)$	ns/V	
Input rise and fall time	d _t /d _V	0~10 (V _{CC} = 3.3 V ± 0.3 V)		
		$0~5~(V_{CC} = 5.5~V \pm 0.5~V)$		

Note 1: Data retention only

Note 2: $V_{CC} = 0 V$

Note 3: High or low state

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The information contained herein is subject to change without notice.



Electrical Characteristics

DC Characteristics

Characteristics S		Cumphal	Symbol Test Condition			Ta = 25°C			Ta = -40~85°C		Unit
		Symbol			V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
High level	High lovel	V	_		1.65~1.95	0.75 × V _{CC}	_	_	0.75 × V _{CC}	_	V
	High level	V _{IH}			2.3~5.5	0.7 × V _{CC}	_	_	0.7 × V _{CC}	_	
voltage	Low level	- L V	_		1.65~1.95	-	_	0.25 ×V _{CC}	_	0.25 × V _{CC}	
	Low level	V _{IL}			2.3~5.5		_	0.3 ×V _{CC}	_	$0.3 \\ \times V_{CC}$	
					1.65	1.55	1.65	_	1.55	_	
				I _{OH} = -100 μA	2.3	2.2	2.3	_	2.2		
				ΙΟΗ = -100 μΑ	3.0	2.9	3.0	_	2.9		
					4.5	4.4	4.5	_	4.4		
	High level	Vон	$V_{IN} = V_{IH}$	$I_{OH} = -4 \text{ mA}$	1.65	1.29	1.52	_	1.29	_	V
				$I_{OH} = -8 \text{ mA}$	2.3	1.9	2.15	_	1.9		
				$I_{OH} = -16 \text{ mA}$	3.0	2.4	2.8	_	2.4	_	
				$I_{OH} = -24 \text{ mA}$	3.0	2.3	2.68	_	2.3	_	
Output				$I_{OH} = -32 \text{ mA}$	4.5	3.8	4.2	_	3.8	_	
voltage		.ow level V _{OL}	V _{IN} = V _{IH} or V _{IL}	Ι _{ΟL} = 100 μΑ	1.65	_	0	0.1	_	0.1	
					2.3	_	0	0.1	_	0.1	
					3.0	_	0	0.1	_	0.1	
					4.5	_	0	0.1	_	0.1	
	Low level			$I_{OL} = 4 \text{ mA}$	1.65	_	0.08	0.24	_	0.24	
				I _{OL} = 8 mA	2.3	_	0.1	0.3	_	0.3	
				I _{OL} = 16 mA	3.0	_	0.15	0.4	_	0.4	
				I _{OL} = 24 mA	3.0	_	0.22	0.55	_	0.55	
				$I_{OL} = 32 \text{ mA}$	4.5	_	0.22	0.55	_	0.55	
Input leakage	current	I _{IN}	$V_{IN} = 5.5 \text{ V or GND}$		0~5.5	_		±1	_	±10	
Power off lea	Power off leakage current I _{OFF} V _{IN} or V _{OUT} = 5.5 V		0.0	_	_	1	_	10	μΑ		
Quiescent supply current I_{CC} $V_{IN} = 5.5 \text{ V or GND}$		or GND	1.65~5.5	—		1	_	10	μΑ		



AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristics	Cumbal	Test Condition		Ta = 25°C			Ta = -40~85°C		Lloit
Characteristics	Symbol		V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
Propagation delay time	t _{pLH}	$C_L = 15 \text{ pF}, R_L = 1 \text{ M}\Omega$	1.8 ± 0.15	2.0	5.7	10.5	2.0	11.0	ns
			2.5 ± 0.2	1.0	3.5	5.8	1.0	6.2	
			3.3 ± 0.3	0.8	2.6	3.9	0.8	4.3	
			5.0 ± 0.5	0.5	1.9	3.1	0.5	3.3	
		$C_L = 50 \text{ pF}, R_L = 500 \Omega$	3.3 ± 0.3	1.2	3.2	4.8	1.2	5.2	
			5.0 ± 0.5	0.8	2.5	3.7	0.8	4.0	
Input capacitance	C _{IN}		0~5.5		3.0		_		pF
Power dissipation capacitance	C	(Note)	3.3	_	22	_	_	_	pF
	C _{PD}	(Note)	5.5		37	_	_	_	Ы

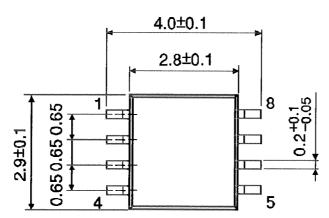
Note: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

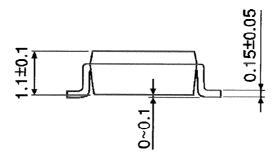
Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/2$

Package Dimensions

SSOP8-P-0.65 Unit: mm

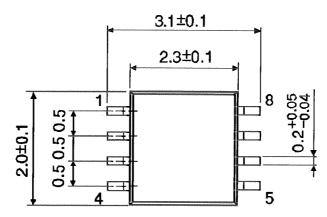


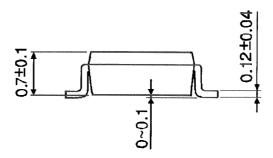


Weight: 0.02 g (typ.)

Package Dimensions

SSOP8-P-0.50A Unit: mm





Weight: 0.01 g (typ.)