**Preliminary** 

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

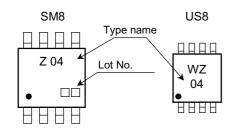
# TC7WZ04FU, TC7WZ04FK

#### **Triple Inverter**

#### **Features**

- High output drive: ±24 mA (min) @VCC = 3 V
- Super high speed operation: tpd 2.3 ns (typ.) @VCC = 5 V, 50 pF
- Operation voltage range:  $V_{CC (opr)} = 1.65 \sim 5.5 \text{ V}$
- Latch-up performance: ±500 mA or more
- ESD performance: ±200 V or more (EIAJ) ±2000 V or more (MIL)
- Power down protection is provided on all inputs and outputs.
- Matches the performance of TC74LCX series when operated at 3.3 V VCC.

#### Marking



# SSOP8-P-0.65 (SM8) TC7WZ04FK SSOP8-P-0.50A (US8)

Weight

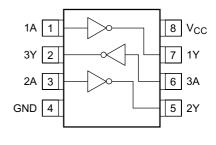
TC7WZ04FU

SSOP8-P-0.65 : 0.02 g (typ.) SSOP8-P-0.50A : 0.01 g (typ.)

#### **Maximum Ratings (Ta = 25°C)**

	•		
Characteristics	Symbol	Rating	Unit
Power supply voltage	V <sub>CC</sub>	-0.5~6	V
DC input voltage	V <sub>IN</sub>	-0.5~6	V
DC output voltage	V <sub>OUT</sub>	-0.5~6	V
Input diode current	I <sub>IK</sub>	-20	mA
Output diode current	lok	-20	mA
DC output current	lout	±50	mA
DC V <sub>CC</sub> /ground current	I <sub>CC</sub>	±50	mA
Power dissipation	P <sub>D</sub>	300 (SM8) 200 (US8)	mW
Storage temperature	T <sub>stg</sub>	-65~150	°C
Lead temperature (10s)	TL	260	°C

#### Pin Assignment (top view)



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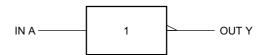
<sup>•</sup> The TOSHIBA products listed in this document are intended for usage in general electronics applications (computer, personal equipment, office equipment, measuring equipment, industrial robotics, domestic appliances, etc.). These TOSHIBA products are neither intended nor warranted for usage in equipment that requires extraordinarily high quality and/or reliability or a malfunction or failure of which may cause loss of human life or bodily injury ("Unintended Usage"). Unintended Usage include atomic energy control instruments, airplane or spaceship instruments, transportation instruments, traffic signal instruments, combustion control instruments, medical instruments, all types of safety devices, etc.. Unintended Usage of TOSHIBA products listed in this document shall be made at the customer's own risk.



#### **Truth Table**

Α	Υ
L	Н
Н	L

#### **Logic Diagram**



#### **Recommended Operating Conditions**

Characteristics	Symbol	Rating	Unit	
Supply voltage	V <sub>CC</sub>	1.65~5.5	V	
Supply voltage	VCC	1.5~5.5 (Note 1)	V	
Input voltage	V <sub>IN</sub>	0~5.5	V	
Output voltage	V <sub>OUT</sub>	0~5.5 (Note 2)	V	
	VOUT	0~V <sub>CC</sub> (Note 3)	V	
Operating temperature	T <sub>opr</sub>	-40~85	°C	
		$0~20~(V_{CC} = 1.8~V \pm 0.15~V, 2.5~V \pm 0.2~V)$	ns/V	
Input rise and fall time	d <sub>t</sub> /d <sub>v</sub>	0~10 (V <sub>CC</sub> = 3.3 V ± 0.3 V)		
		$0~5~(V_{CC} = 5.5~V \pm 0.5~V)$		

Note 1: Data retention only

Note 2:  $V_{CC} = 0 \text{ V}$ 

Note 3: High or low state

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The information contained herein is subject to change without notice.



## **Electrical Characteristics**

## **DC Characteristics**

Characteristics Symbol Test Condition		Symbol Test Condition			Ta = 25°C			Ta = -40~85°C		Unit	
		Condition	V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Unit		
High level		V <sub>IH</sub>			1.65~ 1.95	0.75 × V <sub>CC</sub>	_	_	0.75 × V <sub>CC</sub>	_	
Input voltage	VIH	_		2.3~5.5	0.7 × V <sub>CC</sub>	_	_	0.7 × V <sub>CC</sub>	_	V	
	I ow level	ow level V <sub>IL</sub>	_		1.65~ 1.95		_	0.25 ×V <sub>CC</sub>	_	0.25 ×V <sub>CC</sub>	V
	Low level				2.3~5.5		_	0.3 ×V <sub>CC</sub>	_	$^{0.3}_{\times\text{V}_{CC}}$	
					1.65	1.55	1.65	_	1.55	_	
				I <sub>OH</sub> = -100 μA	2.3	2.2	2.3	_	2.2		
				ΙΟΗ = -100 μΑ	3.0	2.9	3.0	—	2.9	_	
					4.5	4.4	4.5	_	4.4	_	
	High level	V <sub>OH</sub>	$V_{IN} = V_{IL}$	$I_{OH} = -4 \text{ mA}$	1.65	1.29	1.52	_	1.29	_	V
				$I_{OH} = -8 \text{ mA}$	2.3	1.9	2.15	_	1.9		
				$I_{OH} = -16 \text{ mA}$	3.0	2.4	2.8	_	2.4	_	
				$I_{OH} = -24 \text{ mA}$	3.0	2.3	2.68	_	2.3	_	
Output				$I_{OH} = -32 \text{ mA}$	4.5	3.8	4.2	_	3.8	_	
voltage			V <sub>IN</sub> = V <sub>IH</sub>	Ι <sub>ΟL</sub> = 100 μΑ	1.65	_	0	0.1	_	0.1	
Lov					2.3	_	0	0.1	_	0.1	
					3.0	_	0	0.1		0.1	
					4.5	_	0	0.1	_	0.1	
	Low level	V <sub>OL</sub>		I <sub>OL</sub> = 4 mA	1.65	—	0.08	0.24	_	0.24	
				I <sub>OL</sub> = 8 mA	2.3		0.1	0.3		0.3	
				I <sub>OL</sub> = 16 mA	3.0	—	0.15	0.4		0.4	
				I <sub>OL</sub> = 24 mA	3.0	_	0.22	0.55		0.55	
				$I_{OL} = 32 \text{ mA}$	4.5	_	0.22	0.55	_	0.55	
Input leakage	Input leakage current I <sub>IN</sub> V <sub>IN</sub> = 5.5 V or GND		0~5.5	_	_	±1	_	±10	μΑ		
Power off lea	Power off leakage current I <sub>OFF</sub> V <sub>IN</sub> o		V <sub>IN</sub> or V <sub>OL</sub>	<sub>JT</sub> = 5.5 V	0.0	_	_	1	_	10	μΑ
Quiescent supply current		Icc	V <sub>IN</sub> = 5.5 V or GND		1.65~5.5	—	_	1	_	10	μΑ



# AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristics	Cymahal	Test Condition		Ta = 25°C			Ta = -40~85°C		l loit
Characteristics	Symbol	rest Condition	V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Unit
Propagation delay time	t <sub>pLH</sub>	$C_L = 15 \text{ pF}, R_L = 1 \text{ M}\Omega$	$1.8 \pm 0.15$	1.8	4.4	9.5	2.0	10.0	- ns
			2.5 ± 0.2	1.2	3.0	5.1	1.2	5.6	
			$3.3 \pm 0.3$	0.8	2.2	3.4	0.8	3.8	
			$5.0 \pm 0.5$	0.5	1.8	2.8	0.5	3.1	
	t <sub>pHL</sub>	$C_L = 50 \text{ pF}, R_L = 500 \Omega$	$3.3 \pm 0.3$	1.2	2.9	4.5	1.2	5.0	
			$5.0 \pm 0.5$	0.8	2.3	3.6	8.0	4.0	
Input capacitance	C <sub>IN</sub>	_	0~5.5		3.0		_		pF
Power dissipation capacitance	C <sub>PD</sub>	(Note)	3.3	_	18	_	_	_	pF
		(Note)	5.5	_	23	_	_	_	μΓ

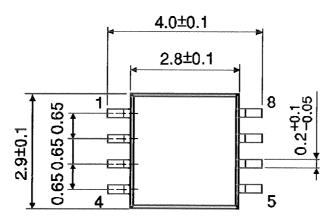
Note: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

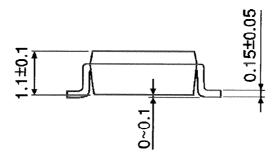
Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/3$ 

# **Package Dimensions**

SSOP8-P-0.65 Unit: mm

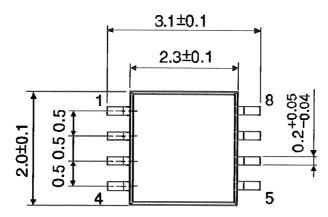


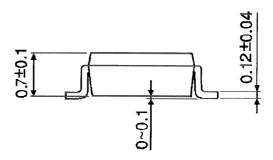


Weight: 0.02 g (typ.)

# **Package Dimensions**

SSOP8-P-0.50A Unit: mm





Weight: 0.01 g (typ.)