Preliminary

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

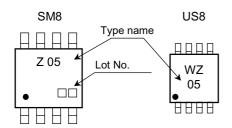
TC7WZ05FU, TC7WZ05FK

Triple Inverter (open drain)

Features

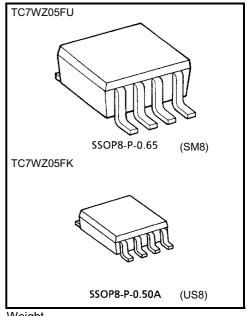
- High output drive: 24 mA (min) @VCC = 3 V
- Super high speed operation: tpZL 2.3 ns (typ.) @VCC = 5 V, 50 pF
- Operation voltage range: $V_{CC (opr)} = 1.65 \sim 5.5 \text{ V}$
- Latch-up performance: ±500 mA or more
- ESD performance: ±200 V or more (EIAJ) ±2000 V or more (MIL)
- Power down protection is provided on all inputs and outputs.
- Matches the performance of TC74LCX series when operated at 3.3 V VCC.

Marking



Maximum Ratings (Ta = 25°C)

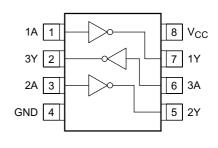
Characteristics	Symbol	Rating	Unit	
Power supply voltage	V _{CC}	-0.5~6	V	
DC input voltage	V _{IN}	-0.5~6	V	
DC output voltage	V _{OUT}	-0.5~6	V	
Input diode current	I _{IK}	-20	mA	
Output diode current	lok	-20	mA	
DC output current	lout	50	mA	
DC V _{CC} /ground current	Icc	±50	mA	
Power dissipation	P _D	300 (SM8) 200 (US8)	mW	
Storage temperature	T _{stg}	-65~150	°C	
Lead temperature (10s)	TL	260	°C	



Weight

SSOP8-P-0.65 : 0.02 g (typ.) SSOP8-P-0.50A : 0.01 g (typ.)

Pin Assignment (top view)



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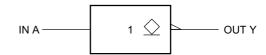


Truth Table

А	Υ
L	*H
Н	L

*: High impedance

Logic Diagram



Recommended Operating Conditions

Characteristics	Symbol	Rating	Unit	
Supply voltage	V _{CC}	1.65~5.5	· V	
Supply voltage	VCC	1.5~5.5 (Note 1)	Ī	
Input voltage	V _{IN}	0~5.5	V	
Output voltage	V _{OUT}	0~5.5 (Note 2)	٧	
		0~V _{CC} (Note 3)		
Operating temperature	T _{opr}	-40~85	°C	
		$0~20~(V_{CC} = 1.8~V \pm 0.15~V, \\ 2.5~V \pm 0.2~V)$	ns/V	
Input rise and fall time	d _t /d _∨	0~10 (V _{CC} = 3.3 V ± 0.3 V)		
		$0~5~(V_{CC} = 5.5~V \pm 0.5~V)$		

Note 1: Data retention only

Note 2: $V_{CC} = 0 V$ Note 3: Low state

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The information contained herein is subject to change without notice.



Electrical Characteristics

DC Characteristics

Characteristics		Symbol Test Condition			Ta = 25°C			Ta = -40~85°C		Unit	
Charac	Sharadonsides Symbol Fest contained		V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic		
Input voltage Low leve	High lovel	el V _{IH}	_		1.65~ 1.95	0.75 × V _{CC}	_	_	0.75 × V _{CC}		V
	T light level				2.3~5.5	0.7 ×V _{CC}	_	_	0.7 × V _{CC}		
	I ow level	V _{IL}	_		1.65~ 1.95		_	0.25 ×V _{CC}	_	0.25 × V _{CC}	
	Low level	ow level VIL			2.3~5.5		_	0.3 ×V _{CC}		$0.3 \\ \times V_{CC}$	
	Low level	V _{OL} \	V _{IN} = V _{IH}	Ι _{ΟL} = 100 μΑ	1.65	_	0	0.1	—	0.1	
					2.3	_	0	0.1	—	0.1	V
					3.0	_	0	0.1		0.1	
					4.5	_	0	0.1	_	0.1	
Output voltage				I _{OL} = 4 mA	1.65	_	0.08	0.24	_	0.24	
				I _{OL} = 8 mA	2.3	_	0.1	0.3		0.3	
				I _{OL} = 16 mA	3.0	_	0.15	0.4		0.4	
				I _{OL} = 24 mA	3.0	_	0.22	0.55		0.55	
				I _{OL} = 32 mA	4.5	_	0.22	0.55	_	0.55	
Input leakage	current	I _{IN}	V _{IN} = 5.5 V or GND		0~5.5	_		±1		±10	μΑ
Off-state carrent		loz	$V_{IN} = V_{IL},$ $V_{OUT} = V_{CC}$ or GND		5.5	_	_	±5	_	±10	μΑ
Power off lea	kage current	loff	V _{IN} or V _{OL}	_{JT} = 5.5 V	0.0	_	_	1	_	10	μΑ
Quiescent supply current		Icc	V _{IN} = 5.5 V or GND		1.65~5.5	_	_	1	_	10	μΑ



AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristics	Cumbal	Test Condition		Ta = 25°C			Ta = -40~85°C		Unit
Characteristics	Symbol	rest Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic
Propagation delay time	t _{pZL}	$C_L = 50 \text{ pF}, R_L = 500 \Omega$	1.8 ± 0.15	1.8	5.5	9.5	1.8	10.5	
			2.5 ± 0.2	1.2	3.7	5.8	1.2	6.4	ns
			3.3 ± 0.3	0.8	2.9	4.4	0.8	4.8	
			5.0 ± 0.5	0.5	2.3	3.5	0.5	3.9	
	t _{pLZ}	$C_L = 50 \text{ pF}, R_L = 500 \Omega$	1.8 ± 0.15	1.8	4.3	9.5	1.8	10.5	
			2.5 ± 0.2	1.2	2.8	5.8	1.2	6.4	
			3.3 ± 0.3	8.0	2.1	4.4	0.8	4.8	
			5.0 ± 0.5	0.5	1.4	3.5	0.5	3.9	
Input capacitance	C _{IN}	_	0~5.5	_	3.0		_	_	pF
Output capacitance	C _{OUT}	_	0~5.5		3.0		_	_	pF
Power dissipation capacitance	C _{PD} (Note)	(Note)	3.3	_	5.2	_		_	pF
		5.5	_	8.5	_		_	ρΓ	

Note: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

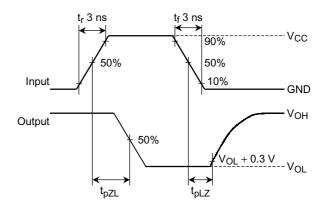
Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/3$

Test Circuit

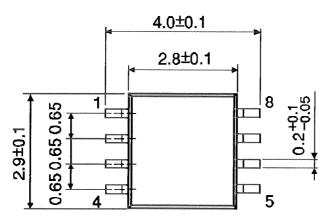
Output O $R_L \geqslant Measure$ $R_L \geqslant Measure$

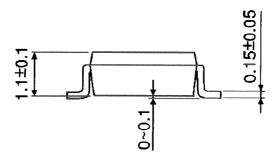
AC Waveform



Package Dimensions

SSOP8-P-0.65 Unit: mm



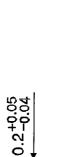


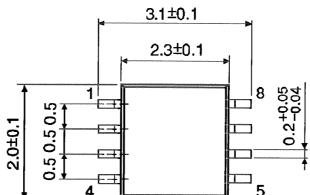
Weight: 0.02 g (typ.)

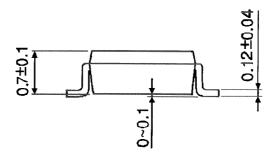
Unit: mm

Package Dimensions

SSOP8-P-0.50A







Weight: 0.01 g (typ.)