TOSHIBA Photocoupler GaAs Ired & Photo-Triac

TLP3520

Triac Driver **Programmable Controllers** AC-Output Module Solid State Relay

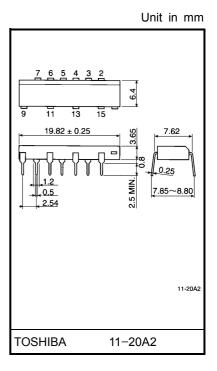
The TOSHIBA TLP3520 consists of a photo-triac optically coupled to a gallium arsenide infrared emitting diode in a 16 lead plastic DIP package.

- Peak off-state voltage: 400 V (min.)
- Trigger LED current: 10 mA (max.)
- On-state current: 1.0 A_{rms} (max.)
- Isolation voltage: 2500 V_{rms} (min.)
- UL recognized: UL1577, file no. E67349
- Trigger LED current

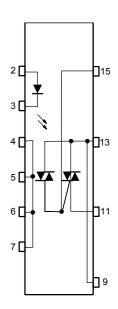
Classi– fication *	Trigger LED Current (mA) V _T = 6 V, Ta = 25°C		Marking Of Classification		
	Min.	Max.	Classification		
(IFT5)	_	5.0	T5		
(IFT7)	_	7.0	T5, T7		
Standard	_	10	T5, T7, blank		

*Ex. (IFT5); TLP3520 (IFT5)

(Note) Application type name for certification test, please use standard product type name, i.e.TLP3520 (IFT5): TLP3520



Pin Configuration (top view)



2: Anode 3: Cathode 4,5,6,7: N.C.

9,13: Triac T2 11: Triac T1

15: Triac gate

Maximum Ratings (Ta = 25°C)

Characteristic			Symbol	Rating	Unit	
	Forward current	I _F	50	mA		
LED	Forward current derating (Ta ≥ 53	ΔI _F / °C	-0.7	mA / °C		
	Peak forward current (100 µs puls	I _{FP}	1	Α		
	Reverse voltage	V _R	5	V		
	Junction temperature	Tj	125	°C		
	Off-state output terminal vaoltage		V_{DRM}	400	V	
	On-state RMS current	Ta = 40°C	l=(p, io)	1.0	Α	
Detector		Ta = 60°C	I _{T(RMS)}	0.7	^	
	On–state current derating (Ta ≥ 40	ΔI _T / °C	-14.3	mA / °C		
	Peak current from snubber circuit (100 µs pulse, 120 pps)	I _{SP}	2	А		
	Peak nonrepetitive surge current (I _{TSM}	10	Α		
	Junction temperature	Tj	110	°C		
Storage temperature range			T _{stg}	-40~125	°C	
Operating temperature range		T _{opr}	-20~80	°C		
Lead soldering temperature (10 s)			T _{sol}	260	°C	
Isolatio	Isolation voltage (AC, 1 min., R.H.≤ 60%) (Note)			2500	V _{rms}	

(Note) Device considered a two terminal: LED side pins shorted together and detector side pins shorted together.

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Recommended Operating Conditions

Characteristic	Symbol	Min.	Тур.	Max.	Unit
Supply voltage	V_{AC}	-	_	120	V _{ac}
Forward current	I _F	15	20	25	mA
Peak current from snubber circuit	I _{SP}		_	1	Α
Operating temperature	T_{opr}	-20	_	80	°C

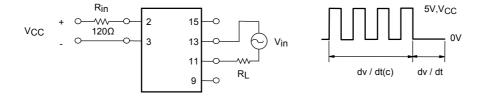
Individual Electrical Characteristics (Ta = 25°C)

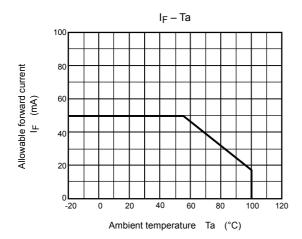
Characteristic		Symbol	Test Condition	Min.	Тур.	Max.	Unit
LED	Forward voltage	V _F	I _F = 10 mA	1.0	1.15	1.3	V
	Reverse current	I _R	V _R = 5 V	_	_	10	μΑ
	Capacitance	C _T	V = 0, f = 1 MHz	_	30	_	pF
Detector	Peak off-state current	I _{DRM}	V _{DRM} = 400 V, Ta = 110°C	_	_	100	μA
	Peak on-state voltage	V _{TM}	I _{TM} = 1.5 A	_	_	3.0	V
	Holding current	lΗ	R _L = 100Ω	_	_	25	mA
	Critical rate of rise of off–state voltage	dv / dt	$V_{in} = 120 V_{rms}$ (Fig.1)	200	500	_	V / µs
	Critical rate of rise of commutating voltage	dv / dt (c)	V _{in} = 120 V _{rms} , I _T = 1.0 A _{rms} (Fig.1)	_	5	_	V / µs

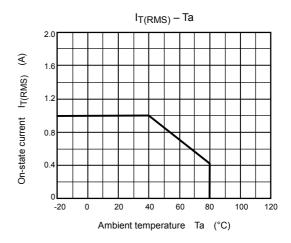
Coupled Electrical Characteristics (Ta = 25°C)

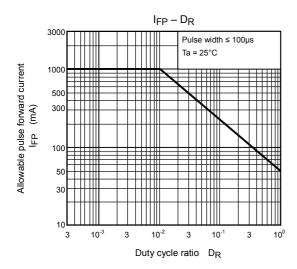
Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Trigger LED current	I _{FT}	V _T = 6 V	-	_	10	mA
Capacitance (input to output)	C _S	V _S = 0, f = 1 MHz	1	1.5	١	pF
Isolation resistance	R _S	V _S = 500 V	5×10 ¹⁰	10 ¹⁴	_	Ω
	BVS	AC, 1 minute	2500	_	_	- V _{rms}
Isolation voltage		AC, 1 second, in oil	_	5000	_	
		DC, 1 minute, in oil	_	5000	_	V _{dc}

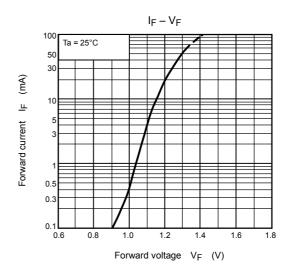
Fig.1: dv / dt test circuit

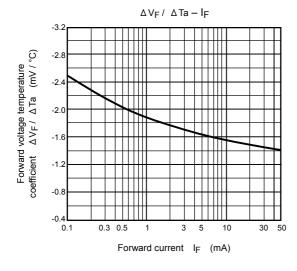


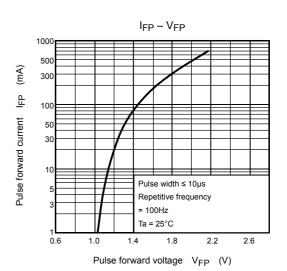


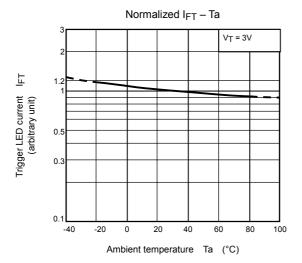


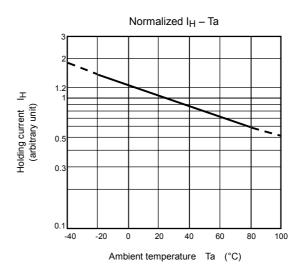


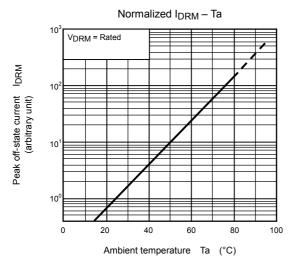


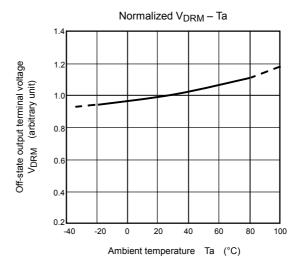


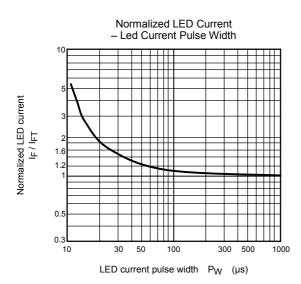












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