TOSHIBA Field Effect Transistor Silicon P Channel MOS Type (U-MOS III)

## **TPCF8102**

# Notebook PC Applications Portable Equipment Applications

- Low drain-source ON resistance: RDS (ON) = 24 m $\Omega$  (typ.)
- High forward transfer admittance:  $|Y_{fs}| = 14 \text{ S (typ.)}$
- Low leakage current:  $IDSS = -10 \mu A (max) (VDS = -20 V)$
- Enhancement mode:  $V_{th} = -0.5 \text{ to } -1.2 \text{ V}$

 $(V_{DS} = -10 \text{ V}, I_{D} = -200 \mu\text{A})$ 

## **Maximum Ratings (Ta = 25°C)**

Characte	ristics	Symbol	Rating	Unit	
Drain-source voltage		$V_{DSS}$	-20	V	
Drain-gate voltage (R	<sub>GS</sub> = 20 kΩ)	V <sub>DGR</sub>	-20	V	
Gate-source voltage		$V_{GSS}$	±8	٧	
Drain current	DC (Note 1)	I <sub>D</sub>	-6	А	
Diain current	Pulsed (Note 1)	I <sub>DP</sub>	-24		
Drain power dissipation	on $(t = 5 s)$ (Note 2a)	$P_{D}$	2.5	W	
Drain power dissipation	on (t = 5 s) (Note 2b)	P <sub>D</sub>	0.7	W	
Single pulse avalanch	ne energy(Note 3)	E <sub>AS</sub>	5.9	mJ	
Avalanche current		I <sub>AR</sub>	-3	Α	
Repetitive avalanche	energy (Note 4)	E <sub>AR</sub>	0.25	mJ	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature	range	T <sub>stg</sub>	-55~150	°C	

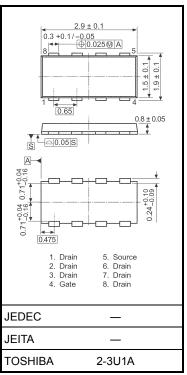
#### **Thermal Characteristics**

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to ambient (t = 5 s) (Note 2a)	R <sub>th (ch-a)</sub>	50.0	°C/W
Thermal resistance, channel to ambient (t = 5 s) (Note 2b)	R <sub>th (ch-a)</sub>	178.6	°C/W

Note 1, Note 2, Note 3, Note 4 and Note 5: See the next page.

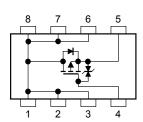
This transistor is an electrostatic-sensitive device. Please handle with caution.

Unit: mm



Weight: 0.011 g (typ.)

## **Circuit Configuration**



## Electrical Characteristics (Ta = 25°C)

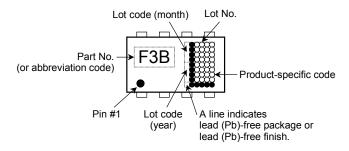
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit	
Gate leakage cur	rent	I <sub>GSS</sub>	$V_{GS} = \pm 8 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μΑ	
Drain cut-off current		I <sub>DSS</sub>	$V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V}$	_	_	-10	μΑ	
Drain-source breakdown voltage		V <sub>(BR)DSS</sub>	$I_D = -10$ mA, $V_{GS} = 0$ V	-20			V	
		V <sub>(BR)DSX</sub>	$I_D = -10$ mA, $V_{GS} = 8$ V	-12				
Gate threshold ve	oltage	$V_{th}$	$V_{DS} = -10 \ V, \ I_D = -200 \ \mu A$	-0.5	_	-1.2	V	
		R <sub>DS (ON)</sub>	$V_{GS} = -1.8 \text{ V}, I_D = -1.5 \text{ A}$	_	67	90		
Drain-source ON resistance		R <sub>DS (ON)</sub>	$V_{GS} = -2.5 \text{ V}, I_D = -3.0 \text{ A}$	_	36	41	$m\Omega$	
		R <sub>DS (ON)</sub>	$V_{GS} = -4.5 \text{ V}, I_D = -3.0 \text{ A}$	_	24	30		
Forward transfer	admittance	Y <sub>fs</sub>	$V_{DS} = -10 \text{ V}, I_D = -3.0 \text{ A}$	7	14		S	
Input capacitance		C <sub>iss</sub>		_	1550		pF	
Reverse transfer capacitance		C <sub>rss</sub>	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	215	_		
Output capacitance		C <sub>oss</sub>		_	265			
Switching time	Rise time	t <sub>r</sub>	$V_{GS} = -3.0 \text{ A}$	_	7			
	Turn-on time	t <sub>on</sub>		_	13		ns	
	Fall time	t <sub>f</sub>	4.7.0 4.7.0 4.7.0 4.7.0 8.0 1.0 1.0 1.0 1.0 1.0 1.0 1.0 1	_	21		115	
	Turn-off time	t <sub>off</sub>	$V_{DD} \simeq -10 \text{ V}$ Duty $\leq$ 1%, $t_W = 10 \mu\text{s}$	_	68			
Total gate charge (gate-source plus gate-drain)		Qg	$V_{DD} \simeq -16 \text{ V}, V_{GS} = -5 \text{ V},$		19			
Gate-source charge		Q <sub>gs</sub>	$I_D = -6.0 \text{ A}$		14		nC	
Gate-drain ("miller") charge		Q <sub>gd</sub>		_	5	_		

## Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics Symbol		Symbol	Test Condition	Min	Тур.	Max	Unit
Drain reverse current	Pulse (Note 1)	I <sub>DRP</sub>	_	_	_	-24	Α
Forward voltage (diode) V <sub>DSF</sub>		V <sub>DSF</sub>	$I_{DR} = -6.0 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	1.2	V

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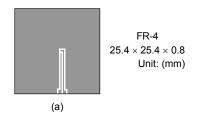
## Marking (Note 5)

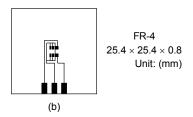


Note 1: Ensure that the channel temperature does not exceed 150°C.

#### Note 2: (a) Device mounted on a glass-epoxy board (a)

(b) Device mounted on a glass-epoxy board (b)

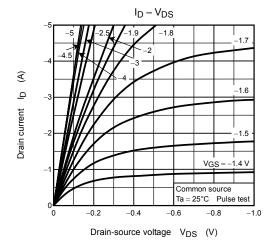


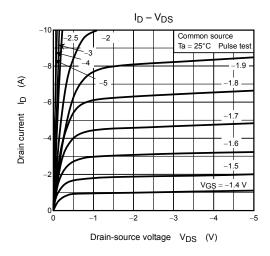


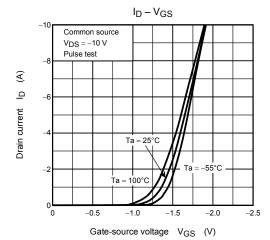
Note 3:  $V_{DD} = -16~V$ ,  $T_{ch} = 25^{\circ}C$  (initial), L = 0.5~mH,  $R_G = 25~\Omega$ ,  $I_{AR} = -3.0~A$ 

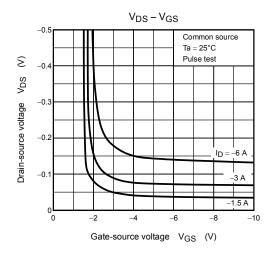
Note 4: Repetitive rating: pulse width limited by maximum channel temperature

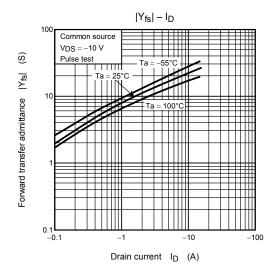
Note 5: A dot on the lower left of the marking indicates Pin 1.

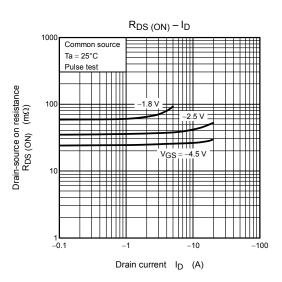


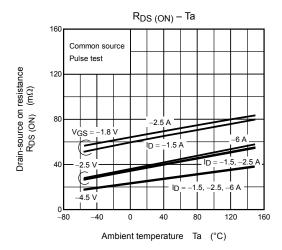


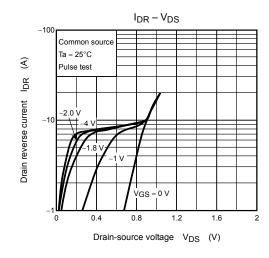


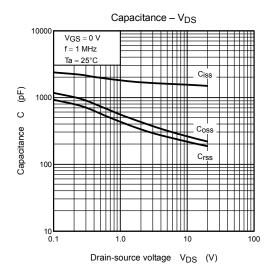


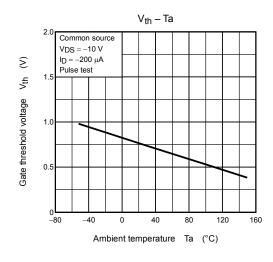


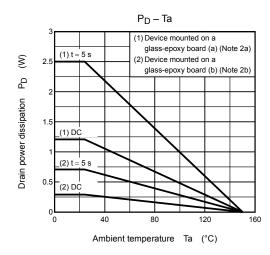


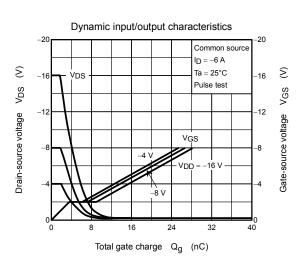




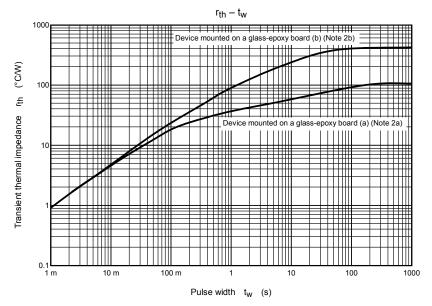


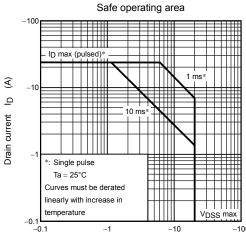






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Drain-source voltage  $V_{DS}$  (V)

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Handbook" etc..

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