Unit: mm

TOSHIBA Field Effect Transistor Silicon P Channel MOS Type (U-MOSII)

TPC6101

Notebook PC Applications Portable Equipment Applications

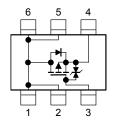
- Low drain-source ON resistance: $RDS(ON) = 48 \text{ m}\Omega$ (typ.)
- High forward transfer admittance: $|\,Y_{\rm fs}\,|$ = 8.2 S (typ.)
- Low leakage current: $I_{DSS} = -10 \mu A (max) (V_{DS} = -20 V)$
- Enhancement model: V_{th} = -0.5 to -1.2 V (V_{DS} = -10 V, I_D = -200 $\mu A)$

Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit		
Drain-source voltage		V _{DSS}	-20	V	
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)		V _{DGR}	-20	V	
Gate-source voltage		V _{GSS}	±12	V	
Drain current	DC (Note 1)	۱ _D	-4.5	A	
	Pulse (Note 1)	I _{DP}	-18		
Drain power dissipation	(t = 5 s) (Note 2a)	PD	2.2	W	
Drain power dissipation	(t = 5 s) (Note 2b)	PD	0.7	W	
Single pulse avalanche ene	E _{AS}	3.3	mJ		
Avalanche current	I _{AR}	-2.25	А		
Repetitive avalanche energ	E _{AR}	0.22	mJ		
Channel temperature	T _{ch}	150	°C		
Storage temperature range	T _{stg}	-55 to 150	°C		

Weight: 0.011 g (typ.)

Circuit Configuration



Thermal Characteristics

Characteristics	Symbol	Max	Unit	
Thermal resistance, channel to ambient $(t = 5 s)$ (Note 2a)	R _{th (ch-a)}	56.8	°C/W	
Thermal resistance, channel to ambient $(t = 5 s)$ (Note 2b)	R _{th (ch-a)}	178.5	°C/W	

Note 1, Note 2, Note 3, Note 4 and Note 5: See the next page.

This transistor is an electrostatic- sensitive device. Please handle with caution.

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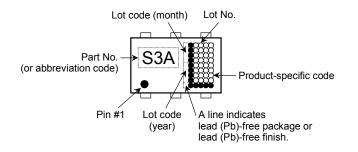
Electrical Characteristics (Ta = 25°C)

Cha	aracteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I _{GSS}	$V_{GS}=\pm 10~V,~V_{DS}=0~V$	_	_	±10	μA
Drain cut-OFF cu	rrent	I _{DSS}	$V_{DS}=-20~V,~V_{GS}=0~V$	_	_	-10	μA
Drain-source breakdown voltage		V (BR) DSS	$I_D = -10$ mA, $V_{GS} = 0$ V	-20		_	V
		V (BR) DSX	$I_D = -10$ mA, $V_{GS} = 12$ V	-8			v
Gate threshold ve	oltage	V _{th}	$V_{DS} = -10 \ V, \ I_D = -200 \ \mu A$	-0.5		-1.2	V
Drain-source ON resistance		R _{DS (ON)}	$V_{GS} = -2 V, I_D = -2.2 A$	—	110	180	mΩ
		R _{DS (ON)}	$V_{GS} = -2.5 \text{ V}, \text{ I}_{D} = -2.2 \text{ A}$	_	75	100	
		R _{DS (ON)}	$V_{GS}=-4.5~V,~I_D=-2.2~A$	_	48	60	
Forward transfer admittance		Y _{fs}	$V_{DS} = -10 \text{ V}, \text{ I}_{D} = -2.2 \text{ A}$	4.1	8.2	_	S
Input capacitance		C _{iss}	$V_{DS} = -10$ V, $V_{GS} = 0$ V, f = 1 MHz	_	830	_	pF
Reverse transfer capacitance		C _{rss}		_	300	_	
Output capacitance		C _{oss}		_	370	_	
Switching time	Rise time	tr	$V_{GS} \xrightarrow{0 V}_{-5 V} \xrightarrow{I_D = -2.2 A}_{C} \xrightarrow{V_{OUT}}_{C} \xrightarrow{G} \xrightarrow{V}_{C} \xrightarrow{V}_{U} \xrightarrow{V}_{U} \xrightarrow{V}_{DD} \simeq -10 V$ Duty $\leq 1\%$, $t_w = 10 \ \mu s$	_	6	_	ns
	Turn-ON time	t _{on}		_	11	_	
	Fall time	tf		_	57	_	
	Turn-OFF time	t _{off}		_	112	_	
Total gate charge (gate-source plus gate-drain)		Qg	VDD ≃ −16 V, VGS = −5 V,	_	12	_	nC
Gate-source charge		Q _{gs}	$I_{\rm D} = -4.5 \rm{A}$	_	6		
Gate-drain ("miller") charge		Q _{gd}			6		

Source-Drain Ratings and Characteristics (Ta = 25°C)

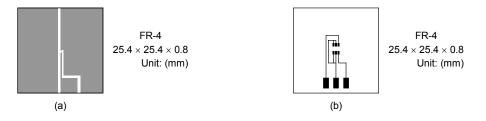
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Pulse drain reverse current	(Note 1)	I _{DRP}	—	_	_	-18	А
Forward voltage (diode)		V _{DSF}	I_{DR} = -4.5 A, V_{GS} = 0 V			1.2	V

Marking (Note 5)



Note 1: Ensure that the channel temperature does not exceed 150°C.

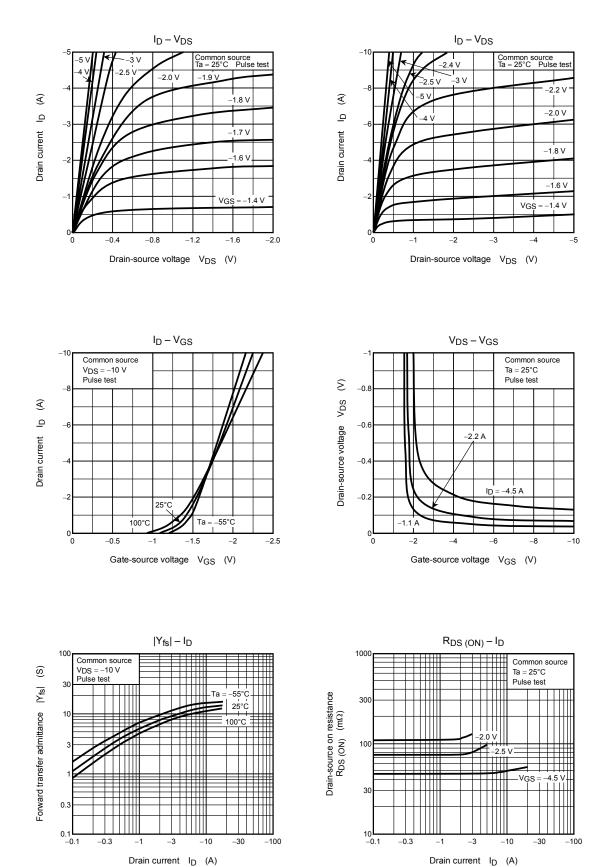
Note 2: (a) Device mounted on a glass-epoxy board (a) (t = 5 s)(b) Device mounted on a glass-epoxy board (b) (t = 5 s)

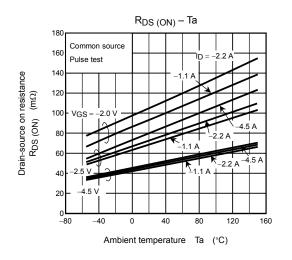


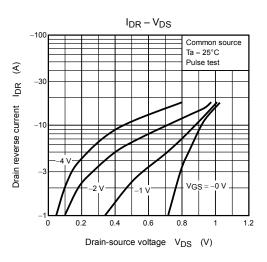
Note 3: $V_{DD} = 16 \text{ V}, \text{ T}_{ch} = 25^{\circ}\text{C}$ (initial), L = 0.5 mH, R_G = 25 Ω , I_{AR} = -2.25 A

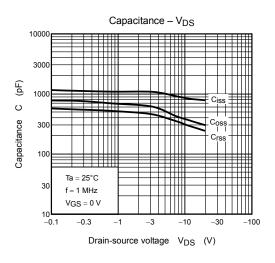
Note 4: Repetitive rating: pulse width limited by maximum channel temperature

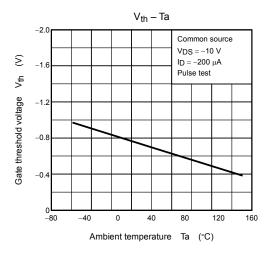
Note 5: • on lower left of the marking indicates Pin 1.

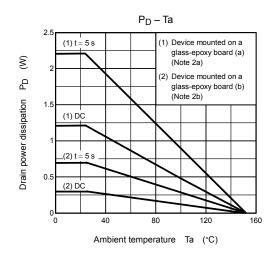


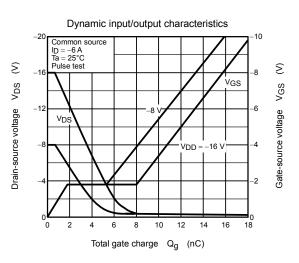


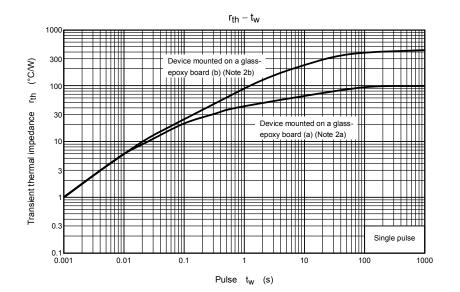




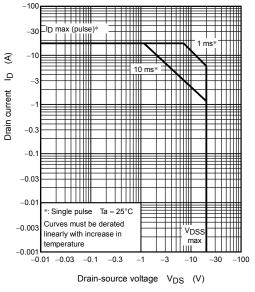












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