

MP6404

High Power High Speed Switching Applications
 3-Phase Motor Drive and Stepping Motor Drive Applications

- 4-V gate drivability
- Small package by full molding (SIP 12 pins)
- High drain power dissipation (6-device operation)
 : P_T = 36 W (T_c = 25°C)
- Low drain-source ON resistance: R_{DS (ON)} = 120 mΩ (typ.) (Nch)
 160 mΩ (typ.) (Pch)
- High forward transfer admittance: |Y_{fs}| = 5.0 S (typ.) (Nch)
 4.0 S (typ.) (Pch)
- Low leakage current: I_{GSS} = ±10 μA (max) (V_{GS} = ±16 V)
 I_{DSS} = 100 μA (max) (V_{DS} = 60 V)
- Enhancement-mode: V_{th} = 0.8 V to 2.0 V (V_{DS} = 10 V, I_D = 1 mA)

Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating		Unit	
		Nch	Pch		
Drain-source voltage	V _{DSS}	60	-60	V	
Drain-gate voltage (R _{GS} = 20 kΩ)	V _{DGR}	60	-60	V	
Gate-source voltage	V _{GSS}	±20	±20	V	
Drain current	DC	I _D	5	-5	A
	Pulse	I _{DP}	20	-20	
Drain power dissipation (1-device operation, Ta = 25°C)	P _D	2.2		W	
Drain power dissipation (6-device operation)	Ta = 25°C	P _{DT}		W	
	Tc = 25°C	36			
Single pulse avalanche energy (Note 1)	E _{AS}	129	273	mJ	
Avalanche current	I _{AR}	5	-5	A	
Repetitive avalanche energy (Note 2)	1 device operation	E _{AR}	0.22		mJ
	6 device operation	E _{ART}	0.44		
Channel temperature	T _{ch}	150		°C	
Storage temperature range	T _{stg}	-55 to 150		°C	

Note 1: Condition for avalanche energy (single pulse)

Nch: V_{DD} = 25 V, starting T_{ch} = 25°C, L = 7 mH, R_G = 25 Ω, I_{AR} = 5 A

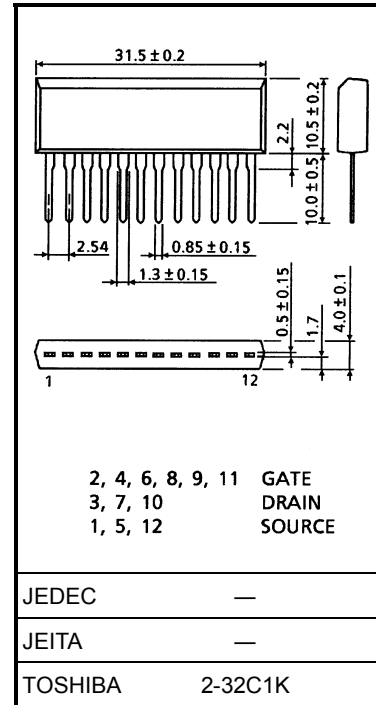
Pch: V_{DD} = -25 V, starting T_{ch} = 25°C, L = 14.84 mH, R_G = 25 Ω, I_{AR} = -5 A

Note 2: Repetitive rating; pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.

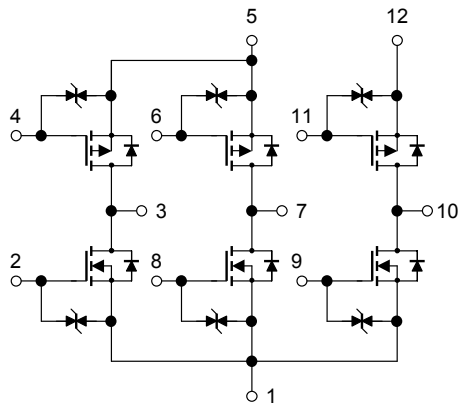
Industrial Applications

Unit: mm



Weight: 3.9 g (typ.)

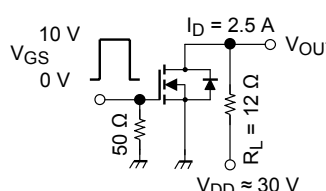
Array Configuration



Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance of channel to ambient (6-device operation, $T_a = 25^\circ\text{C}$)	$\Sigma R_{th(ch-a)}$	28.4	$^\circ\text{C/W}$
Thermal resistance of channel to case (6-device operation, $T_c = 25^\circ\text{C}$)	$\Sigma R_{th(ch-c)}$	3.47	$^\circ\text{C/W}$
Maximum lead temperature for soldering purposes (3.2 mm from case for $t = 10$ s)	T_L	260	$^\circ\text{C}$

Electrical Characteristics (Ta = 25°C) (Nch MOS FET)

Characteristics		Symbol	Test Condition	Min	Typ.	Max	Unit
Gate leakage current		I_{GSS}	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$	—	—	± 10	μA
Drain cut-off current		I_{DSS}	$V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}$	—	—	100	μA
Drain source breakdown voltage		$V_{(BR)DSS}$	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	60	—	—	V
Gate threshold voltage		V_{th}	$V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA}$	0.8	—	2.0	V
Drain-source ON resistance		$R_{DS(ON)}$	$V_{GS} = 4 \text{ V}, I_D = 2.5 \text{ A}$	—	0.21	0.32	Ω
			$V_{GS} = 10 \text{ V}, I_D = 2.5 \text{ A}$	—	0.12	0.16	
Forward transfer admittance		$ Y_{fs} $	$V_{DS} = 10 \text{ V}, I_D = 2.5 \text{ A}$	3.0	5.0	—	S
Input capacitance		C_{iss}	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	—	370	—	μF
Reverse transfer capacitance		C_{rss}		—	60	—	μF
Output capacitance		C_{oss}		—	180	—	μF
Switching time	Rise time	t_r		—	18	—	ns
	Turn-on time	t_{on}		—	25	—	
	Fall time	t_f		—	55	—	
	Turn-off time	t_{off}		—	170	—	
Total gate charge (gate-source plus gate-drain)		Q_g	$V_{DD} \approx 48 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 5 \text{ A}$	—	12	—	nC
Gate-source charge		Q_{gs}		—	8	—	nC
Gate-drain ("miller") charge		Q_{gd}		—	4	—	nC

Source-Drain Diode Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Continuous drain reverse current	I_{DR}	—	—	—	5	A
Pulse drain reverse current	I_{DRP}	—	—	—	20	A
Diode forward voltage	V_{DSF}	$I_{DR} = 5 \text{ A}, V_{GS} = 0 \text{ V}$	—	—	-1.7	V
Reverse recovery time	t_{rr}	$I_{DR} = 5 \text{ A}, V_{GS} = 0 \text{ V}$	—	70	—	ns
Reverse recovery charge	Q_{rr}	$dI_{DR}/dt = 50 \text{ A}/\mu\text{s}$	—	0.1	—	μC

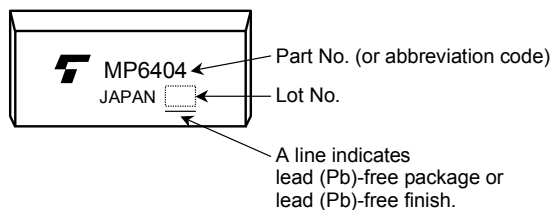
Electrical Characteristics (Ta = 25°C) (Pch MOS FET)

Characteristics		Symbol	Test Condition	Min	Typ.	Max	Unit
Gate leakage current		I_{GSS}	$V_{GS} = \pm 16\text{ V}, V_{DS} = 0\text{ V}$	—	—	± 10	μA
Drain cut-off current		I_{DSS}	$V_{DS} = -60\text{ V}, V_{GS} = 0\text{ V}$	—	—	-100	μA
Drain source breakdown voltage		$V_{(BR)DSS}$	$I_D = -10\text{ mA}, V_{GS} = 0\text{ V}$	-60	—	—	V
Gate threshold voltage		V_{th}	$V_{DS} = -10\text{ V}, I_D = -1\text{ mA}$	-0.8	—	-2.0	V
Drain-source ON resistance		$R_{DS(ON)}$	$V_{GS} = -4\text{ V}, I_D = -2.5\text{ A}$	—	0.24	0.28	Ω
			$V_{GS} = -10\text{ V}, I_D = -2.5\text{ A}$	—	0.16	0.19	
Forward transfer admittance		$ Y_{fs} $	$V_{DS} = -10\text{ V}, I_D = -2.5\text{ A}$	2.0	4.0	—	S
Input capacitance		C_{iss}	$V_{DB} = -10\text{ V}, V_{GS} = 0\text{ V}, f = 1\text{ MHz}$	—	630	—	μF
Reverse transfer capacitance		C_{rss}		—	95	—	μF
Output capacitance		C_{oss}		—	290	—	μF
Switching time	Rise time	t_r		—	25	—	ns
	Turn-on time	t_{on}		—	45	—	
	Fall time	t_f		—	55	—	
	Turn-off time	t_{off}		—	200	—	
Total gate charge (gate-source plus gate-drain)		Q_g	$V_{DD} \approx -48\text{ V}, V_{GS} = -10\text{ V}, I_D = -5\text{ A}$	—	22	—	nC
Gate-source charge		Q_{gs}		—	16	—	nC
Gate-drain ("miller") charge		Q_{gd}		—	6	—	nC

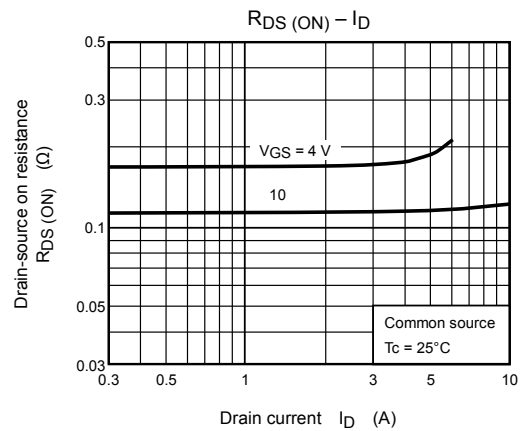
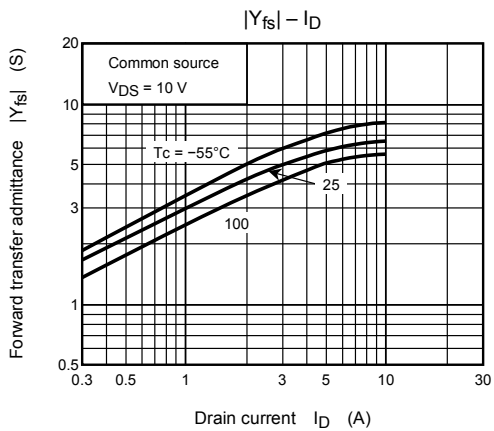
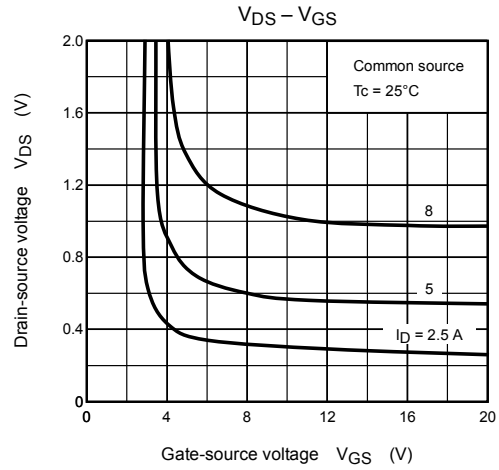
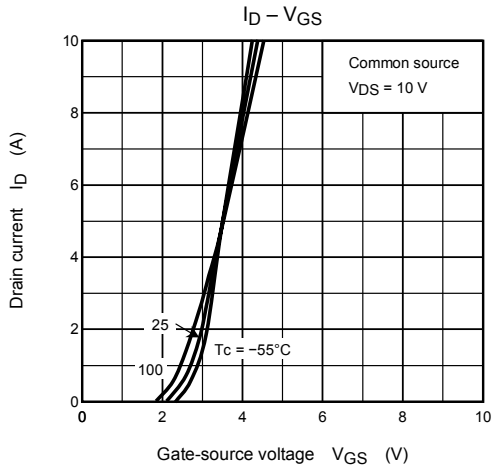
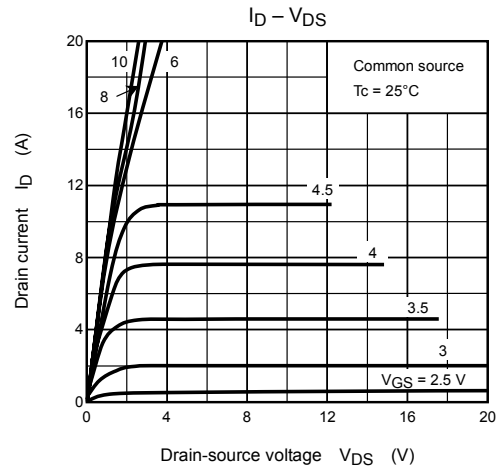
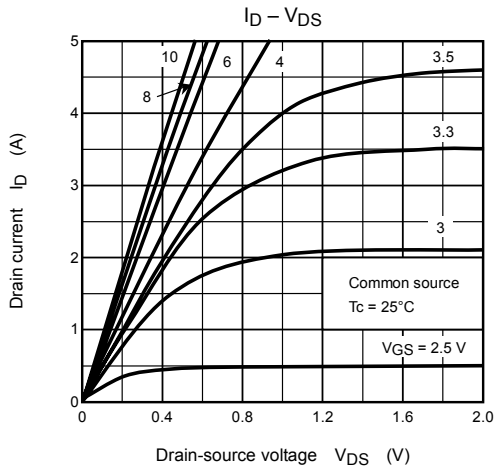
Source-Drain Diode Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Continuous drain reverse current	I_{DR}	—	—	—	-5	A
Pulse drain reverse current	I_{DRP}	—	—	—	-20	A
Diode forward voltage	V_{DSF}	$I_{DR} = -5\text{ A}, V_{GS} = 0\text{ V}$	—	—	1.7	V
Reverse recovery time	t_{rr}	$I_{DR} = -5\text{ A}, V_{GS} = 0\text{ V}$	—	80	—	ns
Reverse recovery charge	Q_{rr}	$dI_{DR}/dt = 50\text{ A}/\mu\text{s}$	—	0.1	—	μC

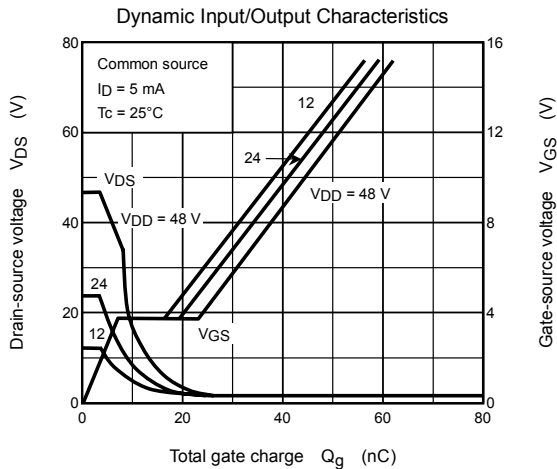
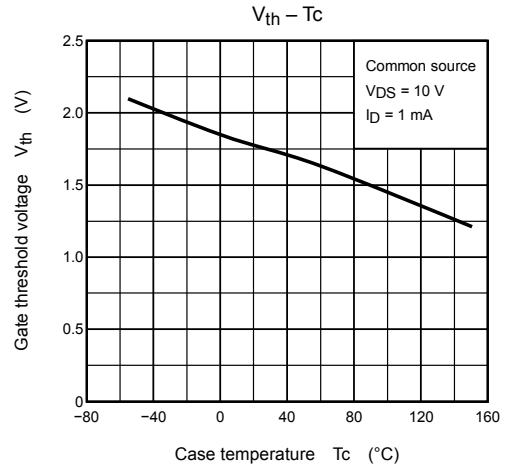
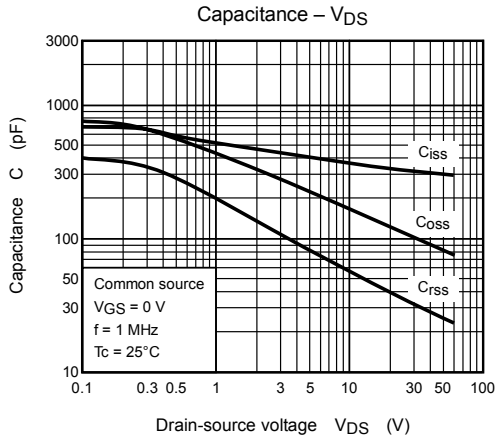
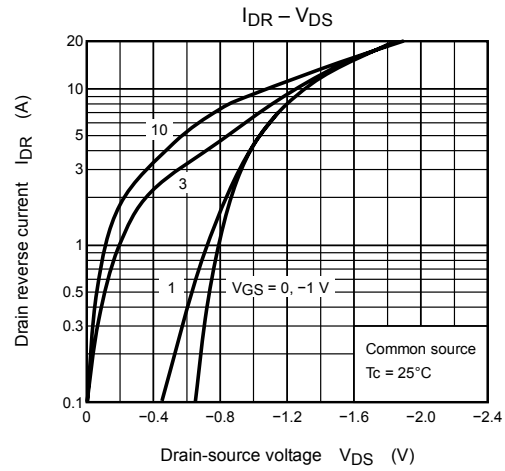
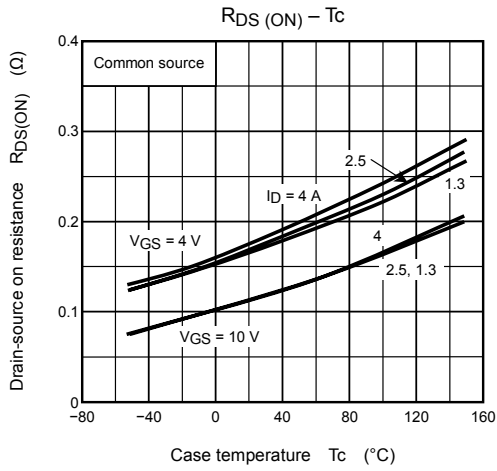
Marking



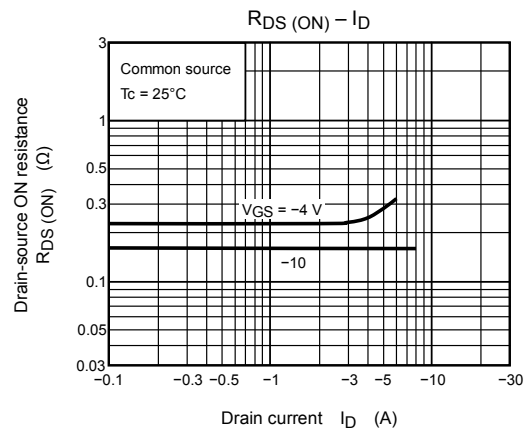
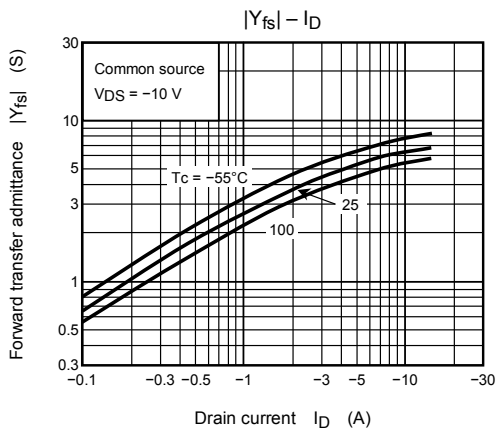
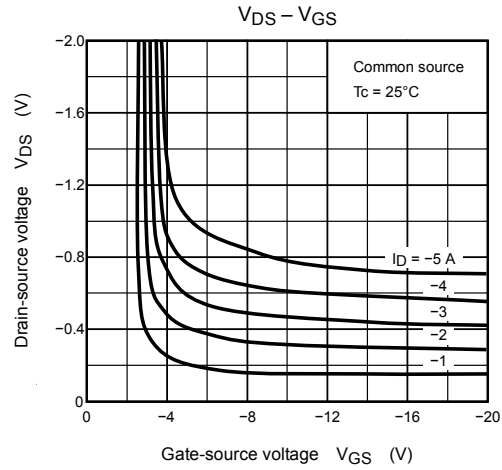
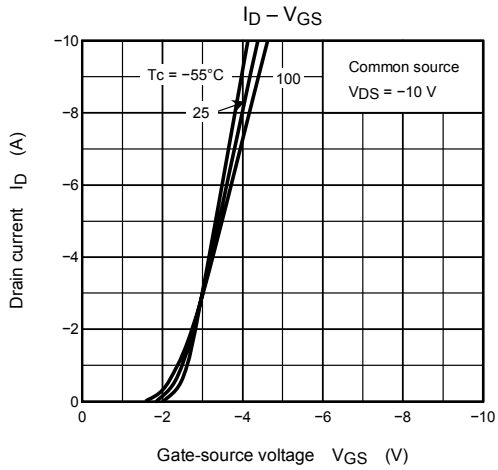
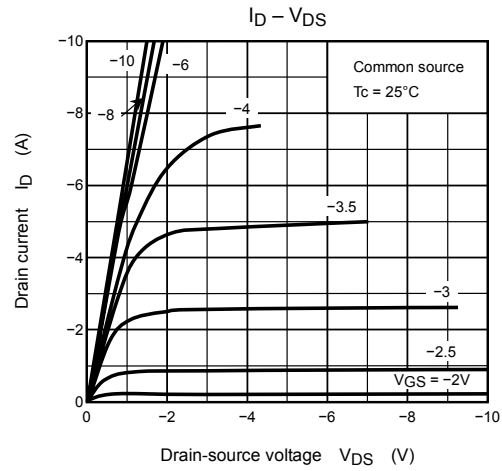
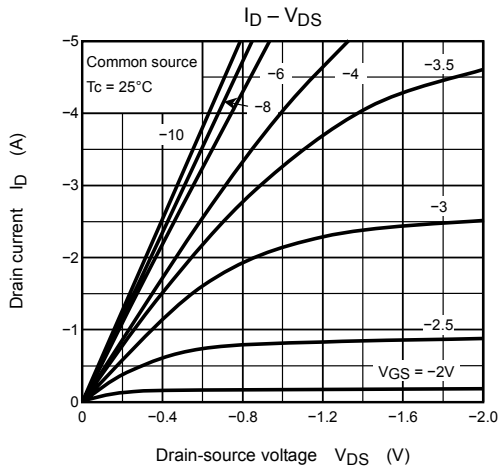
Nch MOS FET



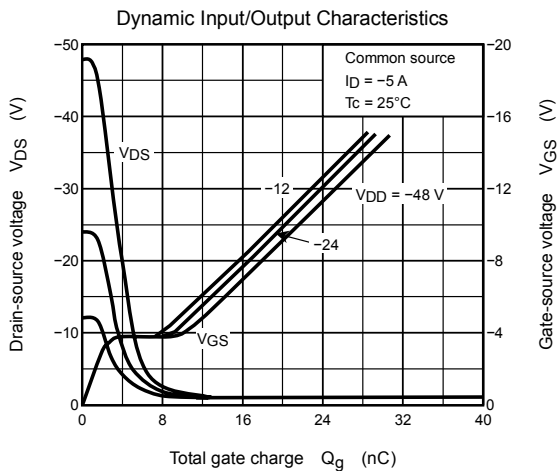
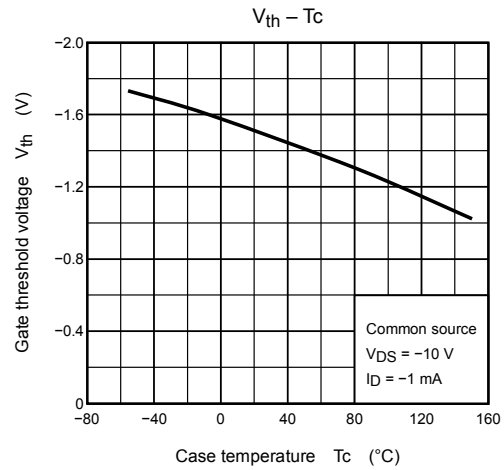
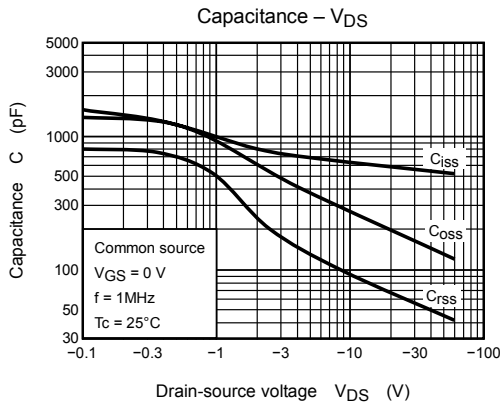
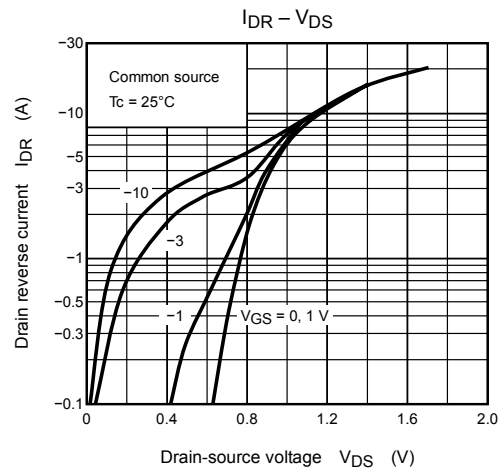
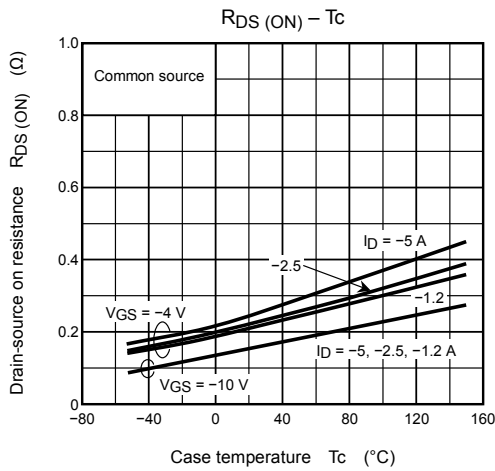
Nch MOS FET

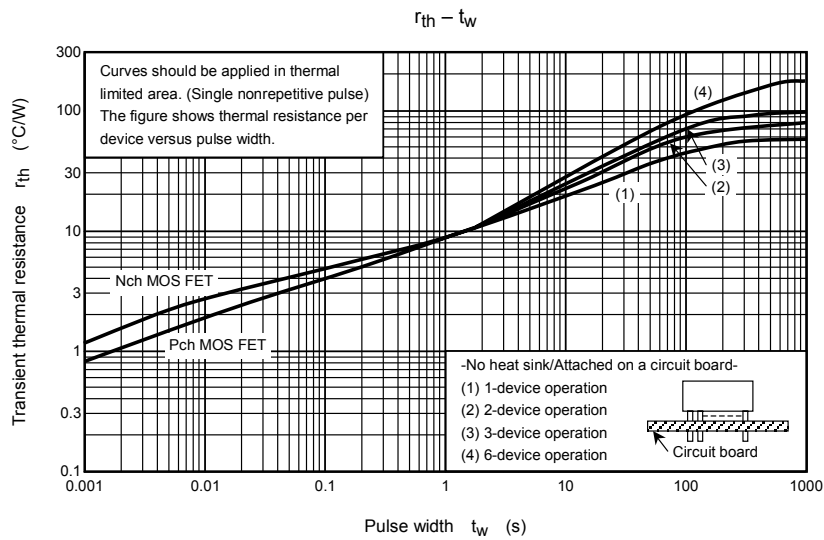
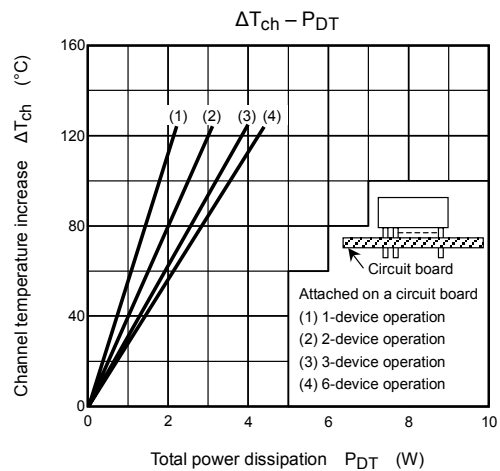
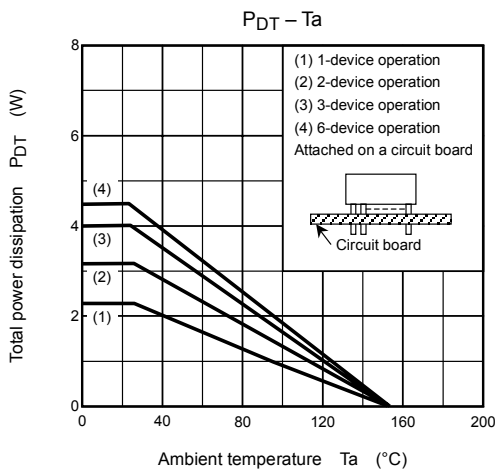


Pch MOS FET

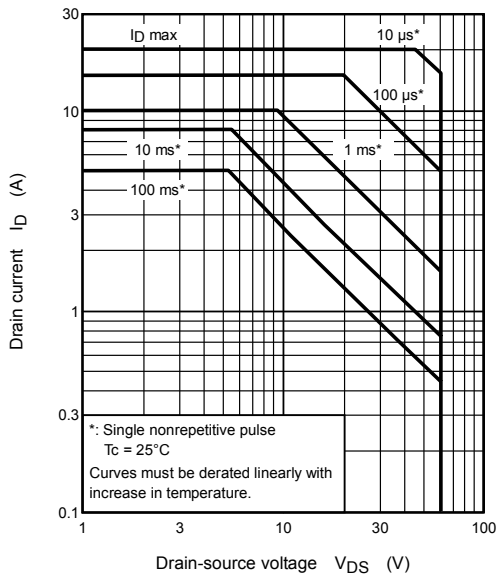


Pch MOS FET

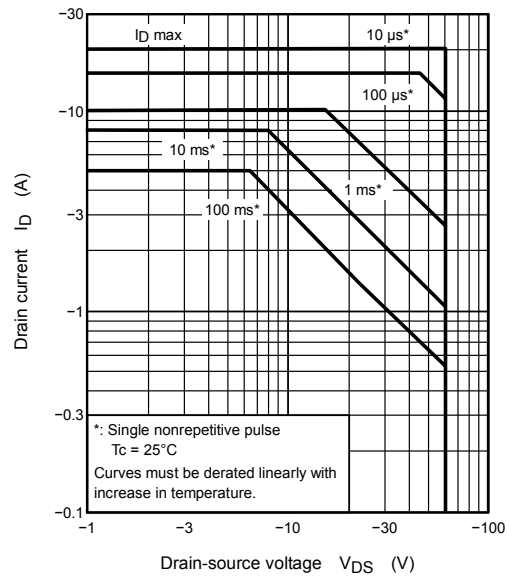




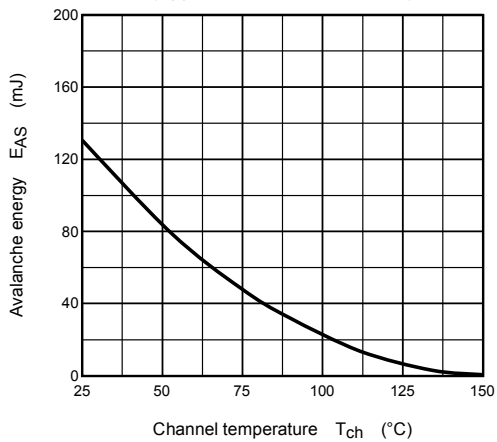
Safe Operating Area
(Applicable to Nch MOS FET)



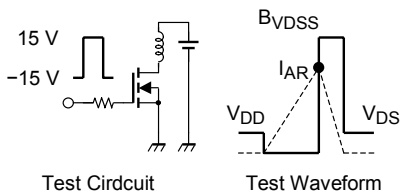
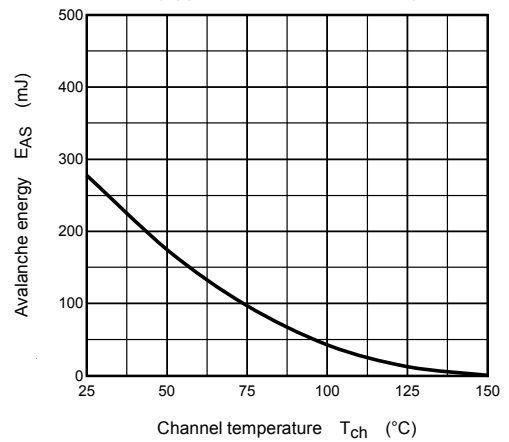
Safe Operating Area
(Applicable to Pch MOS FET)



EAS - T_{ch}
(Applicable to Nch MOS FET)

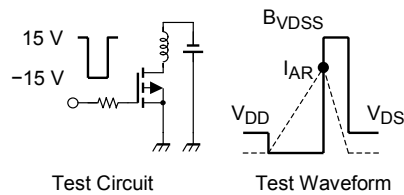


EAS - T_{ch}
(Applicable to Pch MOS FET)



Peak $I_{AR} = 5 \text{ A}$, $R_G = 25 \Omega$
 $V_{DD} = 25 \text{ V}$, $L = 7 \text{ mH}$

$$EAS = \frac{1}{2} L \cdot I^2 \cdot \left(\frac{BVDSS}{BVDSS - V_{DD}} \right)$$



Peak $I_{AR} = -5 \text{ A}$, $R_G = 25 \Omega$
 $V_{DD} = -25 \text{ V}$, $L = 14.84 \text{ mH}$

$$EAS = \frac{1}{2} L \cdot I^2 \cdot \left(\frac{BVDSS}{BVDSS - V_{DD}} \right)$$

RESTRICTIONS ON PRODUCT USE

030619EAA

- The information contained herein is subject to change without notice.
- The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by TOSHIBA for any infringements of patents or other rights of the third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of TOSHIBA or others.
- TOSHIBA is continually working to improve the quality and reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to comply with the standards of safety in making a safe design for the entire system, and to avoid situations in which a malfunction or failure of such TOSHIBA products could cause loss of human life, bodily injury or damage to property.
In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability Handbook" etc..
- The TOSHIBA products listed in this document are intended for usage in general electronics applications (computer, personal equipment, office equipment, measuring equipment, industrial robotics, domestic appliances, etc.). These TOSHIBA products are neither intended nor warranted for usage in equipment that requires extraordinarily high quality and/or reliability or a malfunction or failure of which may cause loss of human life or bodily injury ("Unintended Usage"). Unintended Usage include atomic energy control instruments, airplane or spaceship instruments, transportation instruments, traffic signal instruments, combustion control instruments, medical instruments, all types of safety devices, etc.. Unintended Usage of TOSHIBA products listed in this document shall be made at the customer's own risk.
- TOSHIBA products should not be embedded to the downstream products which are prohibited to be produced and sold, under any law and regulations.