TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

# TA76432AFT,TA76432AF,TA76432AFR,TA76432AS

#### 1.26-V Adjustable High-Precision Shunt Regulators

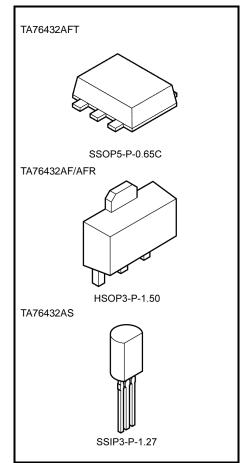
The TA76432 series consists of adjustable high-precision shunt regulators whose output voltage (VKA) can be set arbitrarily using two external resistors.

These devices have a precise internal reference voltage of 1.26 V, enabling them to operate at low voltage.

The devices are ideal for use as error amplifiers in 3-V switching-regulator systems. In addition, they can be used as zener diodes to perform temperature compensation.

#### **Features**

- Precision reference voltage:  $V_{REF} = 1.26 \text{ V} \pm 1\% \text{ (Ta} = 25^{\circ}\text{C)}$
- Small temperature coefficient: | αVREF | = 30 ppm/°C (typ.)
- Adjustable output voltage:  $V_{REF} \le V_{OUT} \le 19 \text{ V}$
- Minimum cathode current for regulation: I<sub>kmin</sub> = 0.5 mA (max.)
- Operating temperature:  $Ta = -40 \sim 85$ °C
- Packages: UFV (TA76432AFT),
   PW-MINI (TA76432AF/AFR) and
   TO-92MOD (TA76432AS)
- The TA76432AFT is housed in an ultra-thin UFV package. (thickness: 0.7 mm typ.)

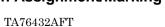


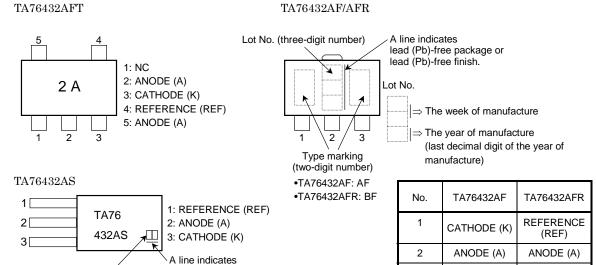
Weight

SSOP5-P-0.65C : 0.007 g (typ.) HSOP3-P-1.50 : 0.05 g (typ.) SSIP3-P-1.27 : 0.36 g (typ.)



## Pin Assignment/Marking





Lot No: The last decimal digit of the year of manufacture followed by the month as letters A to L of the alphabet.

lead (Pb)-free package or

lead (Pb)-free finish.

For example: Jan-2004 is coded as "4A"

Note: TA76432AFvs.TA76432AFR: reverse pin connection.

REFERENCE

(REF)

CATHODE (K)

3

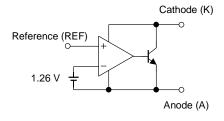
#### **How to Order**

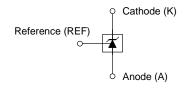
Product No.	Package Type	Packing Type and Capacity	Minimum Order	
TA76432AFT (TE85L)	UFV (surface-mount type)	Embossed tape: 3000/tape	1 tape	
TA76432AF/AFR	PW-MINI (SOT-89)	On cut tape (TE12L): 100/tape section	100	
TA76432AF/AFR (TE12L)	(surface-mount type)	Embossed tape: 1000/tape	1 tape	
TA76432AS	TO-92MOD	Loose in bag: 200/bag	1 bag	
TA76432AS (TPE6)	(lead type)	Radial tape: 2000/tape	1 tape	

Note: The lead pitch for the TA76432AS and TA76432AS (TPE6) may vary.

## **Functional Block Diagram**

#### Circuit Symbol

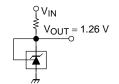


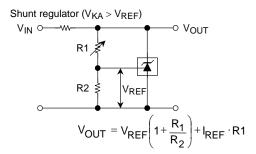




### **Typical Application Circuits**

1.26 V Reference ( $V_{KA} = V_{REF}$ )





## **Precautions during Use**

(1) TA76432AFT, TA76432AF/AFR, TA76432AS

These products contain MOS elements. Please take care to avoid generating static electricity when handling these devices.

(2) TA76432AFT, TA76432AF/AFR, TA76432AS

The oscillation frequency of these devices is determined by the value of the capacitor connected between the anode and the cathode.

When establishing maximum operating condition parameters, please derate the maximum rating values specified in these datasheets so as to allow an operational safety margin.

Use of a laminated ceramic capacitor is recommended.

(3) Precautions when handling anode pin of TA76432AFT

Pin 2 and pin 5 should normally be shorted together. If only pin 5 is used, pin 2 should either be left open or always kept at a lower potential than pin 5. Do not leave pin 5 open and use pin 2 only.

#### Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Cathode voltage		$V_{KA}$	20	V	
Cathode current		Ι <sub>Κ</sub>	20	mA	
Cathode-anode reverse current		-I <sub>K</sub>	10	mA	
Reference voltage		$V_{REF}$	7	٧	
Reference current		I <sub>REF</sub>	50	μΑ	
Reference-anode reverse current		-I <sub>REF</sub>	10	mA	
Power dissipation	TA76432AFT	P <sub>D</sub>	0.45 (Note 1)	W	
	TA76432AF/AFR		0.5		
	TA76432AS		0.8		
Thermal resistance	TA76432AFT		277 (Note 1)	°C/W	
	TA76432AF/AFR	R <sub>th</sub>	250		
	TA76432AS		156		
Operating temperature		T <sub>opr</sub>	-40~85	°C	
Junction temperature		Tj	150	°C	
Storage temperature		T <sub>stg</sub>	<b>−55~150</b>	°C	

Note 1: Glass epoxy board mounting:  $30 \text{ mm} \times 30 \text{ mm} \times 0.8 \text{ mmt}$  (Cu pad area  $35 \text{ mm}^2$ )



#### **Recommended Operating Conditions**

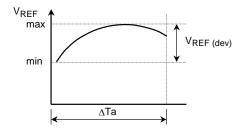
Characteristics	Symbol	Min	Тур.	Max	Unit
Cathode voltage	$V_{KA}$	$V_{REF}$	_	19	V
Cathode current	Ι <sub>Κ</sub>	0.5	_	15	mA
Operating temperature	T <sub>opr</sub>	-40	_	85	°C

# Electrical Characteristics (Unless otherwise specified, Ta = 25°C, $I_K = 5$ mA)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Reference voltage	$V_{REF}$	$V_{KA} = V_{REF}$	1.247	1.26	1.273	V
Deviation of reference input voltage over temperature	V <sub>REF (dev)</sub>	$0^{\circ}\text{C} \le \text{Ta} \le 85^{\circ}\text{C}, V_{KA} = V_{REF}$	_	3	15	mV
Ratio of change in reference input voltage to the change in cathode voltage	ΔV <sub>REF</sub> /ΔV	$V_{REF} \le V_{KA} \le 5 V$	_	0.5	2.5	mV/V
		5 V ≤ V <sub>KA</sub> ≤ 19 V	_	0.3	2.0	
Reference input current	I <sub>REF</sub>	$V_{KA} = V_{REF}$	_	2	4	μА
Deviation of reference input current over temperature	I <sub>REF (dev)</sub>	$\begin{array}{l} 0^{\circ}C \leqq Ta \leqq 85^{\circ}C, \ V_{KA} = V_{REF}, \\ R_{1} = 10 \ k\Omega, \ R_{2} = \infty \end{array}$	_	0.3	1.2	μА
Minimum cathode current for regulation	I <sub>Kmin</sub>	V <sub>KA</sub> = V <sub>REF</sub>	_	0.2	0.5	mA
Off-state cathode current	I <sub>Koff</sub>	V <sub>KA</sub> = 19 V, V <sub>REF</sub> = 0 V	_	_	1.0	μА
Dynamic impedance	Z <sub>KA</sub>	$V_{KA} = V_{REF}, f \le 1 \text{ kHz},$ 0.5 mA $\le I_K \le 15 \text{ mA}$	_	0.2	0.5	Ω

The deviation parameters  $V_{\text{REF (dev)}}$  and  $I_{\text{REF (dev)}}$  are defined as the maximum variation of the  $V_{\text{REF}}$  and  $I_{\text{REF over}}$  the rated temperature range.

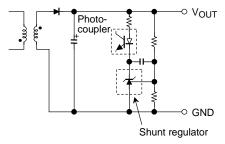
The average temperature coefficient of the  $\ensuremath{V_{\rm REF}}$  is defined as:



$$\left| \alpha V_{REF} \right| = \frac{\left( \frac{V_{REF (dev)} \times 10^6}{V_{REF} @ 25^{\circ}C} \right)}{\Delta Ta} (ppm/^{\circ}C)$$

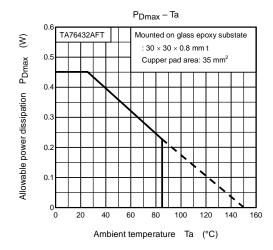
## **Application Circuit Example**

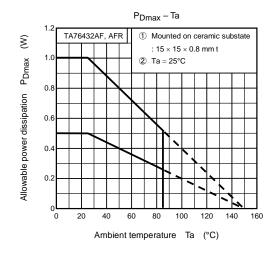
Error amplification circuit for switching power supply

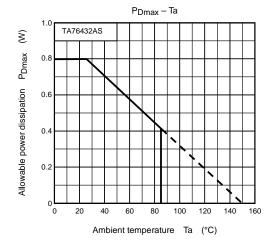


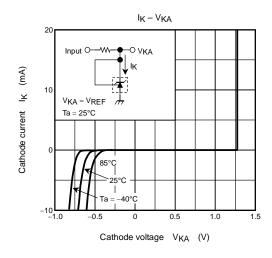
This circuit amplifies the difference between the switching power supply's secondary output voltage and the shunt regulator's reference voltage. It then feeds the amplified voltage back to the primary input voltage via the photocoupler.

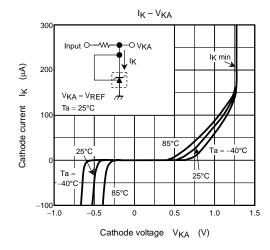
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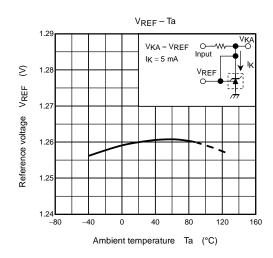


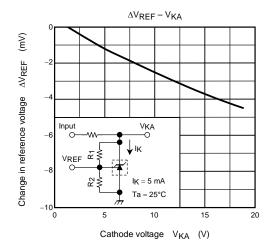


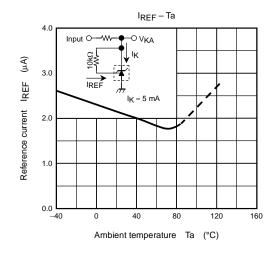


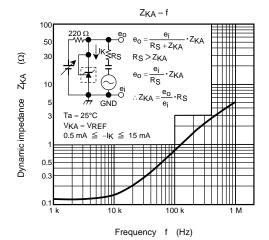


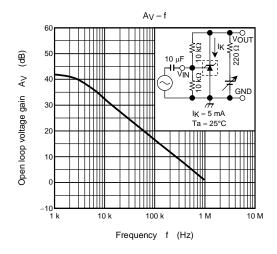


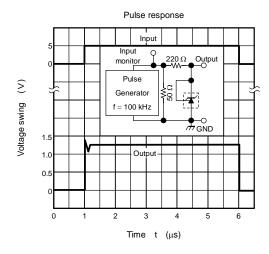


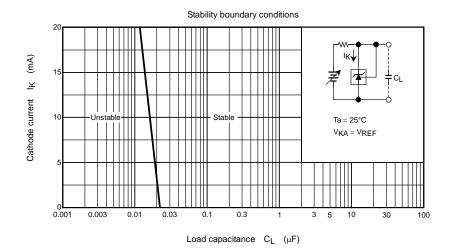








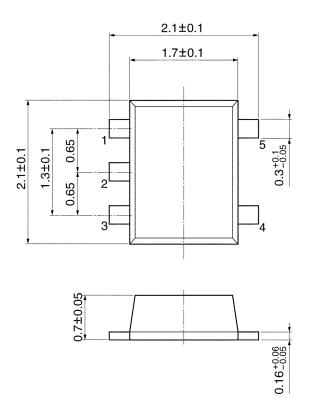




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# **Package Dimensions**

SSOP5-P-0.65C Unit: mm



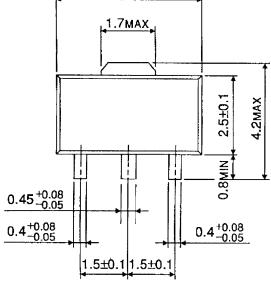
TA76432AFT (UFV)

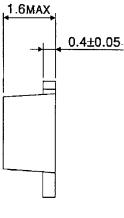
Weight: 0.007 g (typ.)

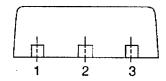
# **Package Dimensions**

HSOP3-P-1.50

Unit: mm **4.6MAX** 1.6MAX 1.7MAX 0.4±0.05







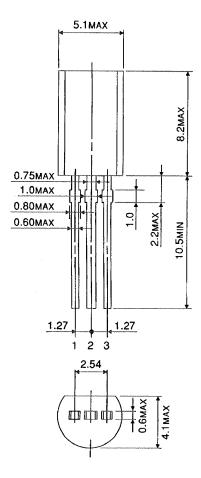
TA76432AF/AFR (PW-MINI)

Weight: 0.05 g (typ.)



# **Package Dimensions**

SSIP3-P-1.27



TA76432AS (TO-92MOD)

Weight: 0.36 g (typ.)

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