TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

# TA2015FNG

Ripple Filter (1.5V USE)

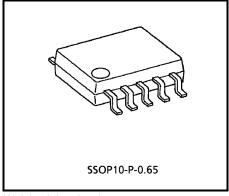
The TA2015FNG is a ripple filter IC, which is developed for low voltage operation (1.5V).

It is especially suitable for supplying voltage for headphone stereo etc.

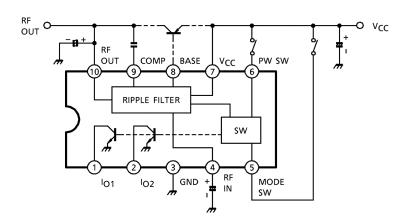
#### Features

- Built-in a power switch
- Excellent ripple rejection ratio: RR = 43dB (typ.)
- Ripple filter output voltage can be controlled by external resistor.
- Output voltage is limited to  $V_{RF} = 1.5V$  (typ.)
- Built-in two constant current sources.
- Excellent low voltage operation.
- Low quiescent supply current (V<sub>CC</sub> = 1.2V, Ta = 25°C)  $I_{CC} = 0.7 mA$  (typ.)
- Operating supply voltage range. (Ta = 25°C)  $V_{CC (opr)} = 0.9 \sim 2.2 V$

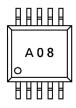
### **Block Diagram**



Weight: 0.04g (typ.)



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**Terminal Explanation** Terminal Voltage: Typical Terminal Voltage with Test Circuit ( $V_{CC}$  = 1.2V, Ta = 25°C, non load)

Terminal		From Aliana		Terminal
No.	Name	Function	Internal Circuit	Voltage (V)
1	I <sub>O1</sub>	Output of constant current source 1 Operating condition PW SW : H MODE SW : L		
2	I <sub>O2</sub>	Output of constant current source 2 Synchronized to PW SW	ſ, ", '	_
3	GND	—	—	0
4	RF in	Ripple filter terminal Ripple filter output voltage can be controlled by external resistor. (See application note)		1.14
5	Mode SW	Mode switch V <sub>CC</sub> : I <sub>O2</sub> , RF OUT on GND / OPEN : I <sub>O1</sub> , I <sub>O2</sub> on	Vcc	
6	PW SW	Power switch V <sub>CC</sub> : Power on GND / OPEN : Power off		_
7	V <sub>CC</sub>	—	—	1.2
8	Base	Base biasing terminal for ripple filter transistor. Output current capacity is 1.2mA with only built–in PNP transistor. This capacity can be increased with an external transistor Q <sub>X</sub> .	RF OUT O + + + + + + + + + + + + + + + + + +	0.5
9	Comp	Phase compensation terminal for a ripple filter circuit		0.5
10	RF out	Ripple filter output	$\Phi$	1.14

### **Application Note**

- 1. Operation mode of constant current source Operation mode is decided by switch condition shown in table.1. Output of constant current source 1 can be used as a reset circuit by changing start up timing of PW SW, MODE SW.
- 2. Ripple filter output

It is necessary to connect an external pull-down resistor with PW SW (pin(6)) and MODE SW (pin(5)) in case that ripple filter circuit doesn't operate normally due to external noise etc.

3. Adjustment of ripple filter output voltage Internal circuit of pin(4) is shown in Fig.1. Ripple filter output voltage is decided by internal resistor R<sub>1</sub>, R<sub>2</sub> and Q<sub>3</sub>, and limited by D<sub>1~2</sub> and Q<sub>3</sub> to V<sub>RF</sub> = 1.5V (typ.) Ripple filter output voltage can be controlled by method below.

- Method to rise the ripple filter output voltage External resistor should be connected between V<sub>CC</sub> and RF IN terminal (pin(4)). In this case, output current capacity of ripple filter circuit is down. Because at ripple filter output stage, collector-emitter voltage of PNP transistor will small, and drive capacity of transistor is down.
- (2) Method to drop the ripple filter output voltage
   External resistor should be connected between GND and RF IN terminal (pin(4)).
   Current flows through external resistor and internal resistor R1, R3 (2.4kΩ, 47kΩ).
   In case that output voltage dropped too much, ripple rejection ratio and other characteristics will be worse, because constant current source of differential amplifier is saturated especially at low voltage.

Mode SW PW SW	Н	L
н	I <sub>O2</sub> RF OUT	I <sub>O1</sub> I <sub>O2</sub>
L	_	—

Table.1 Operation mode

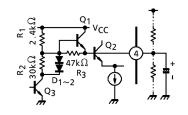


Fig.1 Internal circuit of pin ④

#### Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit	
Supply voltage	V <sub>CC</sub>	4.5	v	
Constant current source output voltage	VS	4.5	v	
Constant current source output current	IS	10		
Ripple filter output current (built–in transistor)	I <sub>RF</sub>	20	mA	
Power dissipation	P <sub>D</sub> (Note)	300	mW	
Operating temperature	T <sub>opr</sub>	-25~75	°C	
Storage temperature	T <sub>stg</sub>	-55~150	C	

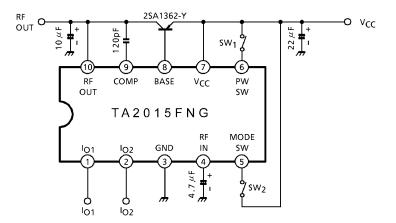
(Note) Derated above Ta =  $25^{\circ}$ C in the proportion of 2.4mW / °C.

#### **Electrical Characteristics**

(unless otherwise specified,  $V_{CC}$  = 1.2V, Ta = 25°C, SW<sub>1</sub>: ON, SW<sub>2</sub>: ON)

Characteristic	Symbol	Test Cir– cuit	Test Condition		Min.	Тур.	Max.	Unit
	I <sub>CC1</sub>	_	PW off, SW <sub>1</sub> : OPEN SW <sub>2</sub> : OPEN		-	0.1	5	μA
Quiescent supply current	I <sub>CC2</sub>	—	SW <sub>2</sub> : Open, I <sub>O1</sub> = I <sub>O2</sub> = 0		—	0.5	0.8	mA
	I <sub>CC3</sub>	_	$I_{RF} = I_{O2} = 0$		_	0.7	1.0	
Ripple filter output voltage	V <sub>RF</sub>	_	V <sub>CC</sub> = 1V, I <sub>RF</sub> = 0		0.91	0.94	_	V
Ripple rejection ratio	RR	_	V <sub>r</sub> = -32dBV f <sub>r</sub> = 100Hz, I <sub>RF</sub> = 30mA		36	43	_	dB
Constant current source	I <sub>O1</sub>	_	SW <sub>2</sub> : OPEN		50	—	_	
output current	I <sub>O2</sub>	_	_		50	_	_	μA
Power switch on current	I <sub>6</sub>	_		V <sub>10</sub> ≥ 0.6V	5	_	-	μA
Power switch off voltage	V <sub>6</sub>	_	V <sub>CC</sub> = 0.9V	V <sub>10</sub> ≤ 0.3V	0	_	0.3	V
Mode switch on current	۱ <sub>5</sub>	_	VCC - 0.9V	V <sub>10</sub> ≥ 0.6V	5	—	_	μA
Mode switch off voltage	V5	_		V <sub>10</sub> ≤ 0.3V	0	_	0.3	V

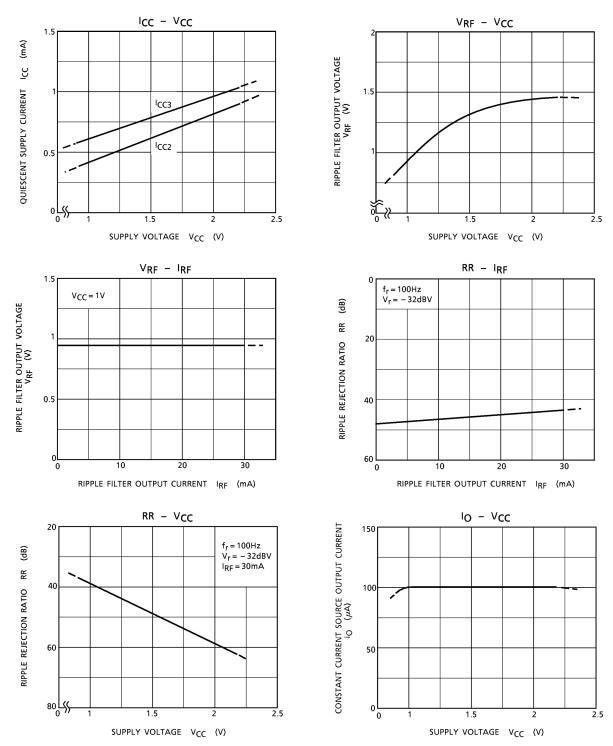
#### **Test Circuit**



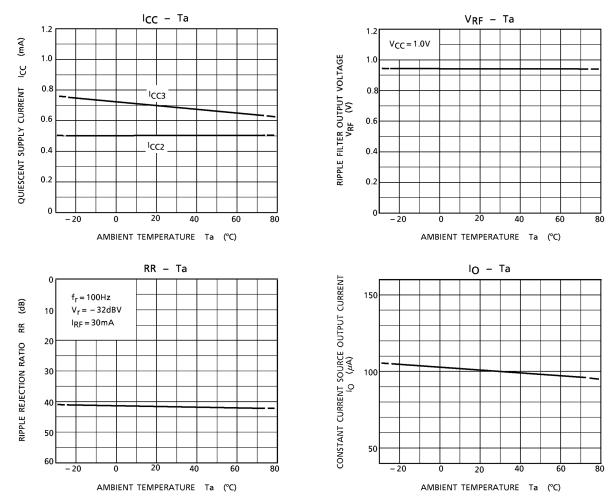
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#### **Characteristic Curves**

(unless otherwise specified, V<sub>CC</sub> = 1.2V,  $I_{RF}$  = 0,  $I_{O1}$  =  $I_{O2}$  = 0, Ta = 25°C)



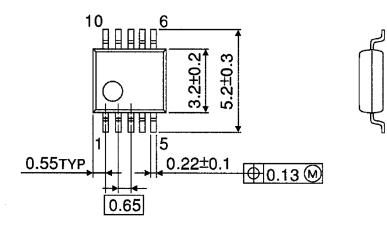
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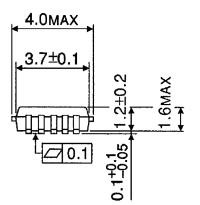


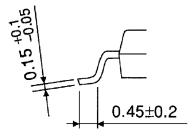
#### **Package Dimensions**

SSOP10-P-0.65

Unit : mm







Weight: 0.04g (typ.)

About solderability, following conditions were confirmed

#### Solderability

(1) Use of Sn-63Pb solder Bath

- solder bath temperature = 230°C
- · dipping time = 5 seconds
- $\cdot$  the number of times = once
- · use of R-type flux
- (2) Use of Sn-3.0Ag-0.5Cu solder Bath
  - solder bath temperature = 245°C
  - dipping time = 5 seconds
  - $\cdot \,$  the number of times = once
  - use of R-type flux

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