TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

# TA2016FNG

#### 1.5V Buffer Amplifier

The TA2016FNG is dual buffer amplifier IC, which is developed for 1.5V headphone stereo.

#### Features

- Output impedance can be changed by mode switch.
  - H mode ..... 230Ω (typ.)
  - MID mode .... 750Ω (typ.)
  - OFF mode .... high impedance
- Built-in two switching transistors, which are synchronized with mode switch.
- Low quiescent current (V<sub>CC</sub> = 1.2V, V<sub>in</sub> = 0, Ta = 25°C) H mode .....  $I_{CCQ1} = 2mA$  (typ.)
  - MID mode .... ICCQ2 = 1mA (typ.) OFF mode .... ICCQ3 = 0.8mA (typ.)
- Operating supply voltage range (Ta = 25°C) VCC (opr) = 0.95~2.2V



Weight: 0.04g (typ.)

#### Marking



### **Block Diagram**



#### **Operation Mode**

Characteristic Mode SW	Impedance [Ω] (Typ.)	SW Tr
Н	230	OFF
MID	750	ON
OFF	High impedance	ON

**Terminal Explanation** Terminal Voltage: Typical Terminal Voltage with Test Circuit at no Signal. ( $V_{CC} = 1.2V$ , Ta = 25°C)

Terminal		Function	Internal Circuit	Terminal	
No.	Name	Function	Internal Circuit	Voltage (V)	
1	EQA	Equalizer switch MID / OFF mode: EQ ON		_	
10	EQB	H mode : EQ OFF			
2	INA	Input of buffer amplifier		0.75	
9	IN <sub>B</sub>				
3	OUTA	Output of buffer amplifier		0.75	
8	8 OUT <sub>B</sub>		-Қ Т		

Terminal		Function	Internal Circuit	Terminal		
No.	Name			Voltage (V)		
4	V <sub>REF</sub>	Reference voltage		0.75		
5	GND	—	—	0		
6	V <sub>CC</sub>			1.2		
7	Mode SW	Mode switch V <sub>CC</sub> : H mode Open : MID mode GND : Off mode	VREEE UNE VREEE UNE VREE UNE VREEE U	_		

#### **Application Note**

#### (1) VREF

 $V_{\rm REF}$  circuit should be stabilized, because this IC operate on  $V_{\rm REF}$  voltage.

(2) Input stage

The resistor between input terminal (pin(2), (9)) and VREF terminal (pin(4)) should be  $33k\Omega$  or less. In case that this resistance is larger than  $33k\Omega$ , this IC doesn't operate normally.

Because, voltage drop at this resistor is increased and constant current source of input stage is saturated. (3) MODE SW

In case of MID mode, care should be taken to operate normally. There is a possibility that this IC doesn't operate normally, because switch condition is open.

#### Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	3	V
Power dissipation	P <sub>D</sub> (Note)	300	mW
Equalizer current	I <sub>EQ</sub>	50	mA
Operating temperature	T <sub>opr</sub>	-10~60	°C
Storage temperature	T <sub>stg</sub>	-55~150	C

(Note) Derated above Ta =  $25^{\circ}$ C in proportion of 2.4mW / °C.

Electrical Characteristics Unless Otherwise Specified: V<sub>CC</sub> = 1.2V, f = 1kHz, R<sub>L</sub> = 4.7k $\Omega$ , Ta = 25°C

Characteristic	Symbol	Test Cir– cuit	Mode SW	Test Condition	Min.	Тур.	Max.	Unit
Quiescent supply current	I <sub>CCQ1</sub>		н	V <sub>in</sub> = 0	_	2	3	mA
	I <sub>CCQ2</sub>		MID		_	1	1.6	
	I <sub>CCQ3</sub>		OFF		_	0.8	1.4	
Voltage gain	G <sub>V1</sub>		Н	V <sub>in</sub> = -32dBV	-2.4	-0.4	1.6	dB
	G <sub>V2</sub>		MID		-3.6	-1.6	0.4	
Maximum output voltage	V <sub>om1</sub>		н	THD = 1%	70	120	_	mV <sub>rms</sub>
	V <sub>om2</sub>		MID		60	110	_	
Total harmonic distortion	THD1	-	н	V <sub>o</sub> = 50mV <sub>rms</sub>	_	0.1	0.6	%
	THD2		MID		_	0.2	0.8	
	V <sub>no1</sub>		н	R <sub>g</sub> = 600Ω	_	5	10	μV <sub>rms</sub>
Output noise voltage	V <sub>no2</sub>		MID		_	6	12	
Cross talk	CT1		н	V <sub>in</sub> = -32dBV	—	76	—	dB
	CT2		MID		_	74	—	
H mode on current	I <sub>7</sub>	_	MID→H	I <sub>EQ</sub> ≤ 300µA	5	_	—	μA
Off mode voltage	V <sub>7</sub>	_	MID→OFF	V <sub>in</sub> = –32dBV ATT ≥ 45dB	0	_	0.12	V

#### **Test Circuit**



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#### **Characteristics Curves**

Unless Otherwise Specified:  $V_{CC}$  = 1.2V, f = 1kHz, R<sub>L</sub> = 4.7k $\Omega$ , Ta = 25°C



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65



### Package Dimensions

SSOP10-P-0.65

Unit : mm







Weight: 0.04g (typ.)

About solderability, following conditions were confirmed

#### Solderability

(1) Use of Sn-63Pb solder Bath

- solder bath temperature = 230°C
- · dipping time = 5 seconds
- $\cdot$  the number of times = once
- · use of R-type flux
- (2) Use of Sn-3.0Ag-0.5Cu solder Bath
  - solder bath temperature = 245°C
  - dipping time = 5 seconds
  - $\cdot \,$  the number of times = once
  - use of R-type flux

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