TOSHIBA INSULATED GATE BIPOLAR TRANSISTOR SILICON N - CHANNEL MOS TYPE

# GT80J101

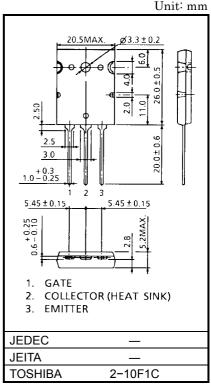
### HIGH POWER SWITCHING APPLICATIONS

- High Input Impedance
  - High Speed  $: t_f = 0.40 \mu s (Max.)$
- Low Saturation Voltage : VCE (sat) = 3.5V (Max.)
- Enhancement–Mode

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#### MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC		SYMBOL	RATING	UNIT	
Collector-Emitter Voltage		V <sub>CES</sub>	600	V	
Gate-Emitter Voltage		V <sub>GES</sub>	±20	V	
Collector Current	DC	ΙC	80	A	
	1ms	I <sub>CP</sub>	160		
Collector Power Dissipation (Tc = 25°C)		P <sub>C</sub>	200	W	
Junction Temperature		Tj	150	°C	
Storage Temperature Range		T <sub>stg</sub>	-55~150	°C	
Screw Torque		—	0.8	N∙m	



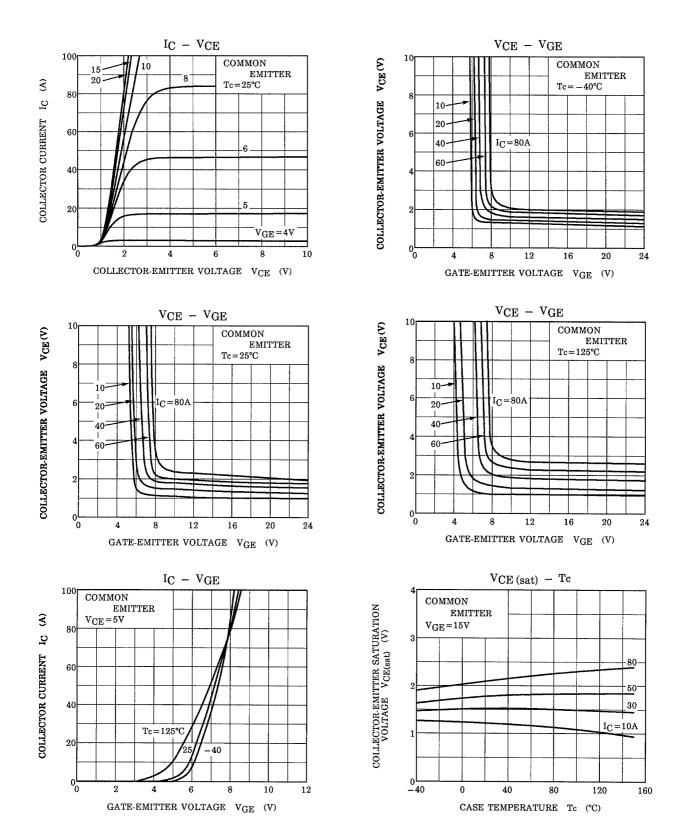
Weight: 9.75g

### ELECTRICAL CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC		SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT	
Gate Leakage Current		I <sub>GES</sub>	$V_{GE} = \pm 20V, V_{CE} = 0$	_	_	±500	nA	
Collector Cut-Off C	urrent	ICES	V <sub>CE</sub> = 600V, V <sub>GE</sub> = 0	—	—	1.0	mA	
Gate-Emitter Cut-off Voltage		V <sub>GE (OFF)</sub>	I <sub>C</sub> = 80mA, V <sub>CE</sub> = 5V	3.0	—	6.0	V	
Collector-Emitter Saturation Voltage		V <sub>CE (sat) (1)</sub>	I <sub>C</sub> = 10A, V <sub>GE</sub> = 15V	_	_	2.0	V	
		V <sub>CE (sat) (2)</sub>	I <sub>C</sub> = 80A, V <sub>GE</sub> = 15V	_	2.5	3.5	v	
Input Capacitance		Cies	V <sub>CE</sub> = 10V, V <sub>GE</sub> = 0, f = 1MHz	_	5500	—	pF	
Switching Time	Rise Time	tr	$15V_{0} V_{IN_{0}} 33\Omega_{0} $	_	0.3	0.6	- µs	
	Turn-on Time	t <sub>on</sub>		_	0.5	0.8		
	Fall Time	t <sub>f</sub>		_	0.25	0.40		
	Turn-off Time	t <sub>off</sub>		_	0.7	1.0		
Thermal Resistance		R <sub>th (j−c)</sub>	—	_	—	0.625	°C/W	

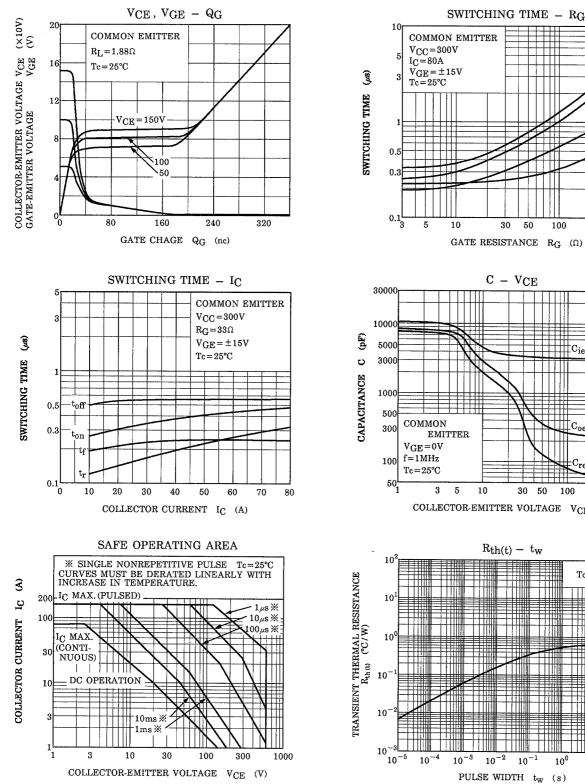
Unit: mm

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toff



ton ŧf 50 100 300 500 GATE RESISTANCE  $R_{G}$  ( $\Omega$ )  $C - V_{CE}$ TTTC ies Cno 30 50 100 300 500 COLLECTOR-EMITTER VOLTAGE VCE (V)  $R_{th(t)} - t_w$  $Tc = 25^{\circ}C$ 

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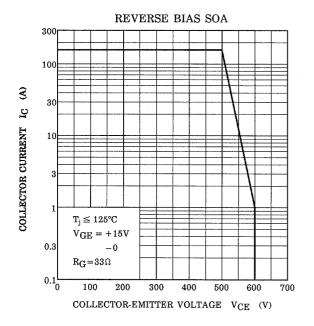
10<sup>0</sup>

10<sup>1</sup>

 $10^2$ 

2001-06-07

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