TOSHIBA Photocoupler GaAłAs Ired & Photo-IC

TLP251

Inverter For Air Conditionor Induction Heating Transistor Inverter Power MOS FET Gate Drive IGBT Gate Drive

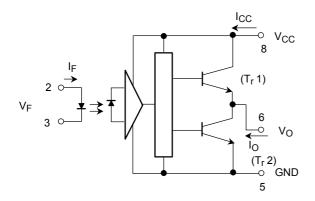
The TOSHIBA TLP251 consists of a GaAtAs light emitting diode and a integrated photodetector.

This unit is 8-lead DIP package.

TLP251 is suitable for gate driving circuit of IGBT or power MOS FET. Especially TLP251 is capable of "direct" gate drive of lower power IGBTs. (~15A)

- Input threshold current: IF=5mA(max.)
- Supply current (ICC): 11mA(max.)
- Supply voltage (V_{CC}): 10–35V
- Output current (I_O): ±0.4A(max.)
- Switching time (t_{pLH} / t_{pHL}): 1µs(max.)
- Isolation voltage: 2500Vrms(min.)
- UL recognized: UL1577, file no.E67349

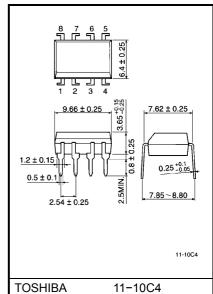
Schematic



A 0.1μ F bypass capcitor must be connected between pin 8 and 5(see Note 5).

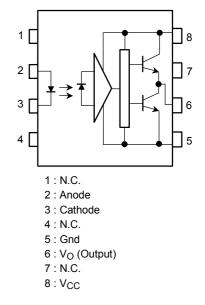
Truth Table

		Tr1	Tr2		
Input	On	On	Off		
LED	Off	Off	On		



Weight: 0.54g

Pin Configuration (top view)



Unit in mm

Maximum Ratings (Ta = 25°C)

	Characteristic	Symbol Rating		Unit	
	Forward current	١ _F	20	mA	
	Forward current derating (Ta ≥ 70°C)		ΔI _F / ΔTa	- 0.36	mA / °C
LED	Peak transient forward current	(Note 1)	IFPT	1	A
	Reverse voltage		V _R	5	V
	Junction temperature		Тj	125	°C
	"H" peak output current (P _W ≤ 2.0µs, f ≤ 15kHz)	(Note 2)	Іорн	- 0.4	A
	"L" peak output current (P _W ≤ 2.0µs, f ≤ 15kHz)	(Note 2)	I _{OPL}	0.4	A
Detector	Output voltage	(Ta ≤ 70°C) (Ta = 85°C)	Vo	35 24	v
De	Supply voltage	(Ta ≤ 70°C) (Ta = 85°C)	V _{CC}	35 24	V
	Output voltage derating (Ta ≥ 70°C)		ΔV _O / ΔTa	- 0.73	V / °C
	Supply voltage derating (Ta ≥ 70°C)		ΔV _{CC} / ΔTa	- 0.73	V / °C
	Junction temperature	Tj	125	°C	
Oper	ating frequency	f	25	kHz	
Oper	ating temperature range	T _{opr}	-20~85	°C	
Stora	ige temperature range	T _{stg}	-55~125	°C	
Lead	soldering temperature(10s)	T _{sol}	260	°C	
	Isolation voltage (AC, 1min., R.H.≤ 60%) (Note 4)			2500	Vrms

(Note 1) Pulse width $P_W \le 1\mu s$, 300pps

(Note 2) Expornential waveform

- (Note 3) Exportential waveform, $I_{OPH} \le -0.25A (\le 2.0\mu s)$, $I_{OPL} \le +0.25A (\le 2.0\mu s)$
- (Note 4) Device considerd a two terminal device: Pins 1, 2, 3 and 4 shorted together, and pins 5, 6,7 and 8 shorted together.
- (Note 5) A ceramic capacitor(0.1µF)should be connected from pin 8 to pin 5 to stabilize the operation of the high gain linear ampifier. Failure to provide the bypassing may impair the swiching property. The total lead length between capacitor and coupler should not exceed 1cm.

Recommended Operating Conditions

Characteristic	Symbol Min. Typ. Max.		ax.	Unit		
Input current, on	I _{F(ON)}	7	8	10		mA
Input voltage, off	V _{F(OFF)}	0	_	0.8		V
Supply voltage	V _{CC}	10	_	30	20	V
Peak output current	I _{OPH} / I _{OPL}	_		±0.1		А
Operating temperature	T _{opr}	-20	25	70	85	°C

Electrical Characteristics (Ta = -20~70°C, unless otherwise specified)

Characteristic		Symbol	Test Cir– cuit	Test Condition		Min.	Тур.*	Max.	Unit
Input forward voltage		V _F		I _F = 10 mA , Ta = 25°C		_	1.6	1.8	V
Temperature coefficient of forward voltage		ΔV _F / ΔTa		I _F = 10 mA		_	-2.0	_	mV / °C
Input reverse current		I _R	_	V _R = 5V, Ta = 25°C		_	_	10	μA
Input capacitance		CT	_	V = 0 , f = 1MHz , Ta = 25°C		_	45	250	pF
Output current	"H" level	I _{ОРН}	3	V _{CC} =30V	I _F = 10mA V ₈₋₆ = 4V	-0.1	-0.25	_	A
	"L" level	I _{OPL}	2	(*1)	I _F =0 V ₆₋₅ = 2.5V	0.1	0.2	_	
Output voltage	"H" level	V _{OH}	4	$ \begin{array}{l} V_{CC1} = +15V, V_{EE1} = -15V \\ R_L = 200\Omega, I_F = 5mA \\ \end{array} \\ V_{CC1} = +15V, V_{EE1} = -15V \\ R_L = 200\Omega, V_F = 0.8V \end{array} $		11	13.2	_	v
	"L" level	V _{OL}	5			_	-14.5	-12.5	
	"H" level	I _{ССН}	_	V _{CC} = 30V Ta = 25°C	/, I _F = 10mA		7.5		
Supply ourrest				V _{CC} = 30V, I _F = 10mA				11	mA
Supply current	"L" level	ICCL	_	V _{CC} = 30V, I _F = 0mA Ta = 25°C		_	8	_	
				V _{CC} = 30V, I _F = 0mA		_	_	11	
Threshould input current	"Output $L \rightarrow H$ "	I _{FLH}		V_{CC1} = +15V, V_{EE1} = -15V R _L = 200Ω, V_O > 0V			1.2	5	mA
Threshold input voltage	"Output $H \rightarrow L$ "	V _{FLH}	_	V_{CC1} = +15V, V_{EE1} = -15V R _L = 200Ω, V_O < 0V		0.8	—	_	V
Supply voltage		V _{CC}	_			10	_	35	V
Capacitance (input–output)		Cs	_	Vs = 0 , f = 1MHz Ta = 25℃		_	1.0	2.0	pF
Resistance (input–output)		Rs		Vs = 500V, Ta = 25°C R.H. ≤ 60%		1×10 ¹²	10 ¹⁴	_	Ω

* All typical values are at Ta=25°C (*1): Duration of I_O time \leq 50µs

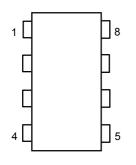
Switching Characteristics (Ta = $-20 \sim 70^{\circ}$ C, unless otherwise specified)

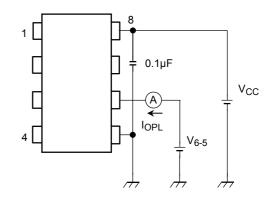
Characteristic		Symbol	Test Cir– cuit	Test Condition	Min.	Тур.*	Max.	Unit
Propagation	L→H	t _{pLH}		I _F = 8mA V _{CC1} = +15V, V _{EE1} = -15V	-	0.25	1.0	μs
delay time	H→L	t _{pHL}	6		_	0.25	1.0	
Output rise time		tr		$R_{L} = 200 \Omega$	_	_	-	μο
Output fall time		t _f			_	_	_	
Common mode transient immunity at high level output		C _{MH}	7	V _{CM} = 600V, I _F = 8mA, V _{CC} = 30V, Ta = 25°C	-5000	_	_	V / µs
Common mode transient immunity at low level output		C _{ML}	7	V _{CM} = 600V, I _F = 0mA, V _{CC} = 30V, Ta = 25°C	5000	_	_	V / µs

*All typical values are at Ta=25°C

Test Circuit 1:

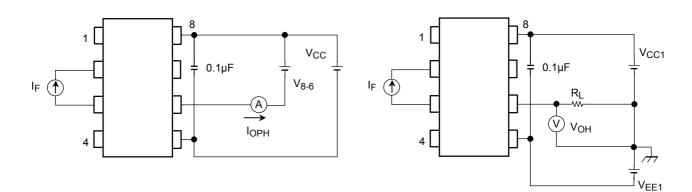
Test Circuit 2: IOPL



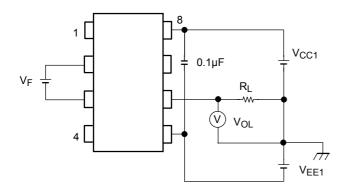


Test Circuit 3: I_{OPH}

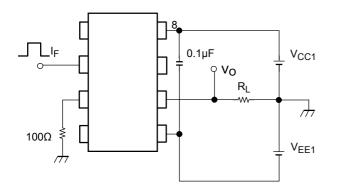
Test Circuit 4: V_{OH}

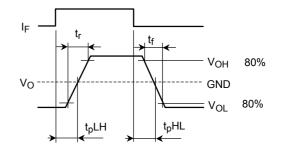


Test Circuit 5: VOL

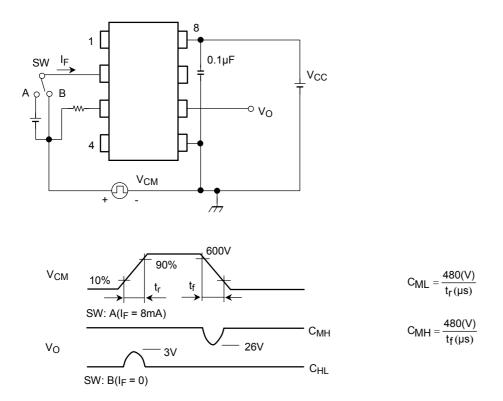


Test Circuit 6: t_{pLH}, t_{pHL}, t_r, t_f

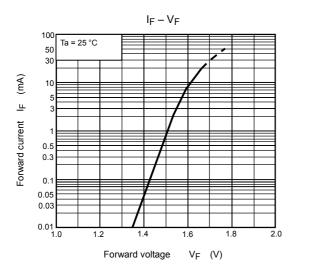


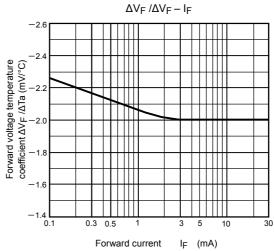


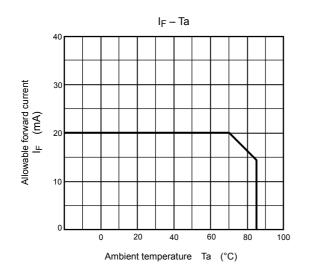
Test Circuit 7: C_{MH,} C_{ML}

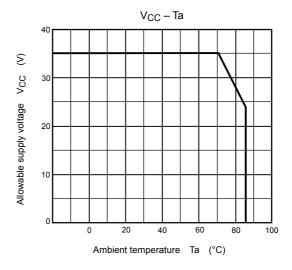


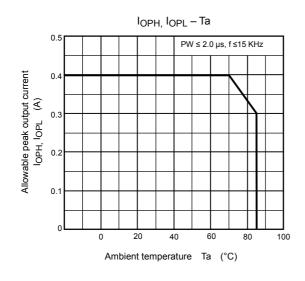
 $C_{ML}\left(C_{MH}\right) \text{ is the maximum rate of rise (fall) of the common mode voltage that can be sustained with the output voltage in the low (high) state.}$











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