TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

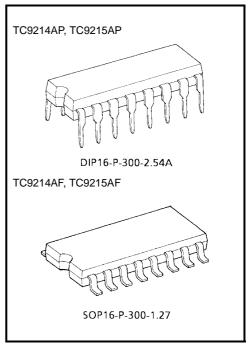
TC9214AP,TC9214AF,TC9215AP,TC9215AF

High Voltage Analog Switch

TC9214AP/AF, TC9215AP/AF are analog switch for high voltage audio application.

Features

- Analog switch circuit formation TC9214AP, TC9214AF: 5 circuits TC9215AP, TC9215AF: 6 circuits
- Dual power supply of (+) and (-) can be used.
- Including level shift circuit, this IC can be operated by (+) power supply only under dual power supply operating.
- Setting low input-threshold-voltage in control signal input terminal. 5 V CPU application can control this IC directly.
- Package: DIP-16 pin SOP-16 pin

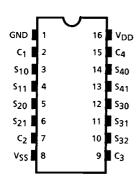


Weight

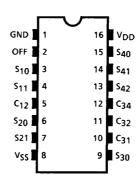
DIP16-P-300-2.54A: 1.0 g (typ.) SOP16-P-300-1.27: 0.16 g (typ.)

Pin Assignment (top view)

TC9214AP, TC9214AF

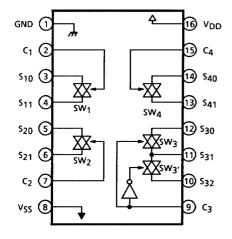


TC9215AP, TC9215AF

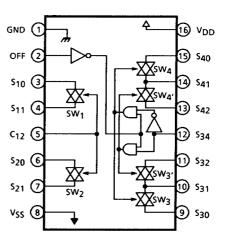


Block Diagram

TC9214AP, TC9214AF



TC9215AP, TC9215AF



Pin Function

1. TC9214AP, TC9214AF

Pin No.	Symbol	Pin Name	Function	Note	
1	GND	Ground Terminal	Dual power supplying: +B → V _{DD}		
8	V_{SS}	(-) Power Supply Terminal	$ \begin{array}{c} 0 \text{ V} \rightarrow \text{GND} \\ -B \rightarrow \text{V}_{SS} \end{array} $	_	
16	V_{DD}	(+) Power Supply Terminal	Single power supplying: $+B \rightarrow V_{DD}$ 0 V \rightarrow GND, V_{SS}		
2	C ₁	Switch (1) Control Terminal	SWITCH CONNECTION		
3	S ₁₀	Switch (1) Input/Output	S ₁₀ S _{W1} S ₁₁		
4	S ₁₁	Terminal	s ₂₀		
5	S ₂₀	Switch (2) Input/Output	S40 S41		
6	S ₂₁	Terminal	s ₃₁		
7	C ₂	Switch (2) Control Terminal	c ₃ Sw ₃ , S ₃₂		
9	C ₃	Switch (3) Control Terminal	TRUTH TABLE	_	
10	S ₃₂		C ₁ , C ₂ , C ₄ SW ₁ , SW ₂ , SW ₃		
11	S ₃₁	Switch (3) Input/Output Terminal	H ON		
12	S ₃₀		L OFF		
13	S ₄₁	Switch (4)	C ₃ S ₃₀ -S ₃₁ S ₃₁ -S ₃₂		
14	S ₄₀	Input/Output Terminal	H ON OFF		
15	C ₄	Switch (4) Control Terminal	L OFF ON		



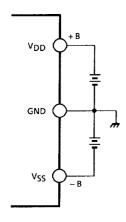
2. TC9215AP, TC9215AF

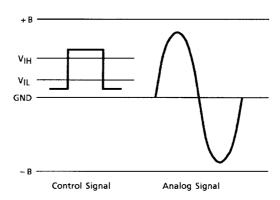
Pin No.	Symbol	Pin Name	Function					Note
1	GND	Ground Terminal	Dual	Dual power supplying: +B → V _{DD}				
8	V _{SS}	(-) Power Supply Terminal	$ \begin{array}{c} 0 \text{ V} \to \text{GND} \\ -B \to \text{V}_{\text{SS}} \end{array} $				_	
16	V_{DD}	(+) Power Supply Terminal	Sing	le powe	er supply			
2	OFF	Switch (3), (4) OFF Input Terminal	SWI	тсн сс	ONNECT			
3	S ₁₀	Switch (1) Input/Output		S ₁ S ₁	₀O— ₁O—	SW ₁ SW ₂		
4	S ₁₁	Terminal			20-			
5	C ₁₂	Switch (1), (2) Control Terminal		\$ ₃	₀⊖⊸ ₂⊖⊸ੌ			
6	S ₂₀	Switch (2) Input/Output		\$3	1 0			
7	S ₂₁	Terminal		C3	4O			
9	S ₃₀		TRU	ITH TAE	BLE	_		
10	S ₃₁	Switch (3) Input/Output Terminal		С	12	SW ₁ , SW	<i>I</i> ₂	
11	S ₃₂			ŀ	1	ON		
12	C ₃₄	Switch (3), (4) Control Terminal		L		OFF		
13	S ₄₂	Switch (4) Input/Output Terminal		OFF	C ₃₄	S ₃₀ -S ₃₁ S ₄₀ -S ₄₁	S ₃₁ -S ₃₂ S ₄₁ -S ₄₂	
14	S ₄₁			L	L	ON	OFF	
	S ₄₀				Н	OFF	ON	
15				H (Note 1)		OFF OFF		
			Note 1: H or L					

Notation: Power Supply

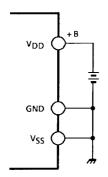
As the power supply is parted between analog switch unit and control unit, the analog switch unit operates in dual power supply of (+) and (-), in which case, the control unit operates in single power supply. Setting a low input-threshold voltage in control input terminal, 5 V CPU application can control this IC directly.

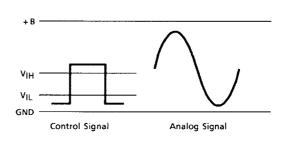
Dual Power Supply Use





Single Power Supply Use





Note 2: In case of using single power supply in common with V_{SS} and GND terminal, half voltage of dual power supply must be supplied because of low operating voltage of a control circuit. ($V_{DD} - GND \le 18 \text{ V}$)



Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Power supply voltage (1)	V_{DD} - V_{SS}	-0.3~36	V
Power supply voltage (2)	V _{DD} -GND	-0.3~20	V
GND input voltage	V _{IN (1)}	-0.3~V _{DD} + 0.3	V
V _{SS} input voltage	V _{IN (2)}	$V_{SS} - 0.3 \sim V_{DD} + 0.3$	V
Power dissipation	PD	600 (300)	mW
Operating temperature	T _{opr}	-40~85	°C
Storage temperature	T _{stg}	-65~150	°C

^{():} SOP-16 pin.

Electrical Characteristics (unless otherwise specified, $V_{DD}=15~V,~V_{SS}=-15~V,~GND=0~V,~Ta=25^{\circ}C$)

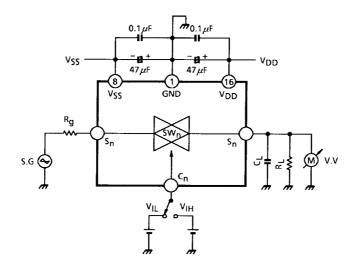
Characteristics		Symbol	Test Circuit	Test Condition		Min	Тур.	Max	Unit
Operating supply voltage (1)		V _{DD} -V _{SS}		Dual power supplying		9.0	~	34	V
Operating supply voltage (2)		V _{DD} -GND		Single power supplying		4.5	~	18	V
Operating supply current		I _{DD}	_	No load, No signal		_	0.1	0.5	mA
Input voltage	"H" level	V _{IH}		Control input to	erminal	4.0	~	V_{DD}	V
	"L" level	V _{IL}		V _{DD} = 4.5~18 V		GND	~	1.0	V
Input current	"H" level	I _{IH}		Control input	V _{IH} = 15 V	-0.1	~	0.1	μА
	"L" level	I _{IL}			V _{IL} = 0 V	-0.1	~	0.1	
Analog switch ON resistance		R _{ON}	_	$V_{DD} = 5.0 \text{ V}, V_{SS} = -5.0 \text{ V}$		_	200	300	Ω
				$V_{DD} = 9.0 \text{ V}, V_{SS} = -9.0 \text{ V}$		_	80	100	
				V _{DD} = 15 V, V _{SS} = -15 V		_	60	80	
Analog switch OFF leak		I _{OFF}	_	$V_{IN} = V_{DD} \sim V_{SS}$		_	±0.1	±100	nA
Total harmonic distortion		THD		$f_{IN} = 1 \text{ kHz}, V_{IN} = 1 \text{ V}_{rms}$ $R_g = 600 \Omega, R_L = 10 \text{ k}\Omega$ $BW = 20 \text{ Hz} \sim 20 \text{ kHz}$		_	0.01	0.05	%
Cross talk		C _T				80	90	_	dB
Output noise voltage		V _N				_	2.0	_	μV_{rms}
Maximum control frequency			1	V _{IL} = 0 V, V _{IH} = 5 V		50	100	_	kHz
Maximum transfer frequency		f _{max}	'	$R_L = 10 \text{ k}\Omega, C_L = 15 \text{ pF}$ (Note 3)		_	5	_	MHz
Field through		F _S		$R_L = 10 \text{ k}\Omega, C_L = 15 \text{ pF}$ (Note 4		_	300	_	kHz

Note 3: To supply the $V_{IN}=1.0\ V_{rms}$ sign wave. f_{max} means 3dB down frequency from $f_{IN}=1\ kHz$.

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Note 4: To supply the $V_{IN} = 1.0 \ V_{rms}$ sign wave. F_S means frequency for cross-talk 50dB.

Test Circuit 1

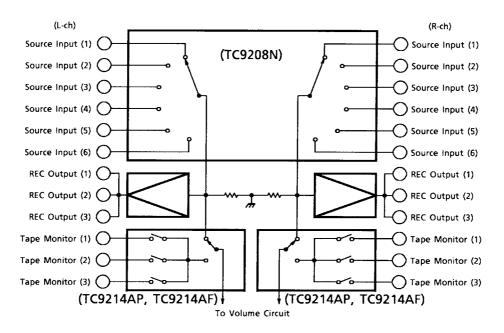


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Application Circuit

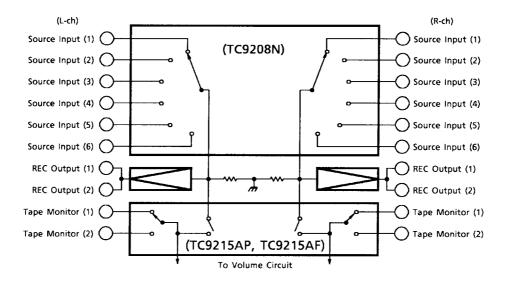
1. TC9208N + TC9214AP, TC9214AF × 2

• Monitor switching for 6 source input circuits and 3 tape-recorder.



2. TC9208N + TC9215AP, TC9215AF

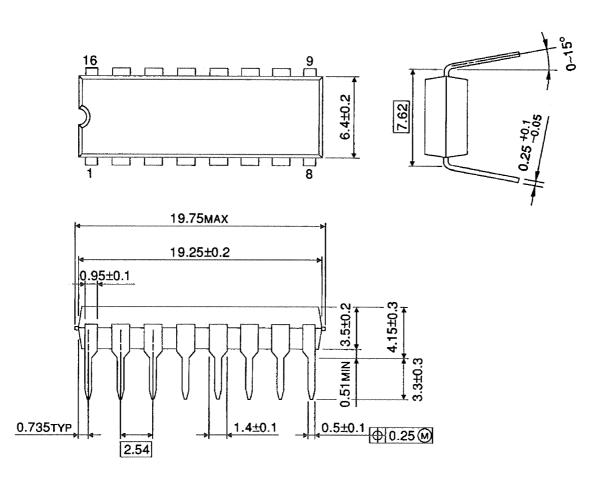
• Monitor switching for 6 source input circuits and 2 tape-recoder.





Package Dimensions

DIP16-P-300-2.54A Unit: mm



8

Weight: 1.0 g (typ.)

0.8±0.2

Package Dimensions

SOP16-P-300-1.27

Unit: mm

16
9
16
18
18
10.8MAX
10.3±0.2

9

Weight: 0.16 g (typ.)

ZZ 0.1

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Handbook" etc..

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