TOSHIBA CMOS Linear Integrated Circuit Silicon Monolithic

# TC75S57F,TC75S57FU,TC75S57FE

#### Single Comparator

The TC75S57F/TC75S57FU/TC75S57FE is a CMOS general-purpose single comparator. The device can operate off a single power supply and draws a lower supply current than a conventional bipolar general-purpose comparator. This device's push-pull output stage can be directly connected to TTL or CMOS logic ICs, among others.

#### **Features**

• Low-current power supply :  $IDD = 100 \mu A \text{ (typ.)}$ 

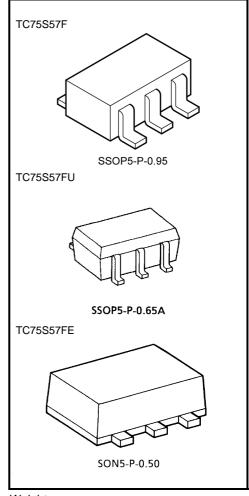
• Single power supply operation

• Wide common mode input voltage range: VSS~VDD - 0.9 V

Push-pull output circuit

Low input bias current

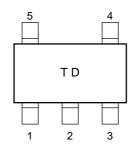
Small package



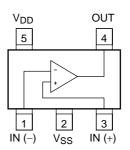
Weight

SSOP5-P-0.95 : 0.014 g (typ.) SSOP5-P-0.65A : 0.006 g (typ.) SON5-P-0.50 : 0.003 g (typ.)

#### Marking (top view)



#### Pin Connection (top view)



## **Maximum Ratings (Ta = 25°C)**

Characteristics		Symbol	Rating	Unit	
Supply voltage		$V_{DD}$ , $V_{SS}$	±3.5 or 7	V	
Differential input voltage		DV <sub>IN</sub>	±7	V	
Input voltage		V <sub>IN</sub>	V <sub>SS</sub> ~V <sub>DD</sub>	٧	
Output Current		lout	±35	mA	
Power dissipation	TC75S57F/FU	P <sub>D</sub>	200	mW	
	TC75S57FE	PD	100	IIIVV	
Operating tempera	ture	T <sub>opr</sub>	−40~85 °C		
Storage temperature		T <sub>stg</sub>	-55~125	°C	

Note: This device's CMOS structure makes it prone to latch-up. To prevent latch-up, please take the following precautions:

- Ensure that no I/O pin's voltage level ever exceeds V<sub>DD</sub> or drops below V<sub>SS</sub>.
  In addition, check the power-on timing.
- Do not subject the device to excessive noise.

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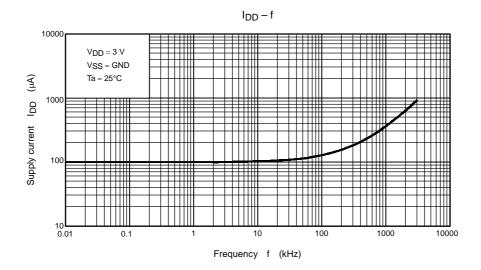
## Electrical Characteristics (unless otherwise specified, $V_{DD} = 5 \text{ V}$ , $V_{SS} = GND$ , $Ta = 25^{\circ}C$ )

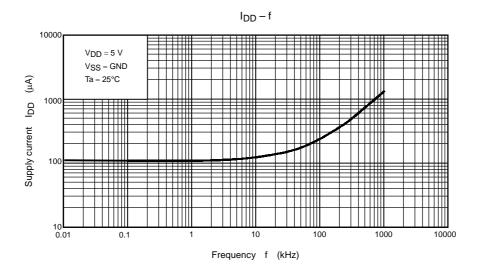
Characteristics	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Input offset voltage	V <sub>IO</sub>	_	_	_	±1	±7	mV
Input offset current	I <sub>IO</sub>	_	_	_	1	_	pА
Input bias current	lį	_	_	_	1	_	pА
Common mode input voltage	CMV <sub>IN</sub>	_	_	0	_	4.1	V
Supply current	I <sub>DD</sub> (Note)	_	_	_	110	220	μА
Voltage gain	G <sub>V</sub>	_	_	_	94	_	dB
Sink current	I <sub>sink</sub>	_	V <sub>OL</sub> = 0.5 V	13	25	_	mA
Source current	I <sub>source</sub>	_	V <sub>OH</sub> = 4.5 V	9	21	_	mA
Output voltage	V <sub>OL</sub>	_	I <sub>sink</sub> = 5.0 mA	_	0.1	0.3	· V
Output voltage	V <sub>OH</sub>	_	I <sub>source</sub> = 5.0 mA	4.7	4.9	_	
Operating supply voltage	$V_{DD}$	_	_	1.8	_	7.0	V
Dranagation dalay time (turn on)	t <sub>PLH</sub> (1)	_	Over drive = 100 mV	_	140	_	ns
Propagation delay time (turn on)	t <sub>PLH</sub> (2)	_	TTL step input	_	90	_	
Propagation delay time (turn off)	t <sub>PHL</sub> (1)	_	Over drive = 100 mV	_	90	_	ns
	t <sub>PHL</sub> (2)	_	TTL step input	_	70	_	
Decrease time	t <sub>TLH</sub>	_	Over drive = 100 mV	_	11	_	- ns
Response time	t <sub>THL</sub>		Over drive = 100 mV	_	7	_	

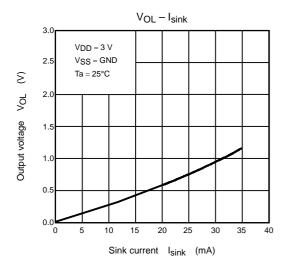
#### Electrical Characteristics (unless otherwise specified, V<sub>DD</sub> = 3 V, V<sub>SS</sub> = GND, Ta = 25°C)

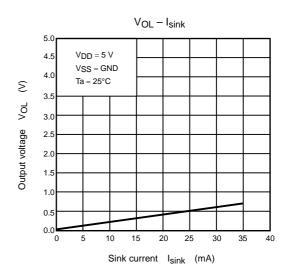
Characteristics	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Input offset voltage	V <sub>IO</sub>	_	_	_	±1	±7	mV
Input offset current	I <sub>IO</sub>	_	_	_	1	_	pА
Input bias current	I <sub>I</sub>	_	_	_	1	_	pА
Common mode input voltage	CMV <sub>IN</sub>	_	_	0	_	2.1	V
Supply current	I <sub>DD</sub> (Note)	_	_	_	100	200	μΑ
Sink current	I <sub>sink</sub>	_	V <sub>OL</sub> = 0.5 V	6	18	_	mA
Source current	I <sub>source</sub>	_	V <sub>OH</sub> = 2.5 V	3	15	_	mA
Output voltage	V <sub>OL</sub>	_	I <sub>sink</sub> = 5.0 mA	_	0.15	0.35	V
	V <sub>OH</sub>	_	I <sub>source</sub> = 5.0 mA	2.65	2.85	_	
Propagation delay time (turn on)	t <sub>PLH</sub>	_	Over drive = 100 mV	_	110	_	ns
Propagation delay time (turn off)	t <sub>PHL</sub>	_	Over drive = 100 mV	_	90	_	ns
Response time	t <sub>TLH</sub>	_	Over drive = 100 mV	_	7	_	no
	t <sub>THL</sub>	_	Over drive = 100 mV	_	8	_	ns

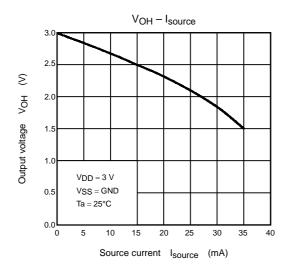
Note: This device's current consumption increases as its operating frequency increases. Note that the power dissipation should not exceed the allowable power dissipation.

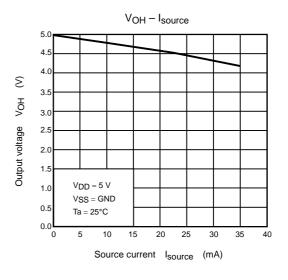


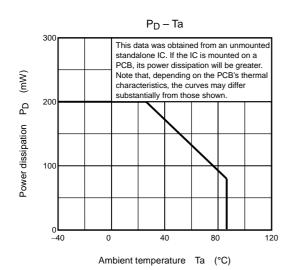










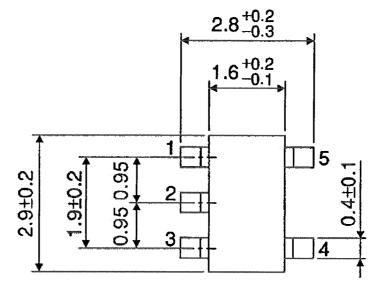


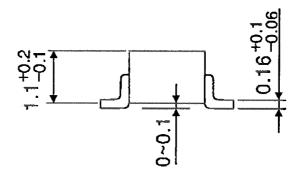
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## **Package Dimensions**

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SSOP5-P-0.95 Unit: mm





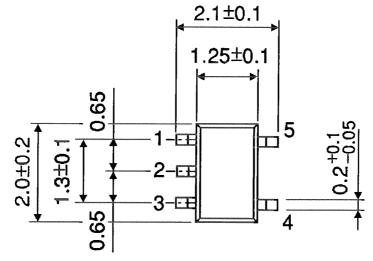
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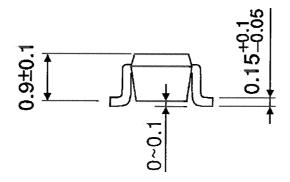
Weight: 0.014 g (typ.)

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## **Package Dimensions**

SSOP5-P-0.65A Unit: mm





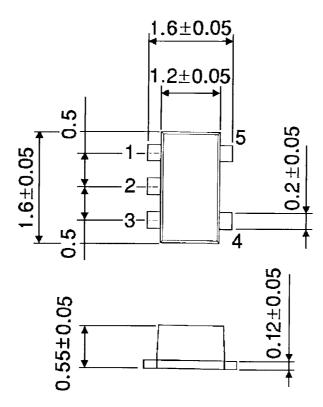
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Weight: 0.006 g (typ.)



## **Package Dimensions**

SON5-P-0.50 Unit: mm



Weight: 0.003 g (typ.)

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